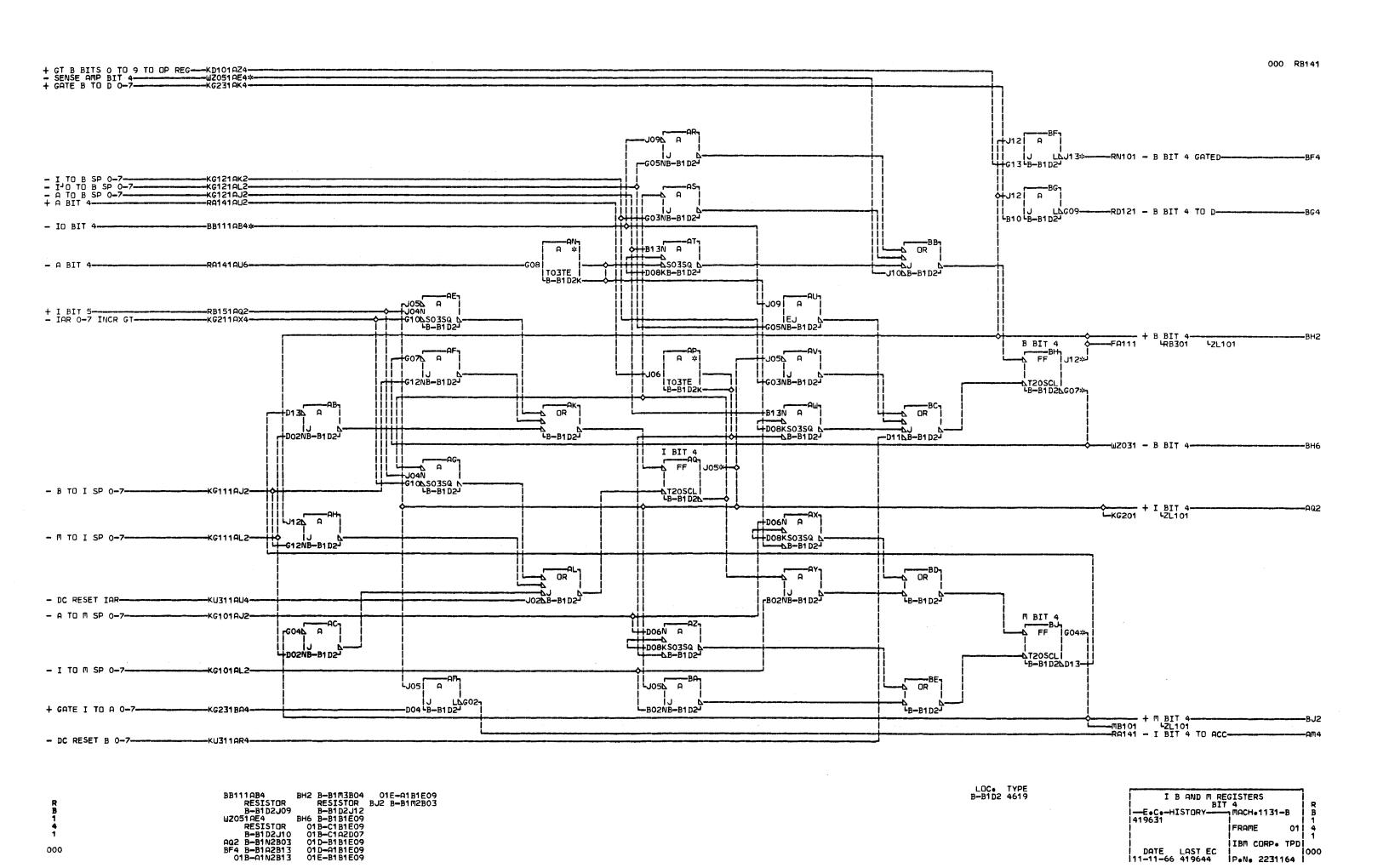
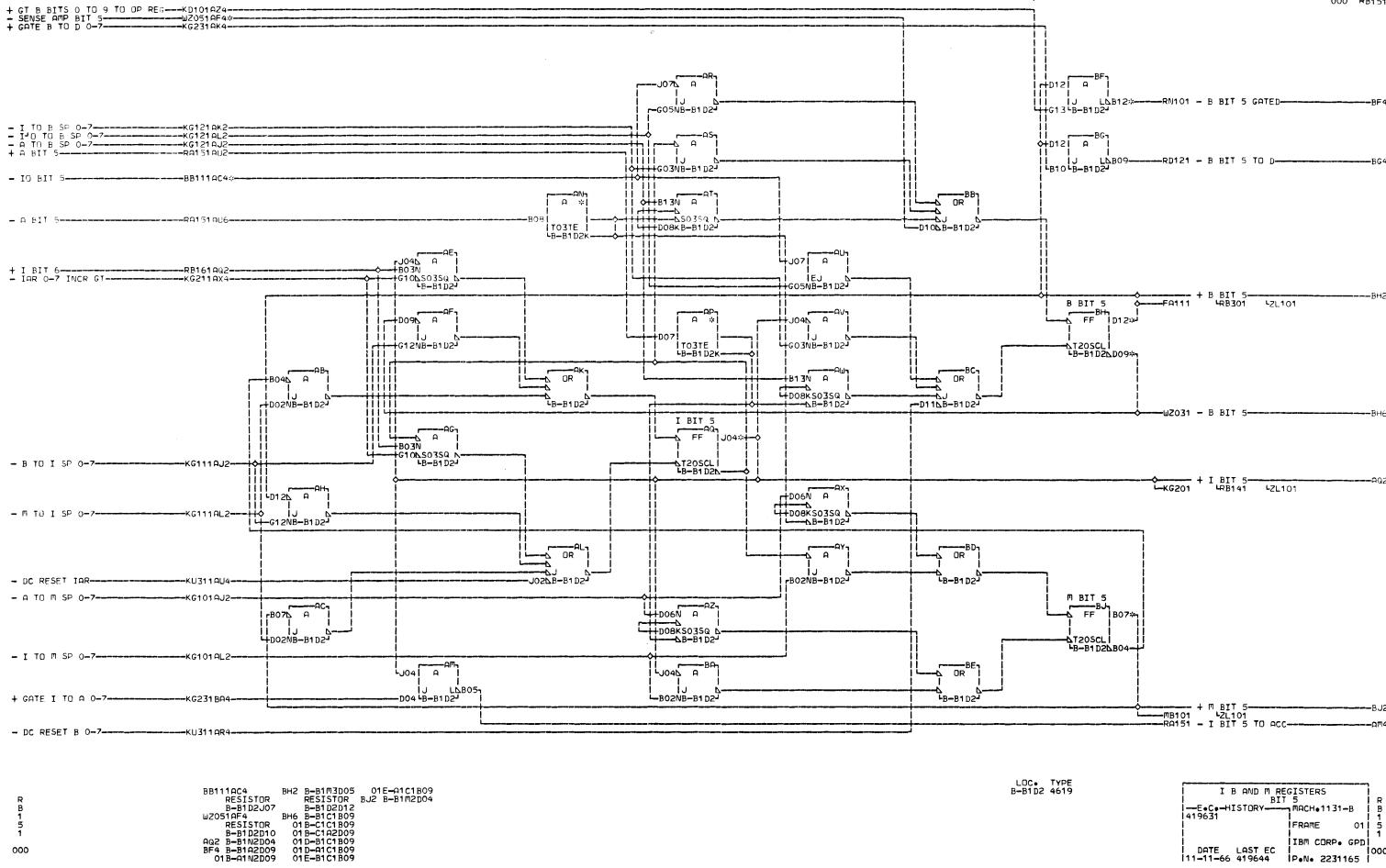


-

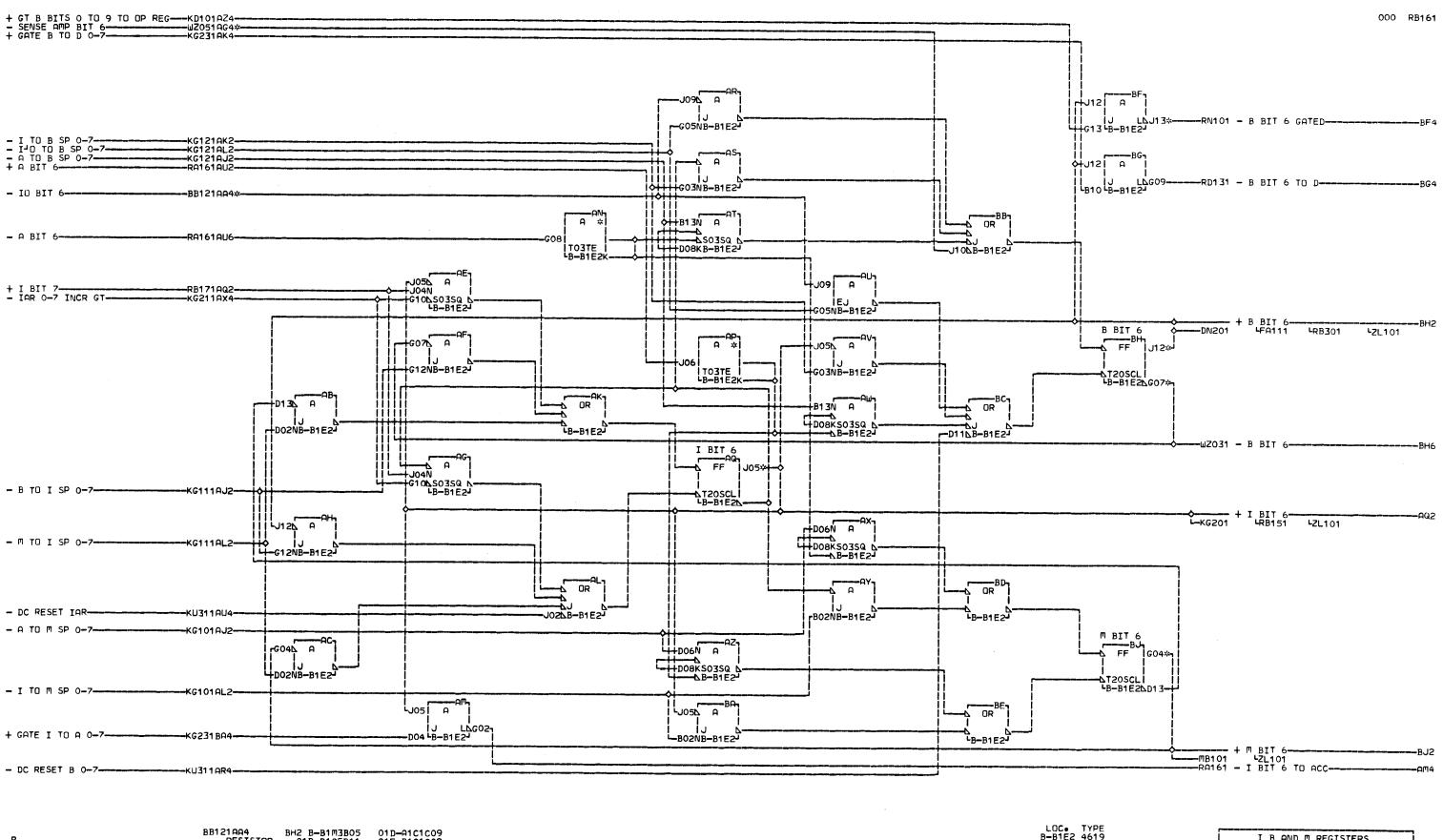




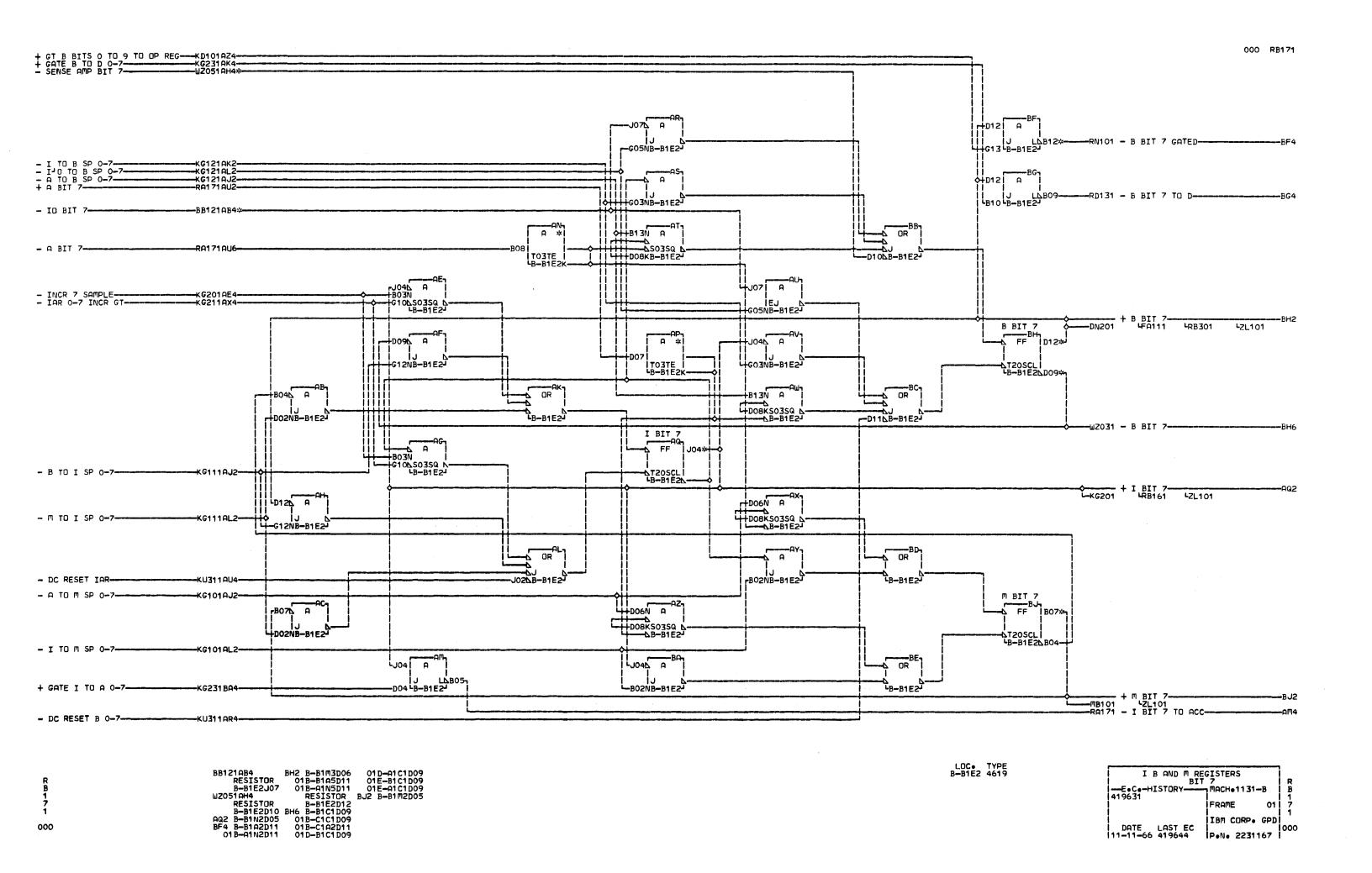
DATE LAST EC

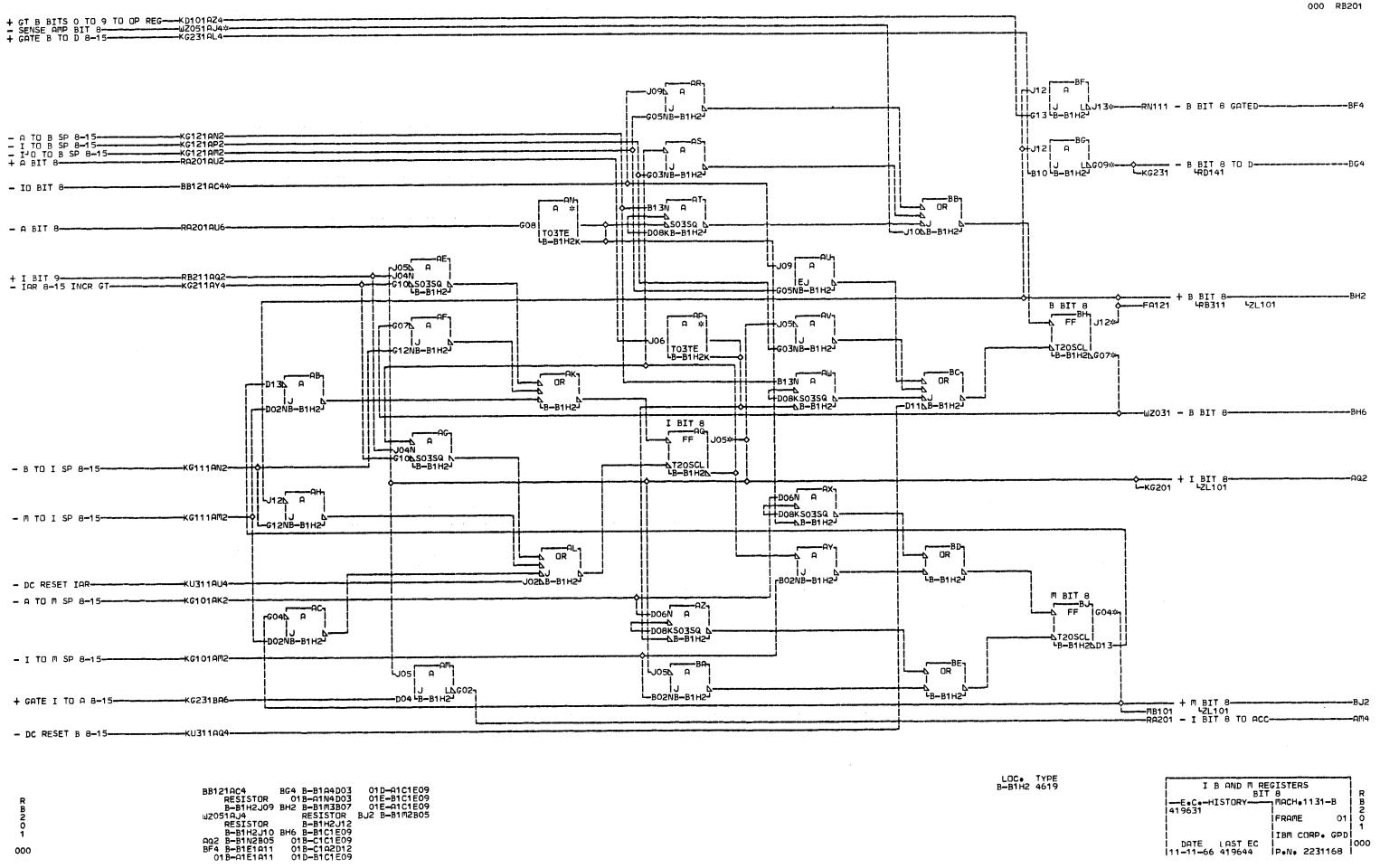
1000

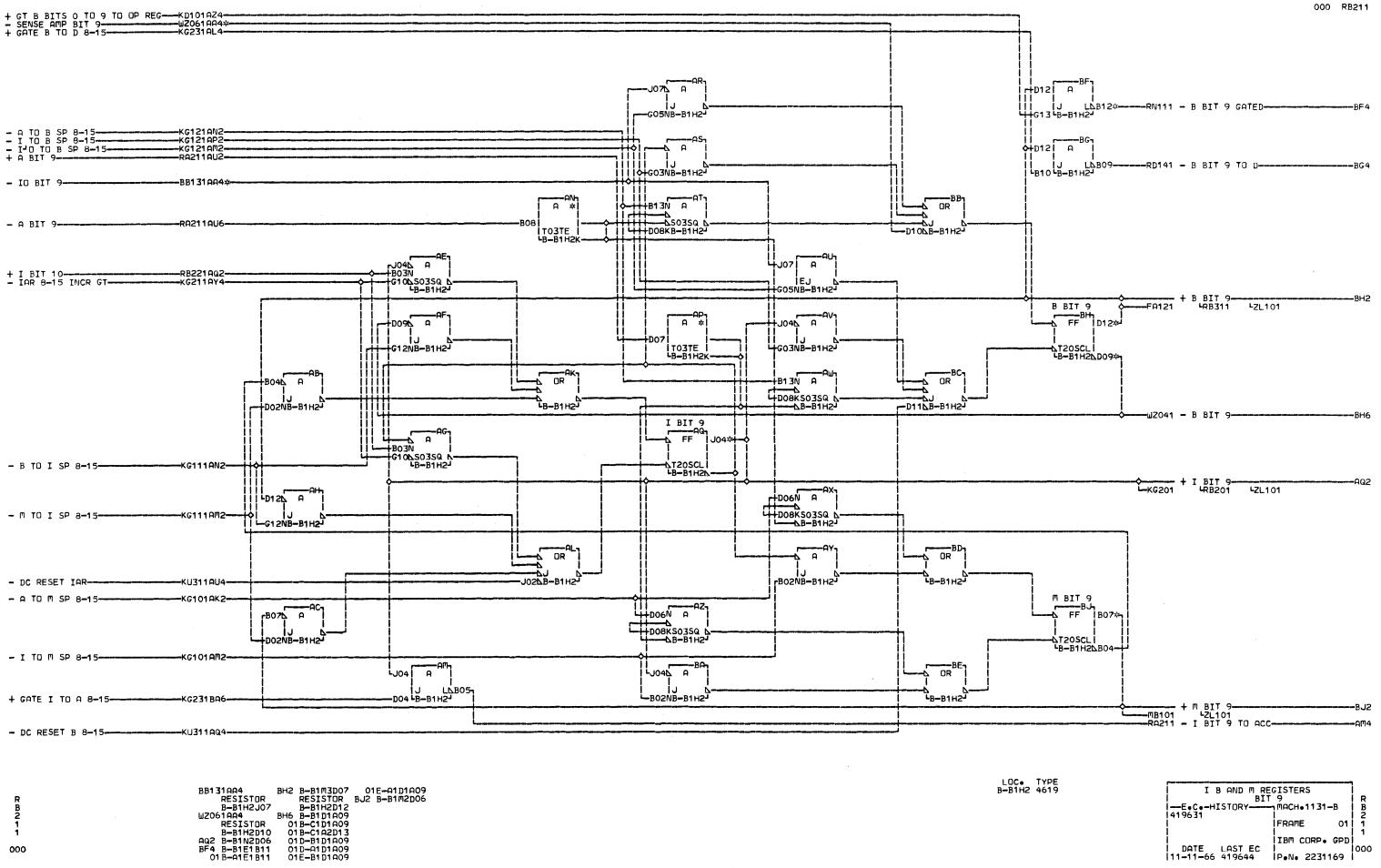
IP.N. 2231165 I

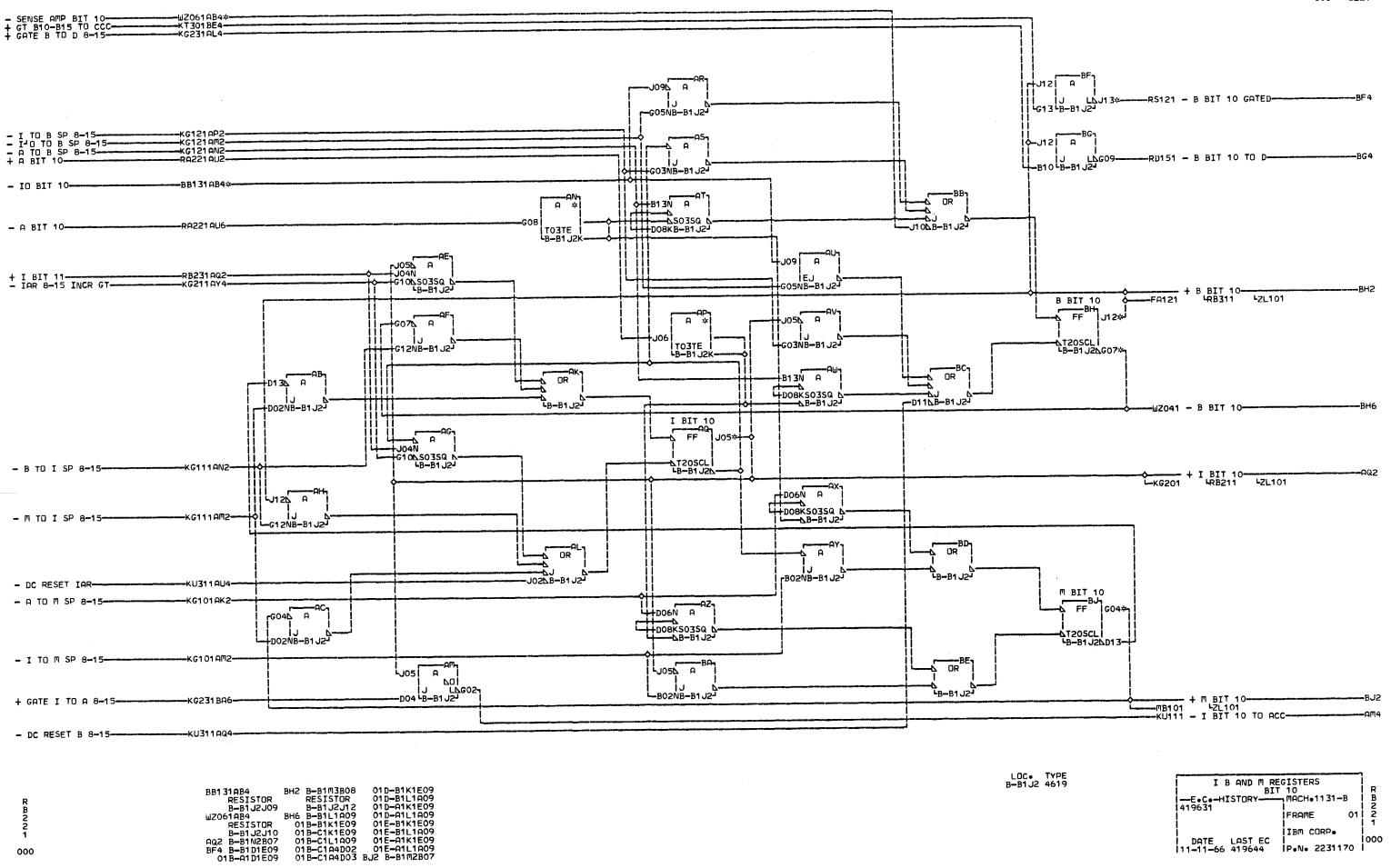


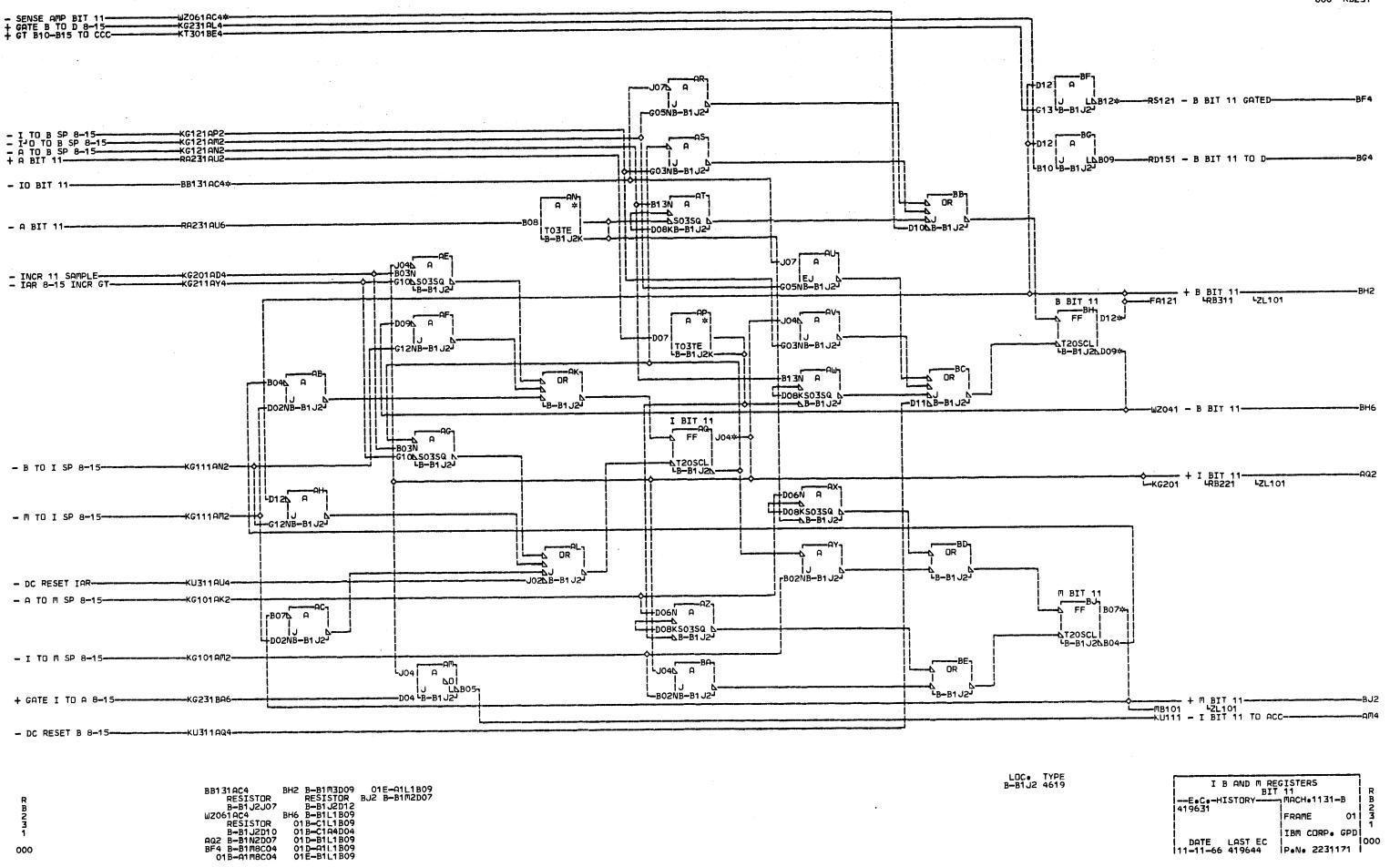
R RESISTOR B-B1 M3B05 01D-A1C1C09
R RESISTOR 01B-B1 A5B11 01E-B1C1C09
B B-B1E2J09 01B-A1N5B11 01E-A1C1C09
1 W2051AG4 RESISTOR B-B1E2J12
1 B-B1E2J10 BH6 B-B1C1C09
000 BF4 B-B1A2D10 01B-C1A2D10
01B-A1N2D10 01D-B1C1C09





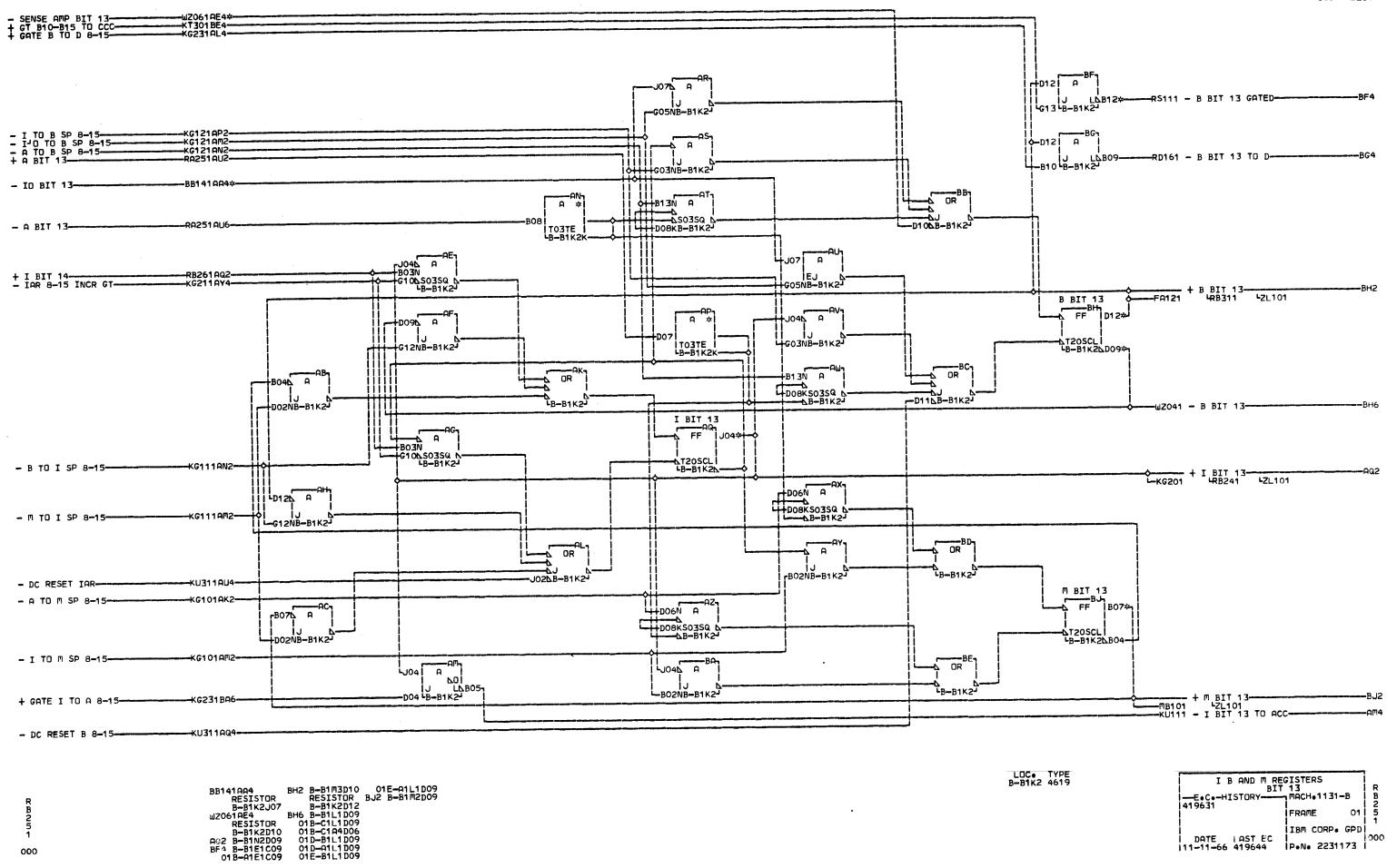


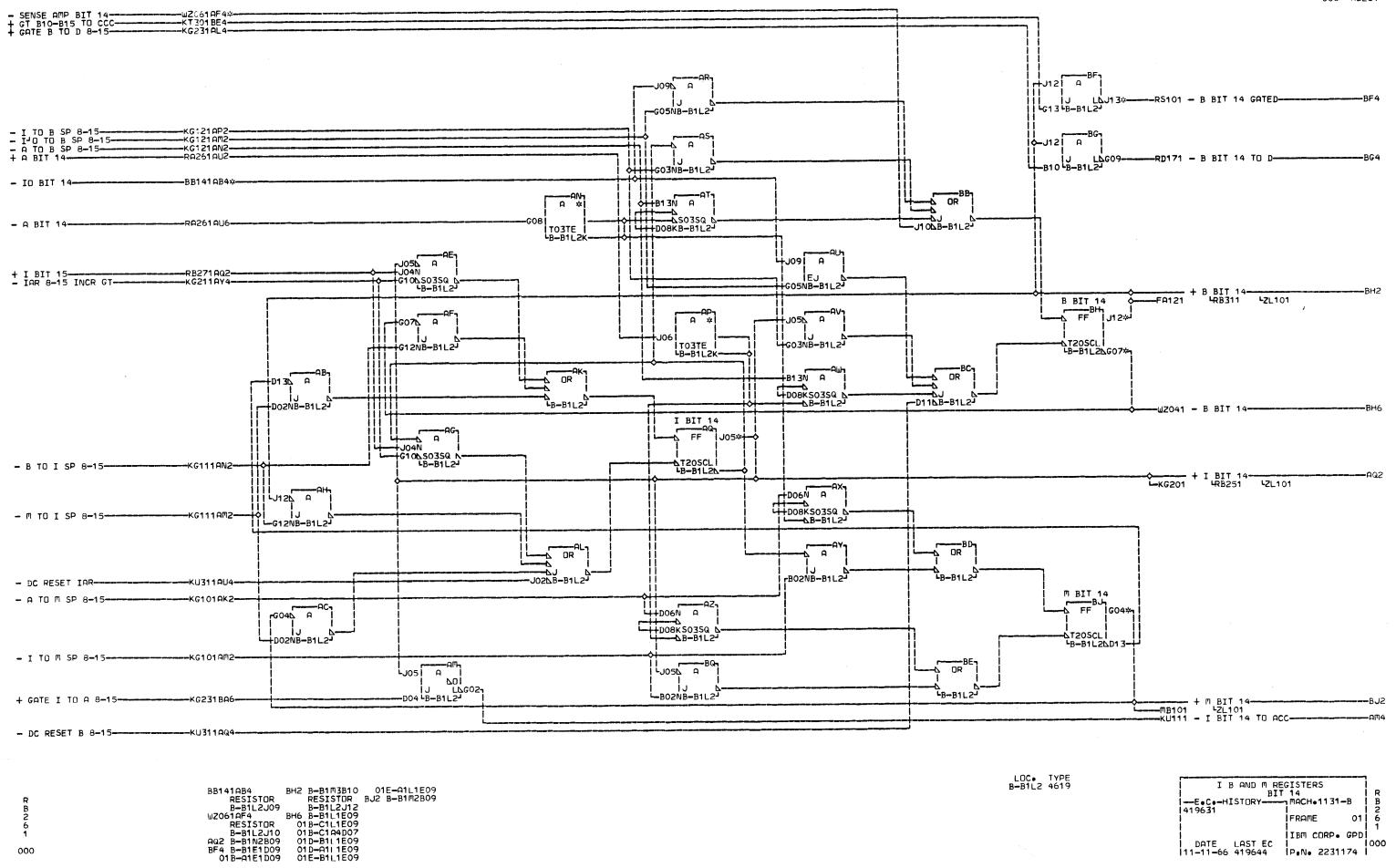


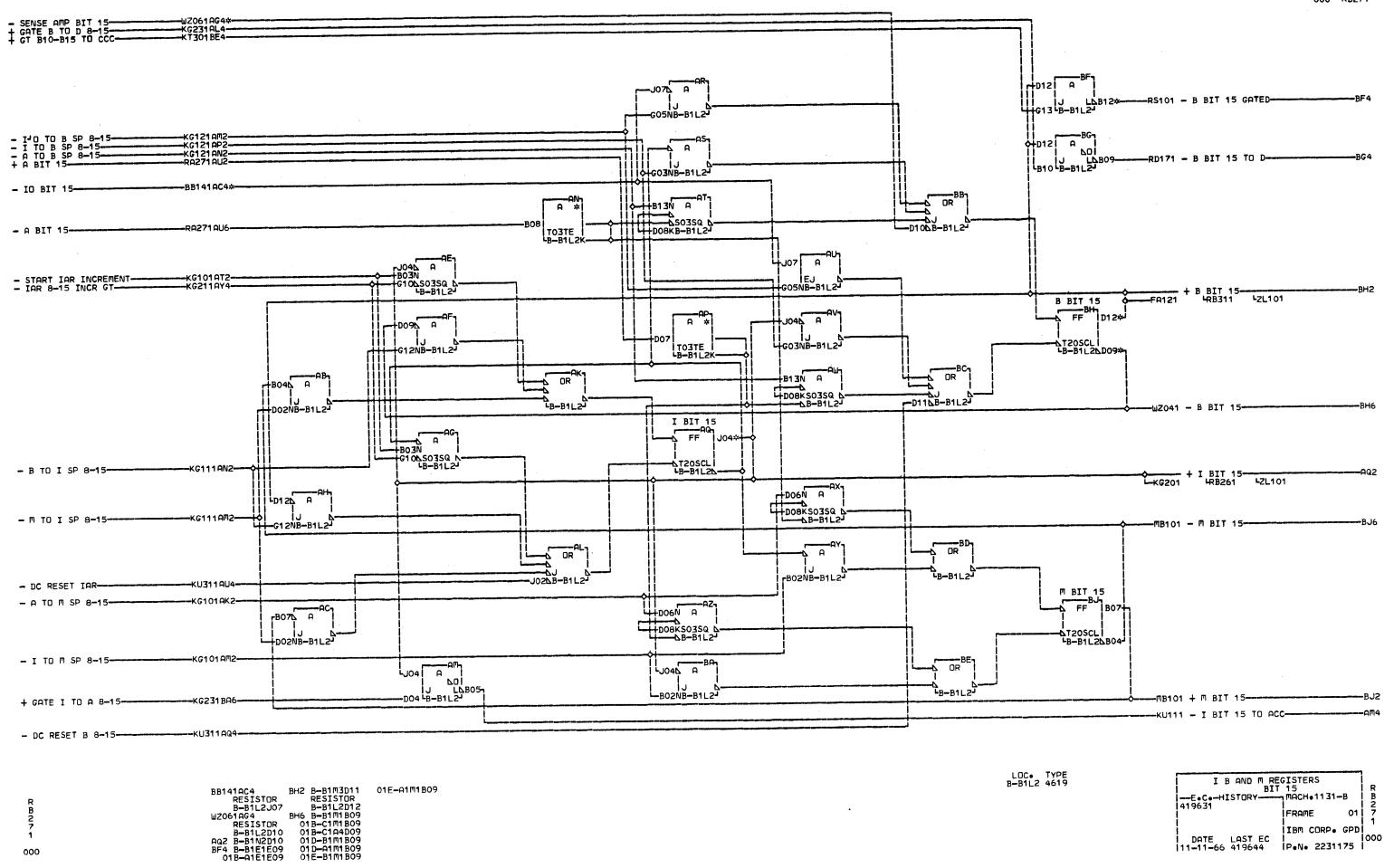


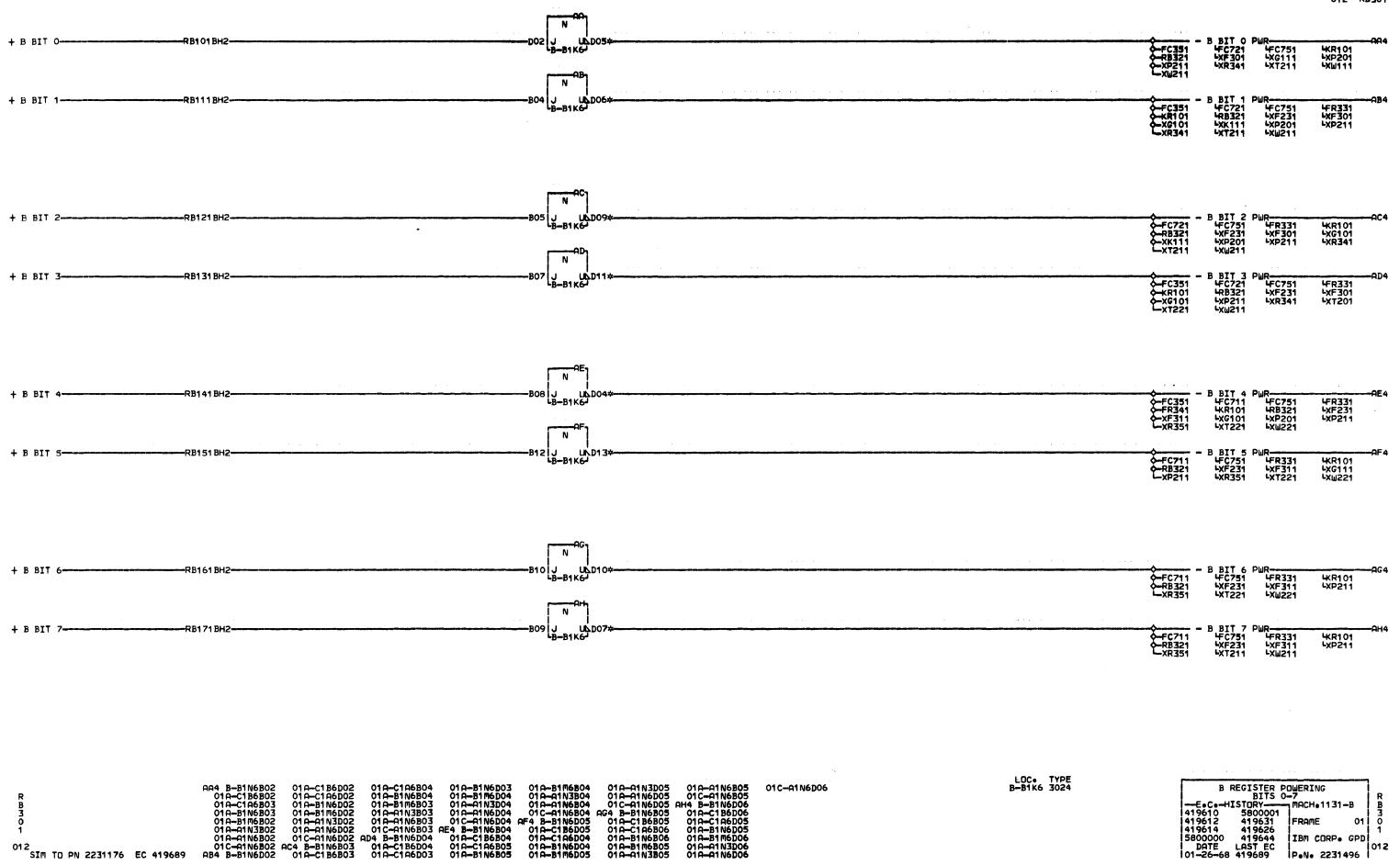
FRAME

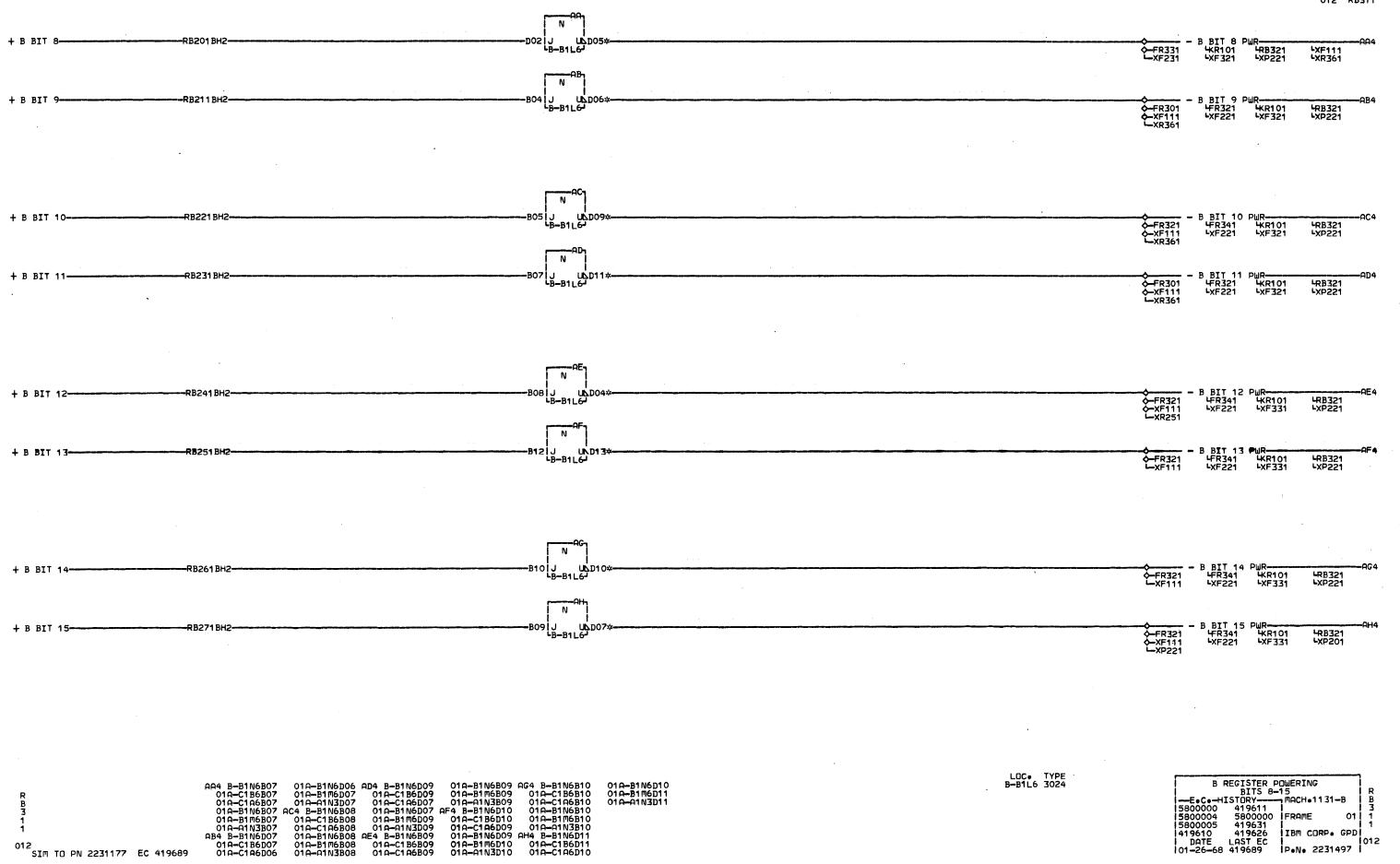
IBM CORP. GPD

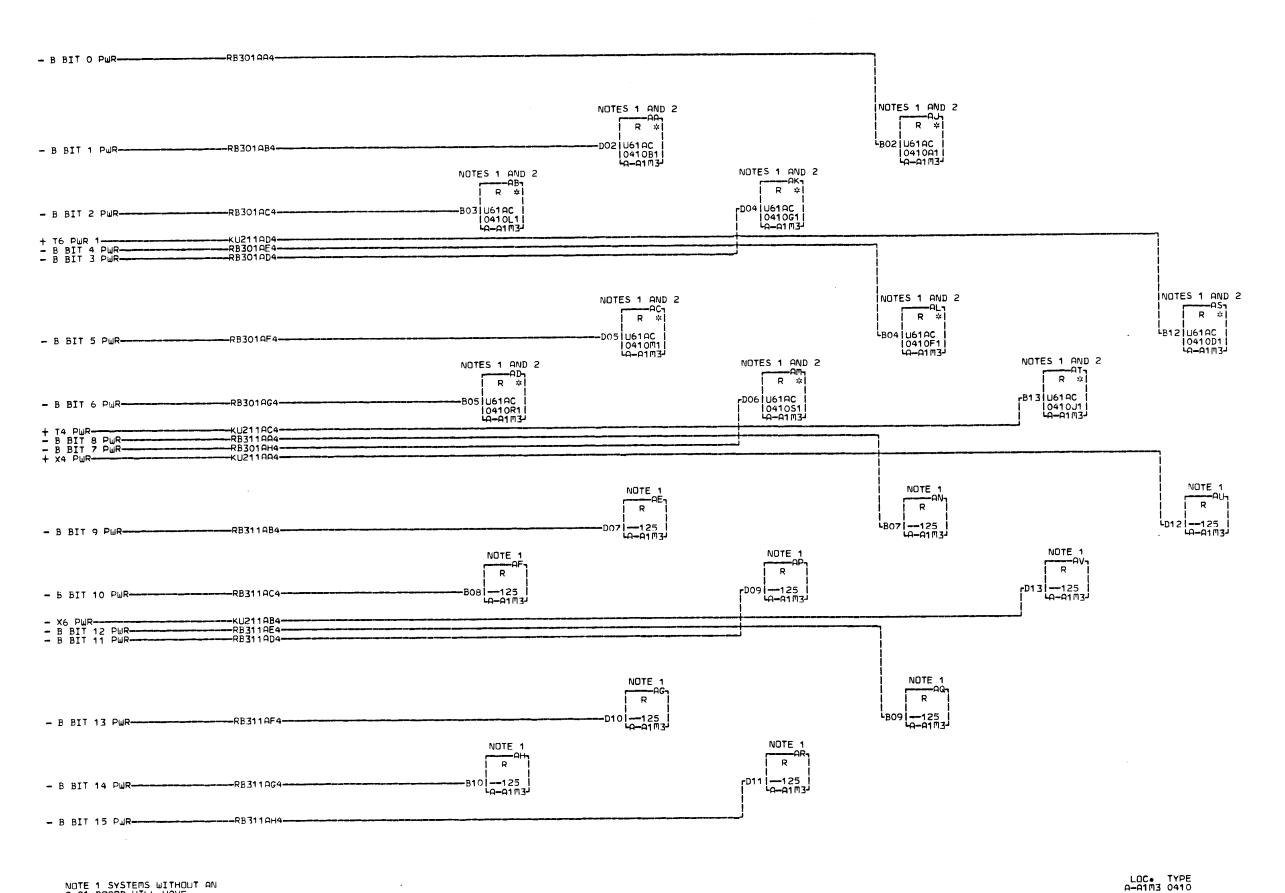




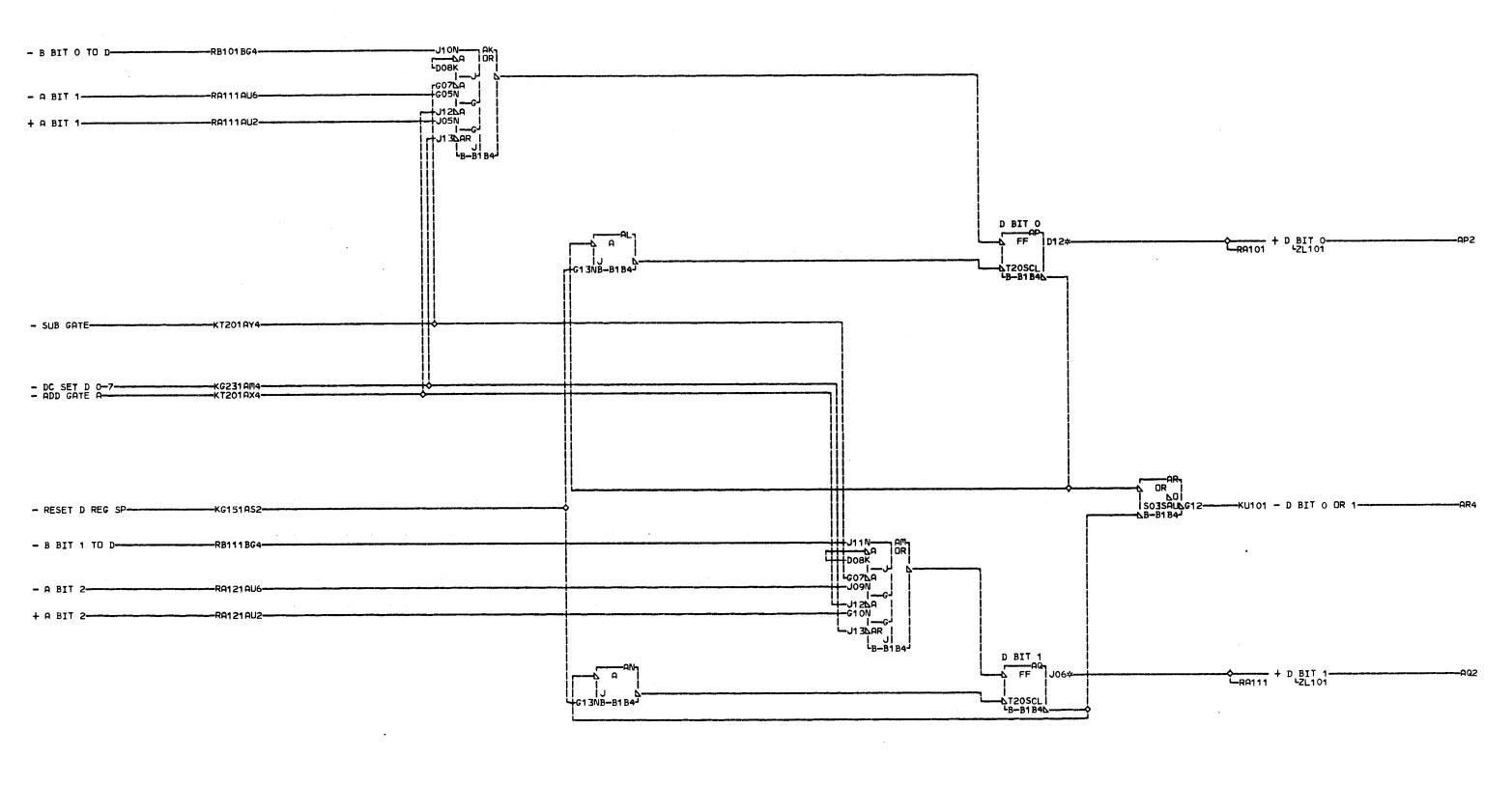








NOTE 1 SYSTEMS WITHOUT AN A-A1 BOARD WILL HAVE
R THIS CARD AT A-B1M6.
B NOTE 2 ON SYSTEMS WITH SCA
3 THE RESISTOR WILL BE
2 DISCONNECTED FROM THE NET.
1
012
SIM TO PN 2231178 EC 419689

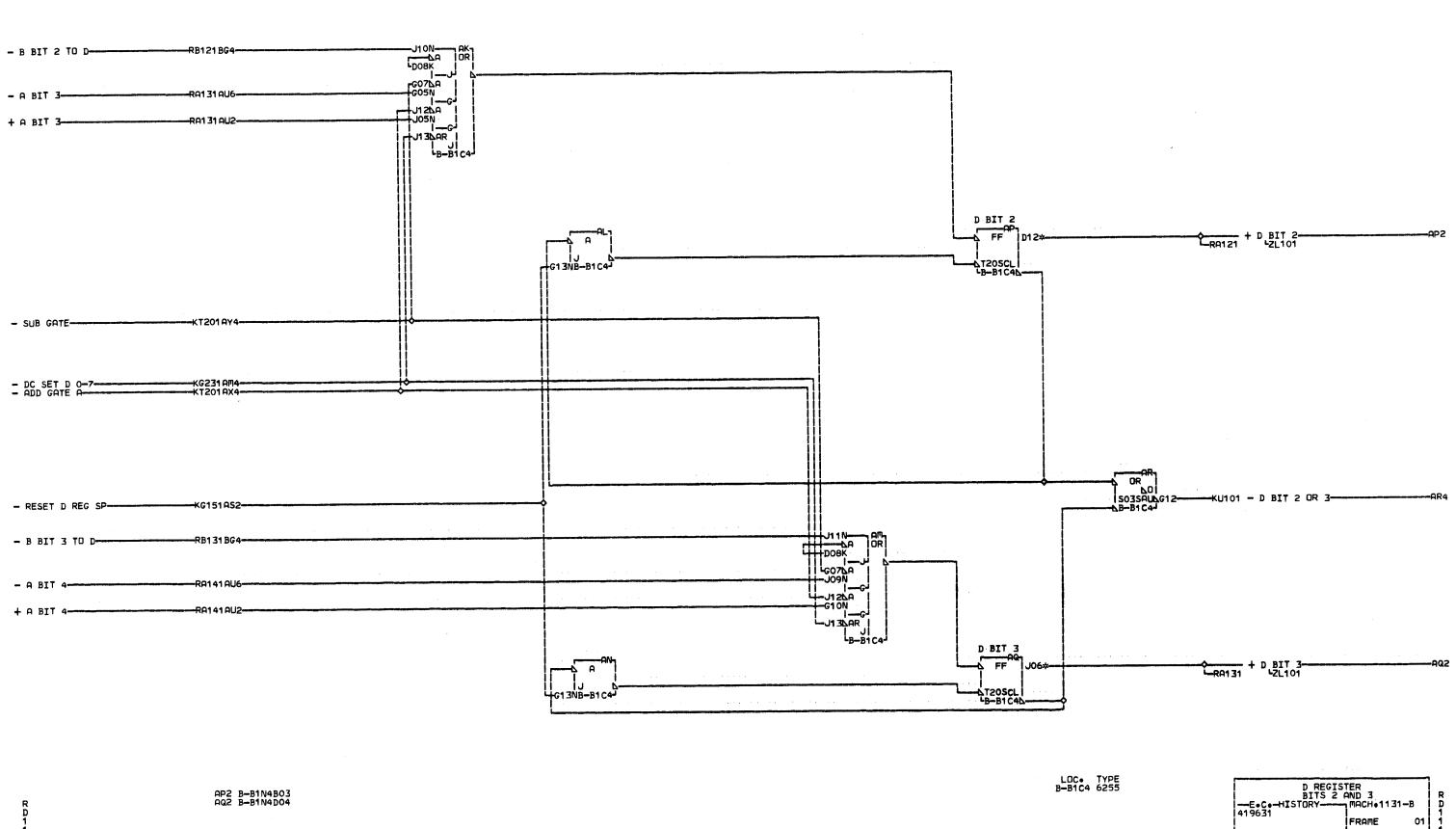


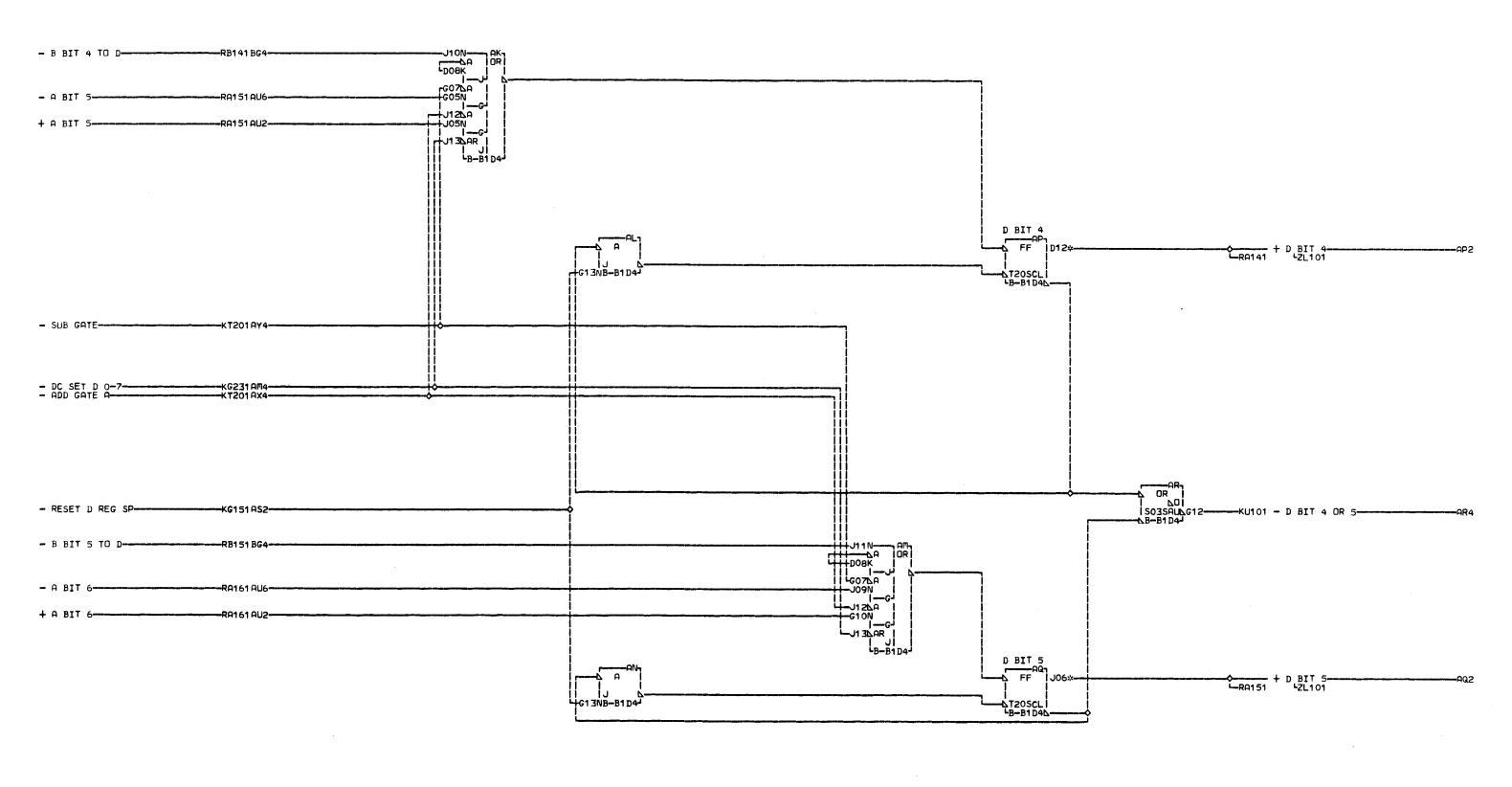
R D 1 0 1 AP2 B-B1N4B02 AQ2 B-B1N4D02 LOC. TYPE B-B1B4 6255

IBM CORP. GPD

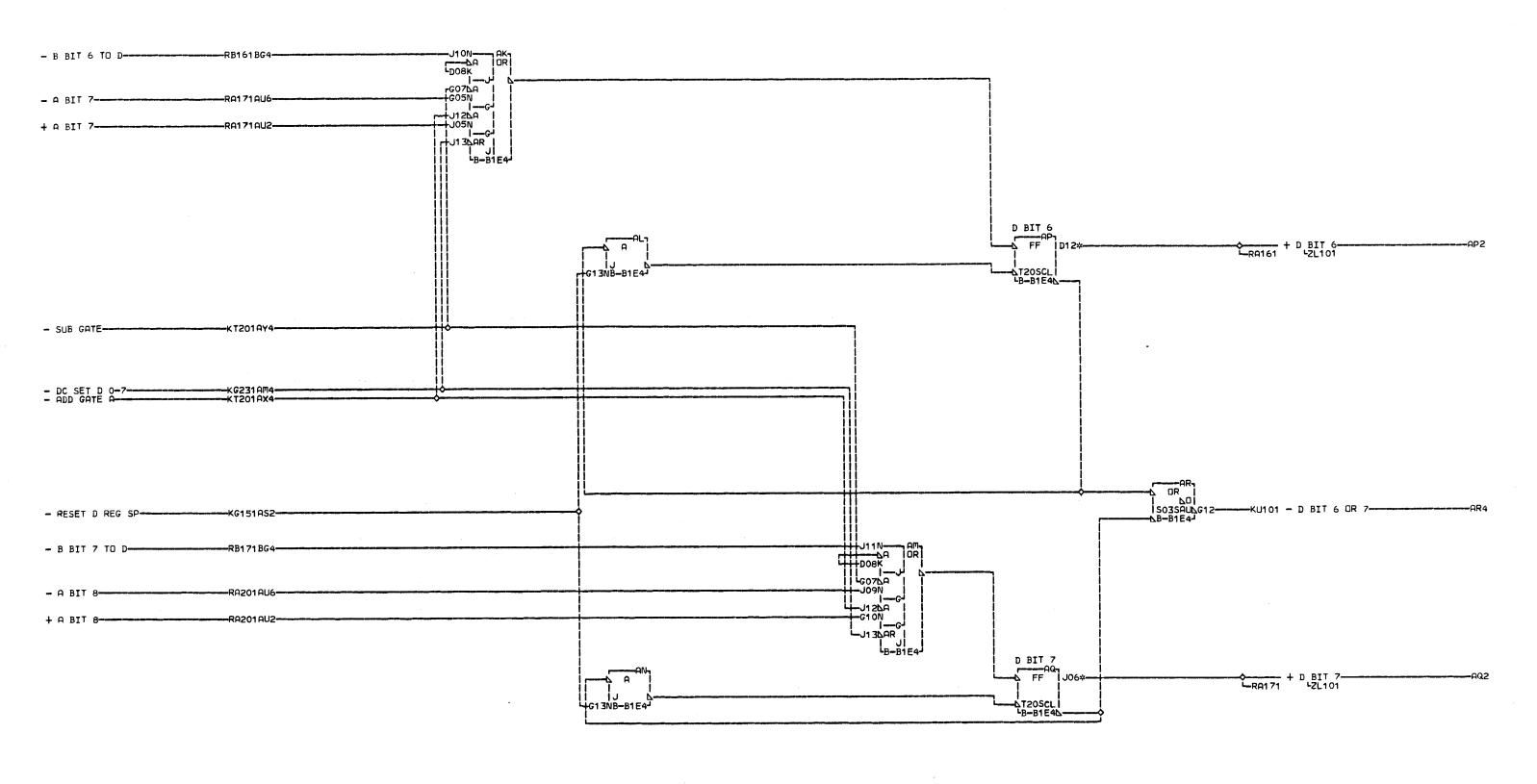
P.N. 2231385

DATE LAST EC





AP2 B-B1 N4B04 AQ2 B-B1 N4D05 LOC. TYPE B-B1D4 6255



R D 1 3 1 AP2 B-B1N4B05 AQ2 B-B1N4D06 LOC. TYPE B-B1E4 6255 D REGISTER
BITS 6 AND 7
R
419631

FRAME

DATE LAST EC
11-11-66 419644

DRIFT ROME

DRIFT LAST EC
11-11-66 419644

DRIFT ROME

DRIFT LAST EC
11-11-66 419644

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DATE LAST EC
11-11-66 419644

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DATE LAST EC
11-11-66 419644

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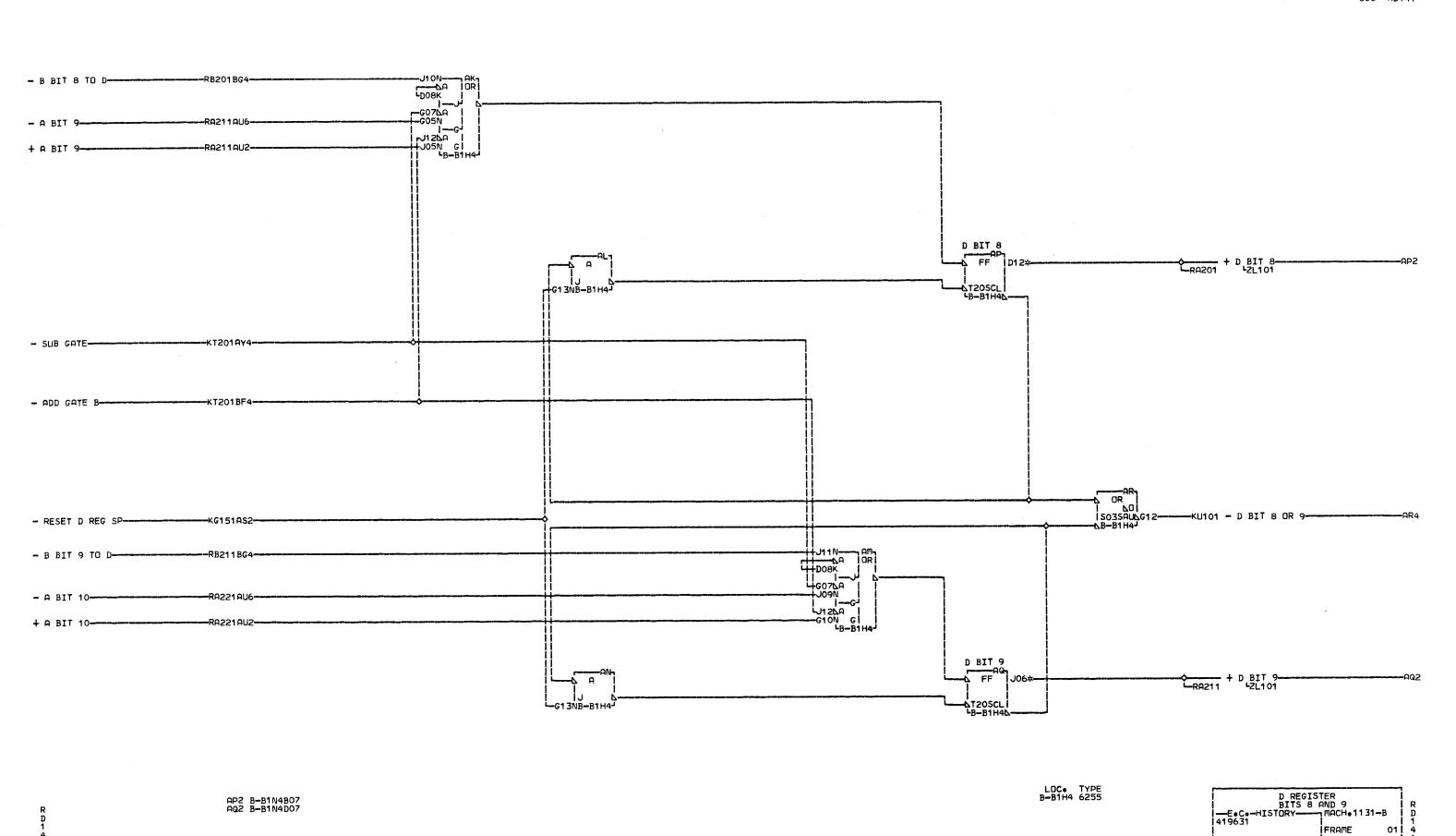
DOOD

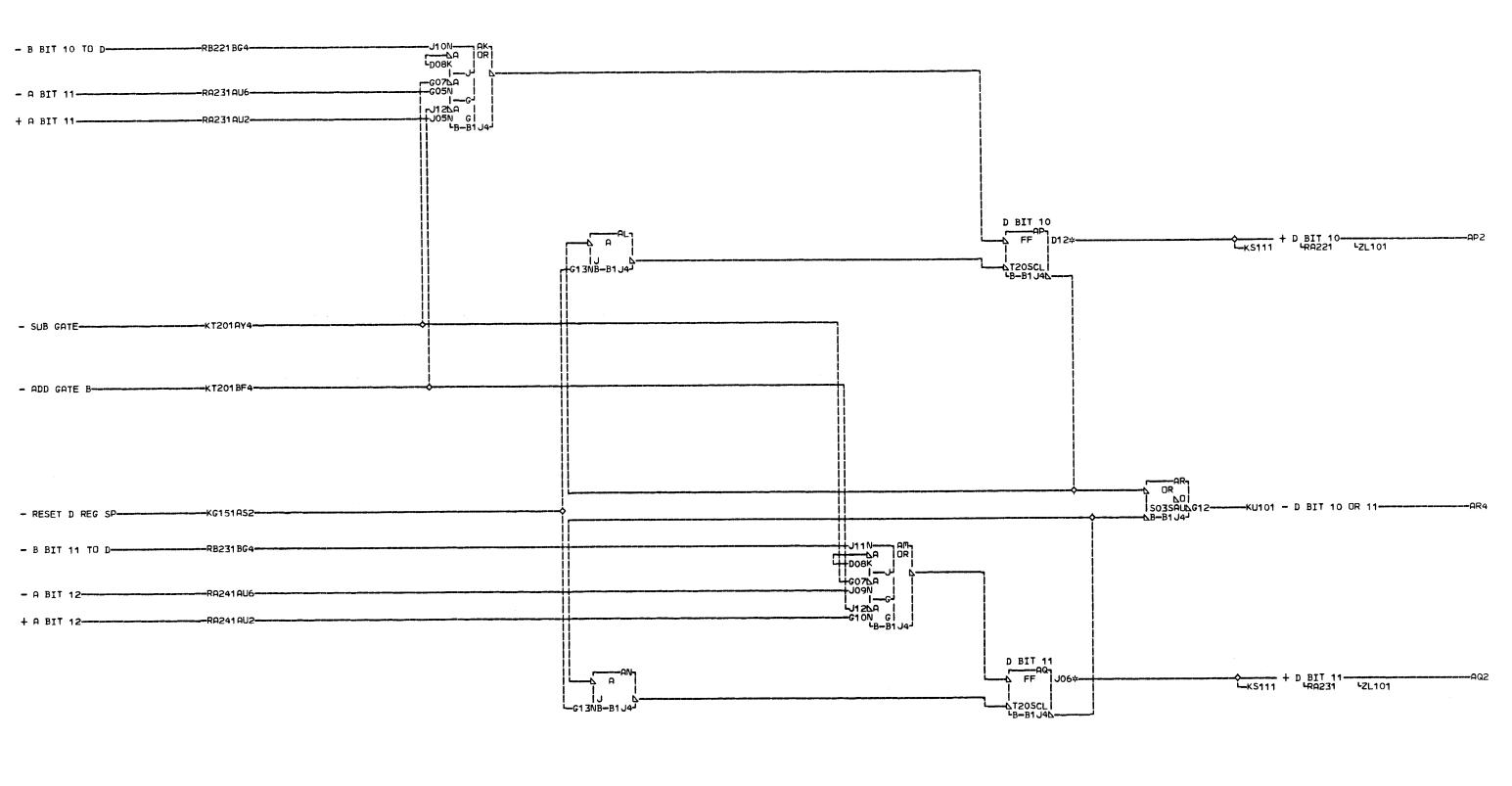
DRIFT ROME

DRIFT

IBM CORP. GPD

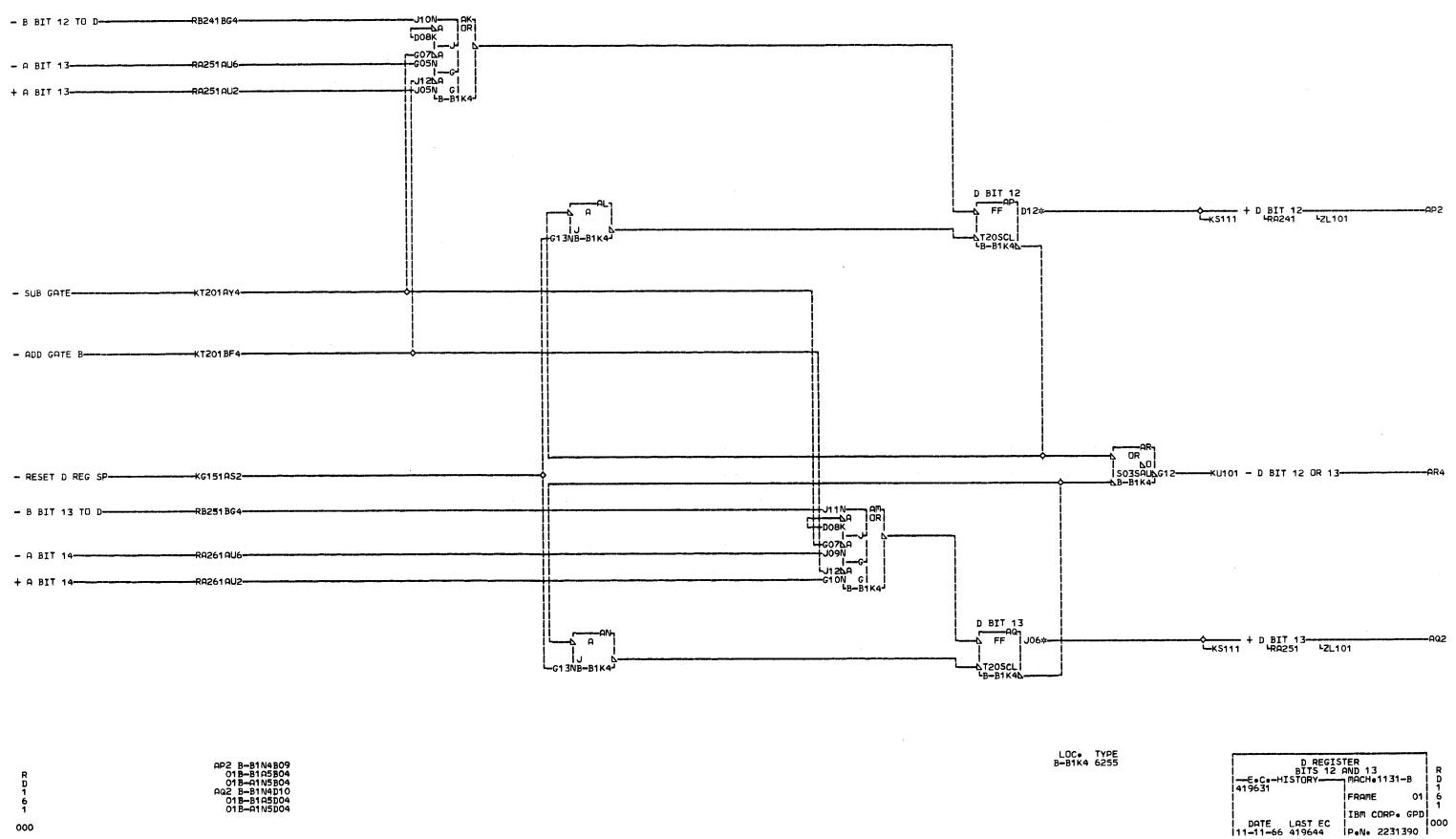
DATE LAST EC | | 111-11-66 419644 | P.N. 2231388 |

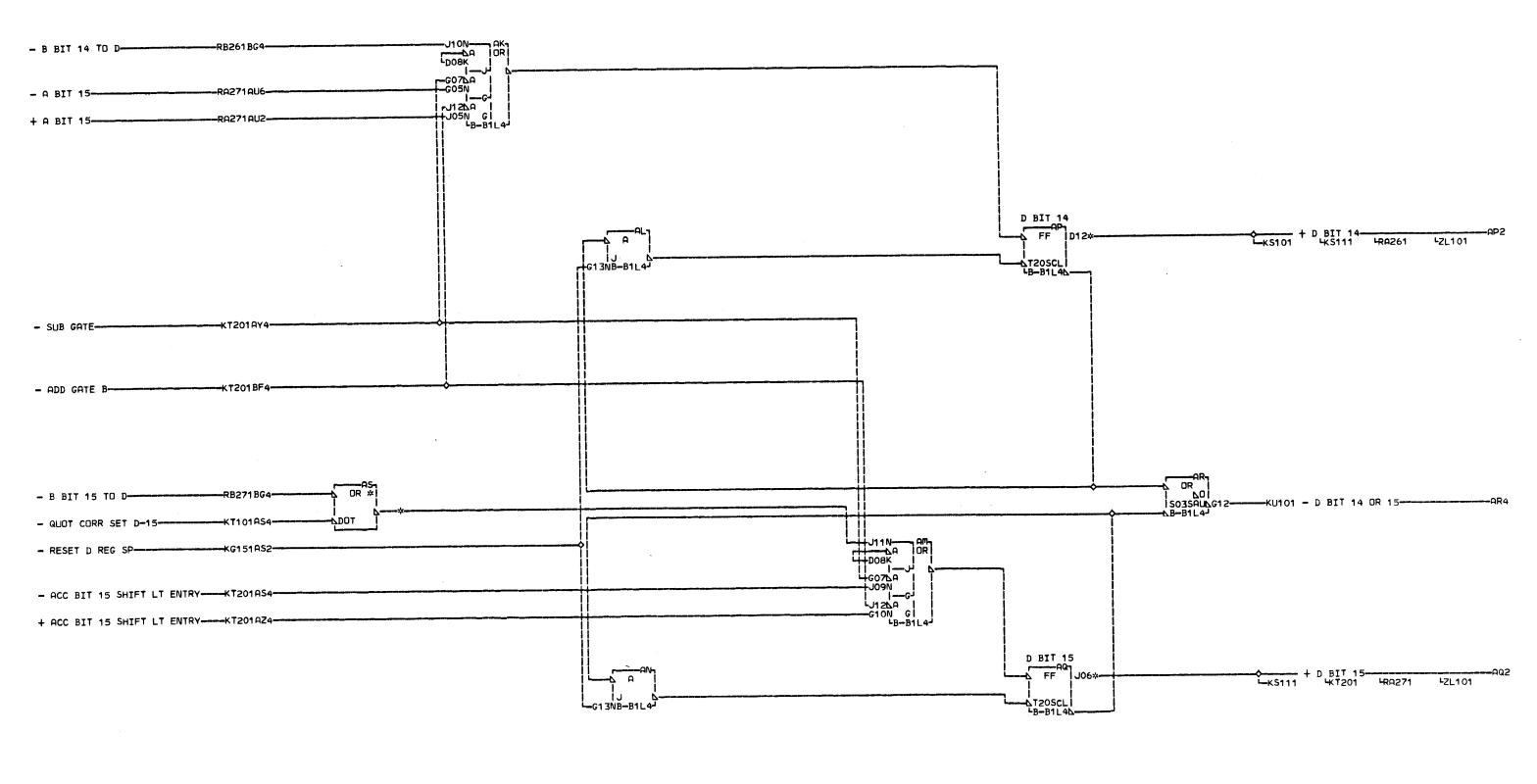




R D 1 5 1 AP2 B-B1N4B08 01B-B1A5B03 01B-A1N5B03 AQ2 B-B1N4D09 01B-B1A5D03 01B-A1N5D03 LOC. TYPE
B-B1J4 6255

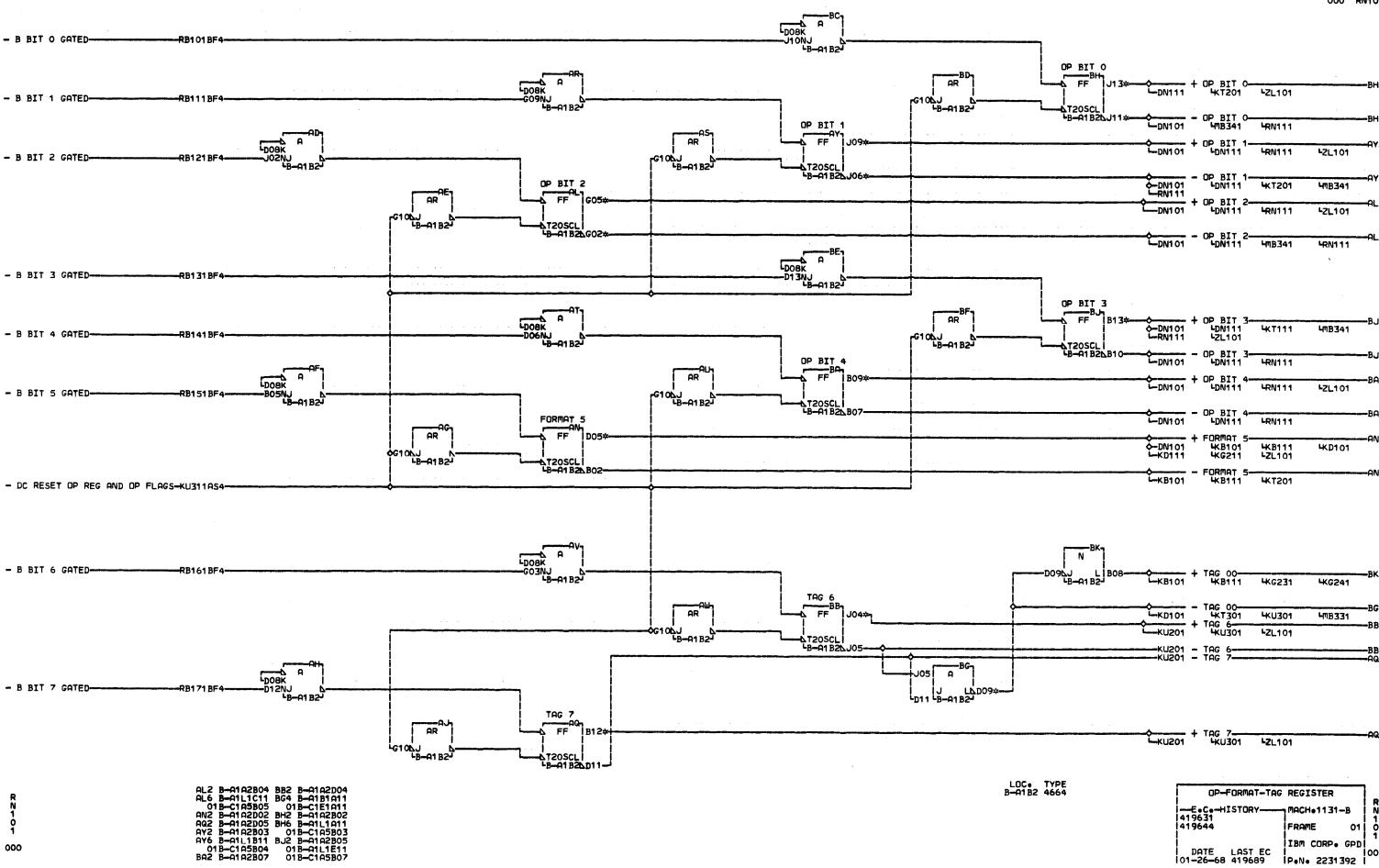
D REGISTER
BITS 10 AND 11
R
419631
FRAME
01 5
DATE LAST EC
11-11-66 419644
PeNe 2231389





R 01B-B1N4B10 01B-B1A5B05 01B-A1N5B05 1 01B-B1A5D05 1 01B-B1A5D05 1 01B-B1A5D05 01B-A1N5D05 01B-A1N5D05 01B-A1N5D05 01B-A1N5D05 01B-A1E1C11 01B-A1E1C11

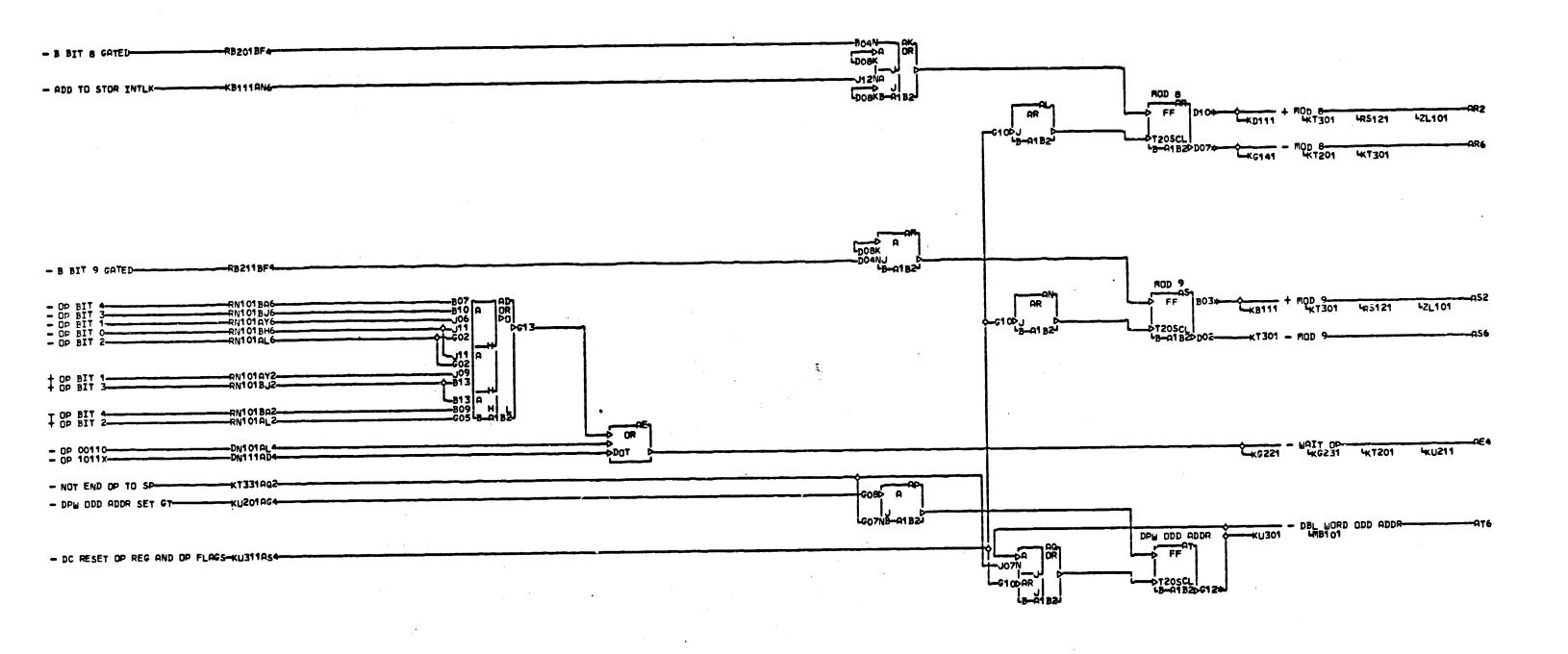
_OC∙ TYPE -B1L4 6255	D REGIS BITS 14	AND 15	
	419031	FRAME 01	
	DATE LAST EC	IBM CORP. GPD	0
	111-11-00 419044	1641A# 5531321	•



DATE LAST EC

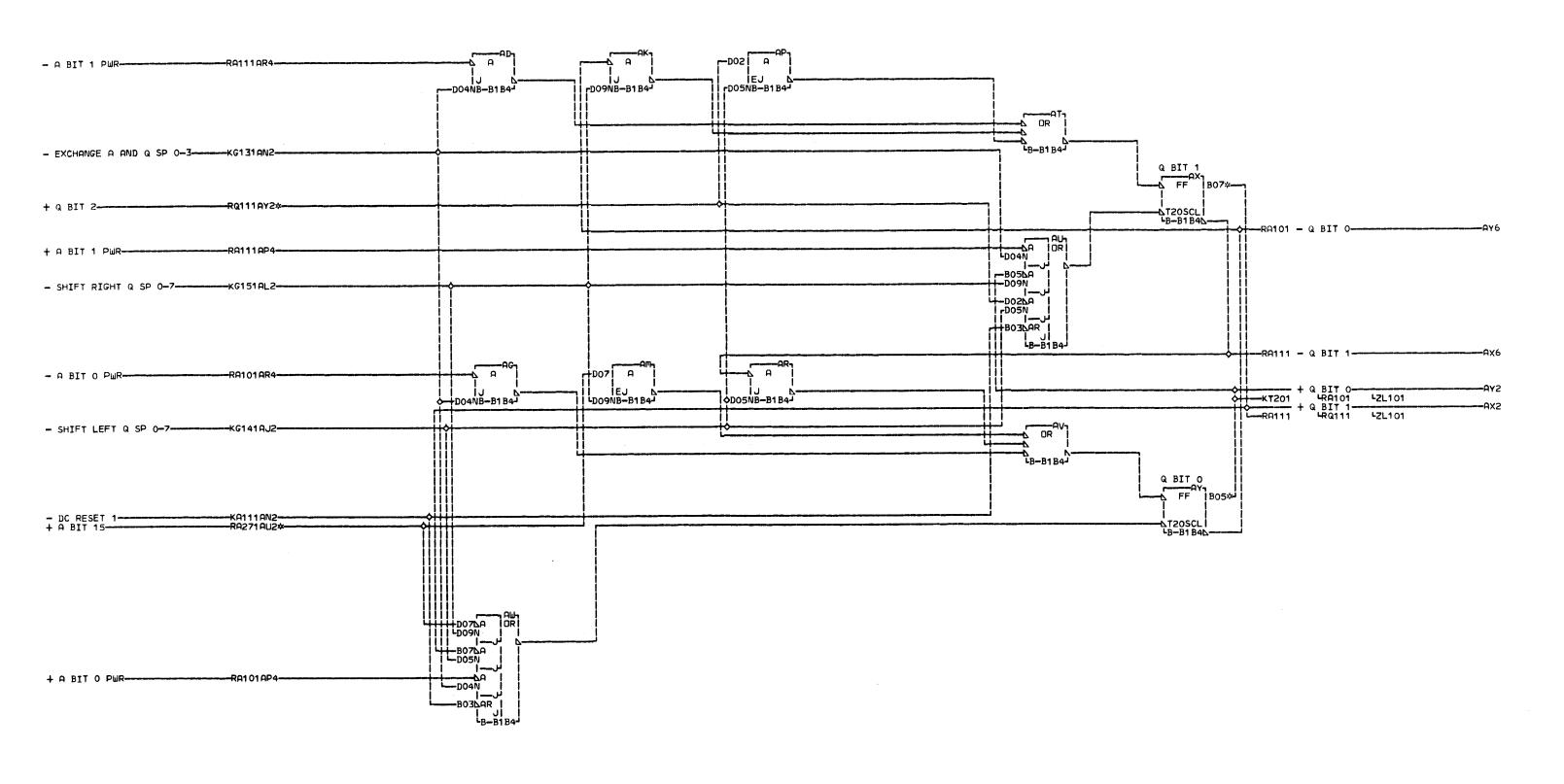
101-26-68 419689

PeNe 2231392



AR2 B-A1 A2D06 AR6 B-A1 N3B09 O1B-B1 A3B09 AS2 B-A1 A2D07 AT6 B-A1 M8B06 O1B-B1 M8B06 LOC. TYPE B-R1B2 4664 MOL 8 MOD 9 WAIT OP
DBL WORD ODD ADDR

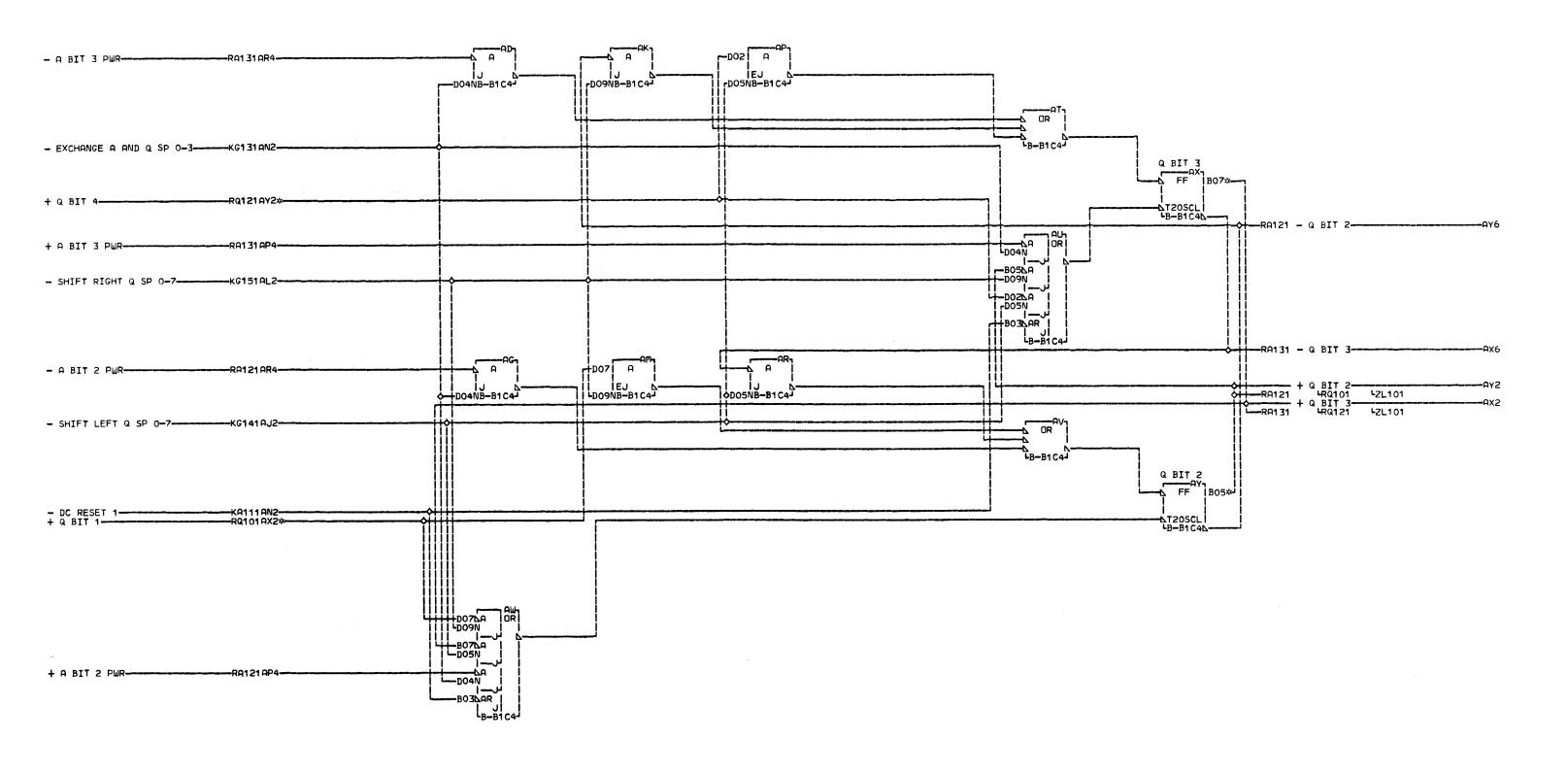
—E.C.-HISTORY— MACH-1131-B
FRAME 01
1
DATE LAST EC
08-30-66 419631 P.N. 2231188



R Q 1 0 1 RA271AU2 RESISTOR B-B1B4D07 RQ111AY2 RESISTOR B-B1B4D02 AX2 B-B1N3D02 AY2 B-B1N3B02 01B-B1A5B08

01 B-A1 N5B08

LOC. TYPE B-B1B4 6255

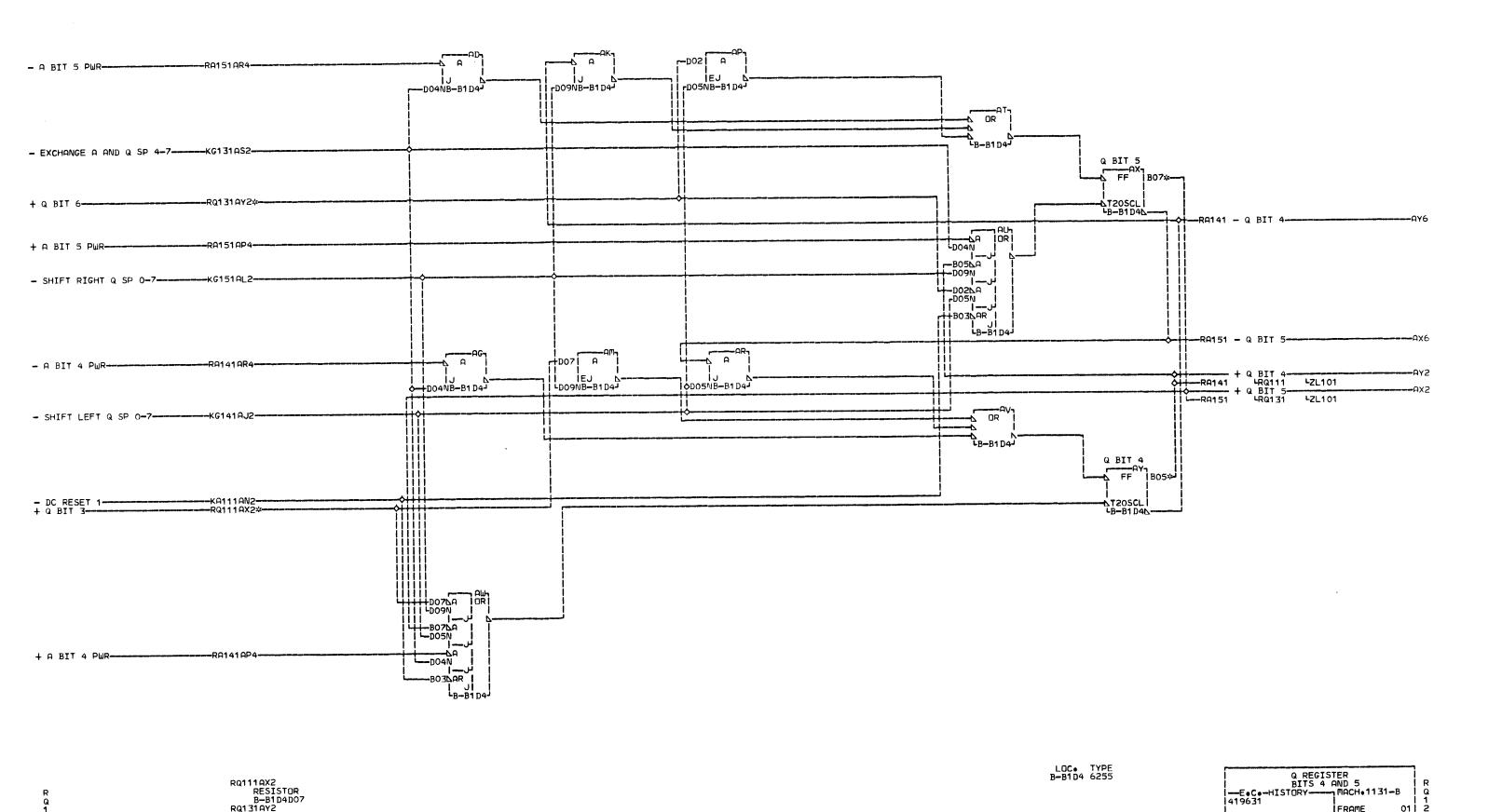


RQ101AX2 RESISTOR B-B1C4D07 RQ121AY2 RESISTOR B-B1C4D02 AX2 B-B1N3D04 AY2 B-B1N3B03 LOC. TYPE B-B1C4 6255

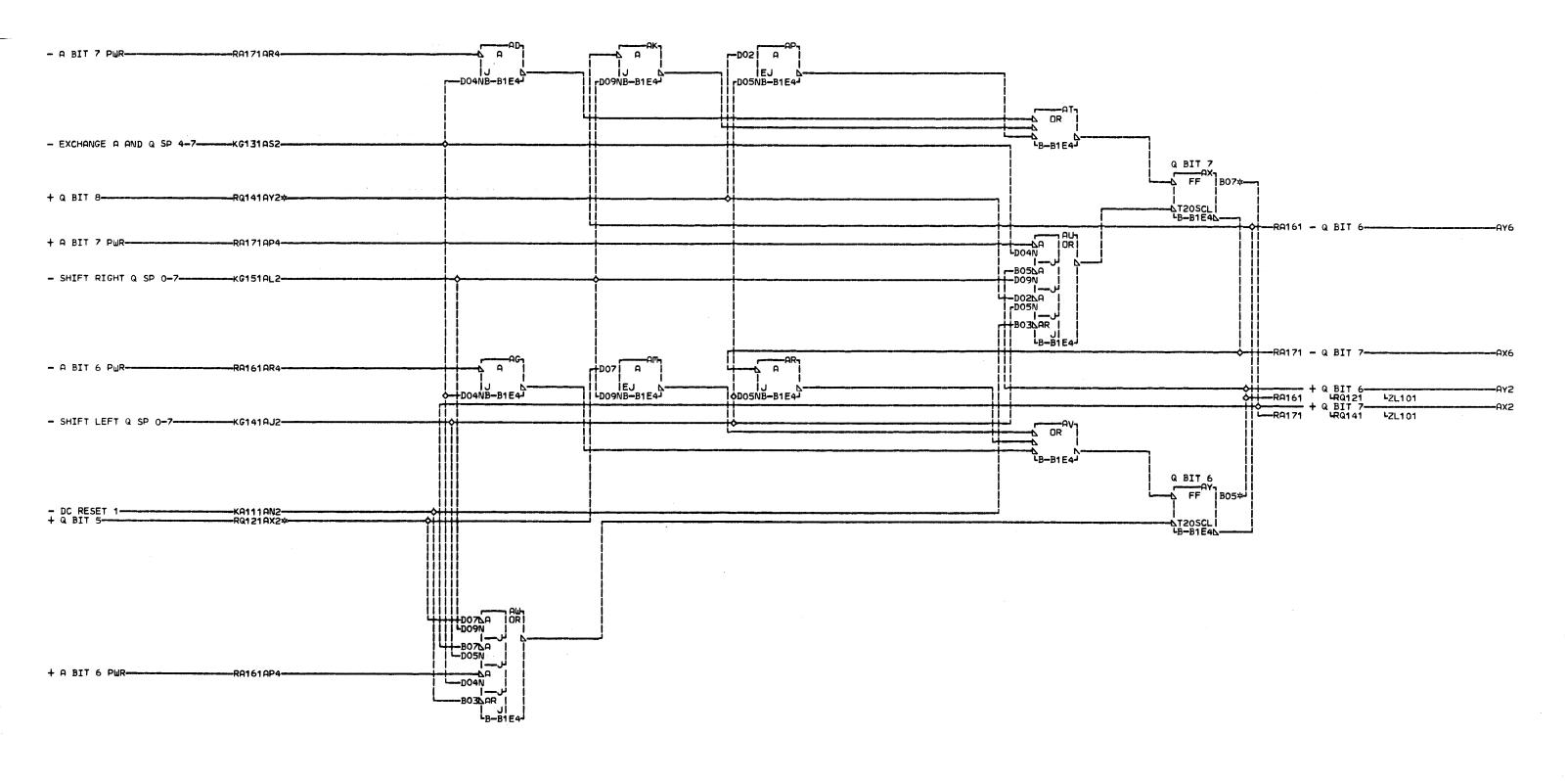
000

IBM CORP. GPD

| DATE LAST EC | 10 |11-11-66 419644 | P.N. 2231395 |



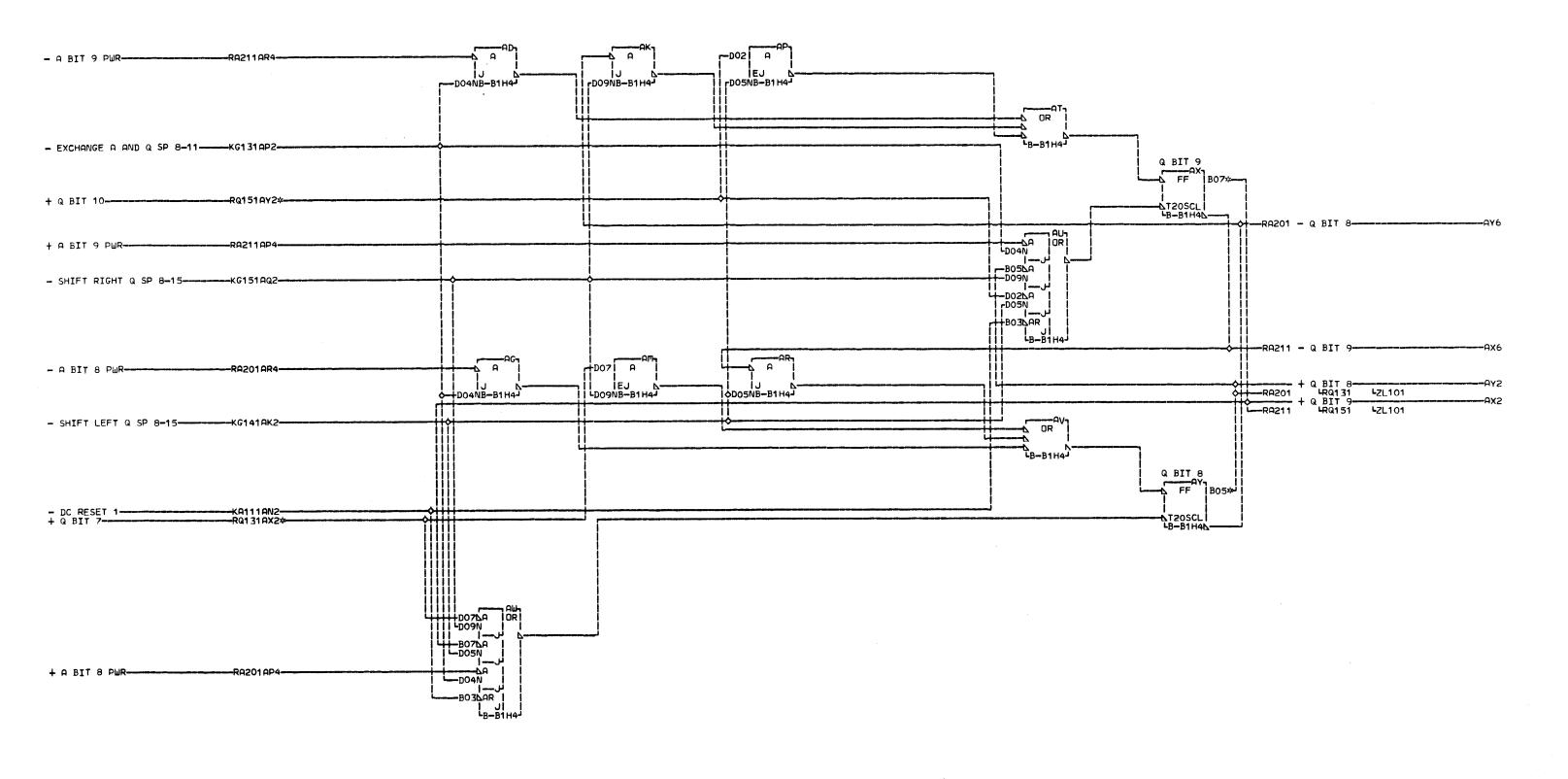
RQ111AX2 RESISTOR B-B1D4D07 RQ131AY2 RESISTOR B-B1D4D02 AX2 B-B1N3D05 AY2 B-B1N3B04



RQ121AX2
RESISTOR
B-B1E4D07
RQ141AY2
RESISTOR
RQ141AY2
RESISTOR
B-B1E4D02
AX2 B-B1N3D06
RY2 B-B1N3B05

LOC. TYPE B-B1E4 6255

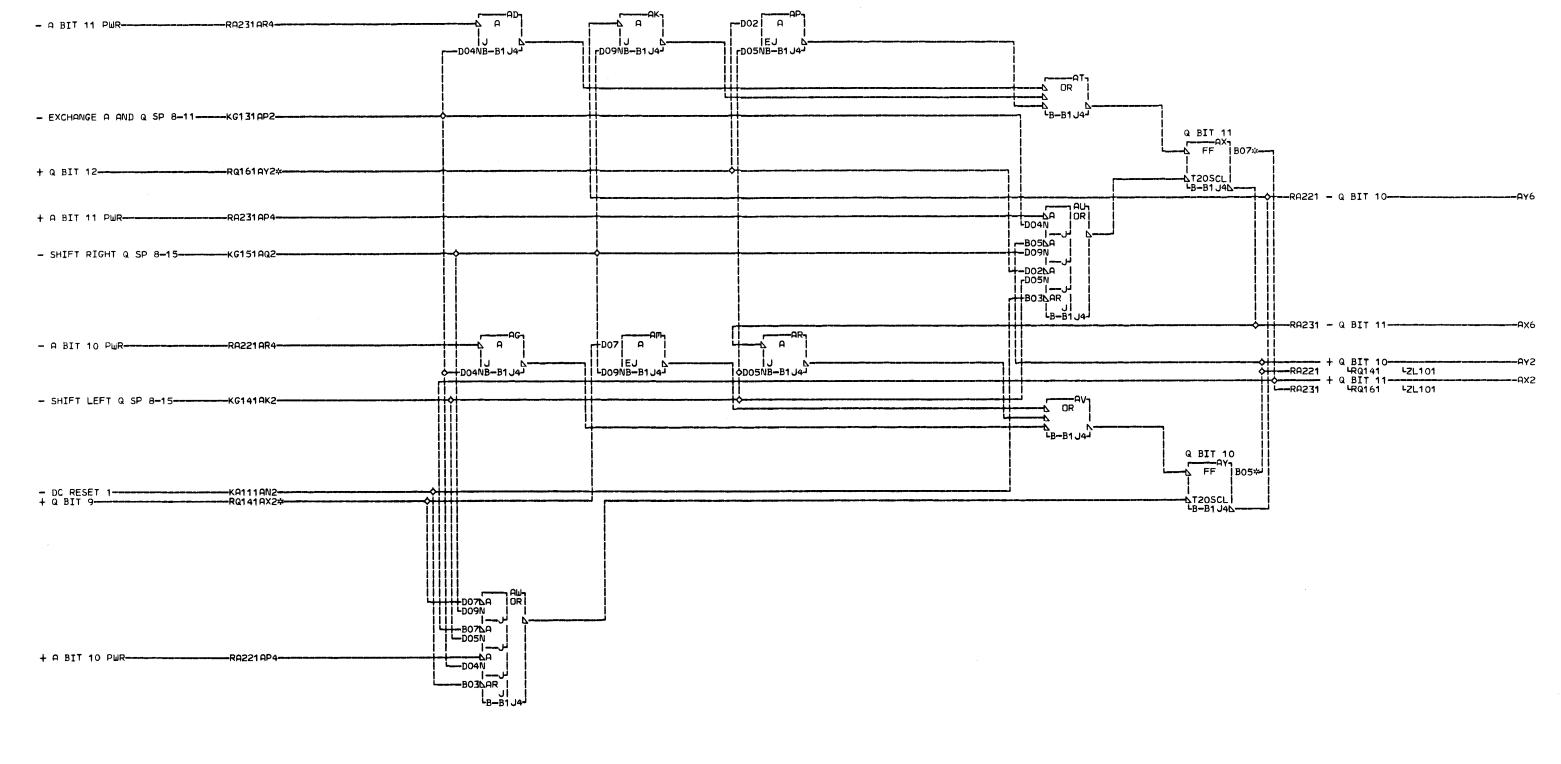
	Q REGISTER BITS 6 AND 7		R
	419631	FRAME 01	1 3
i		IBM CORP. GPD	000
111-11-66	DATE LAST EC	PeNe 2231396	1



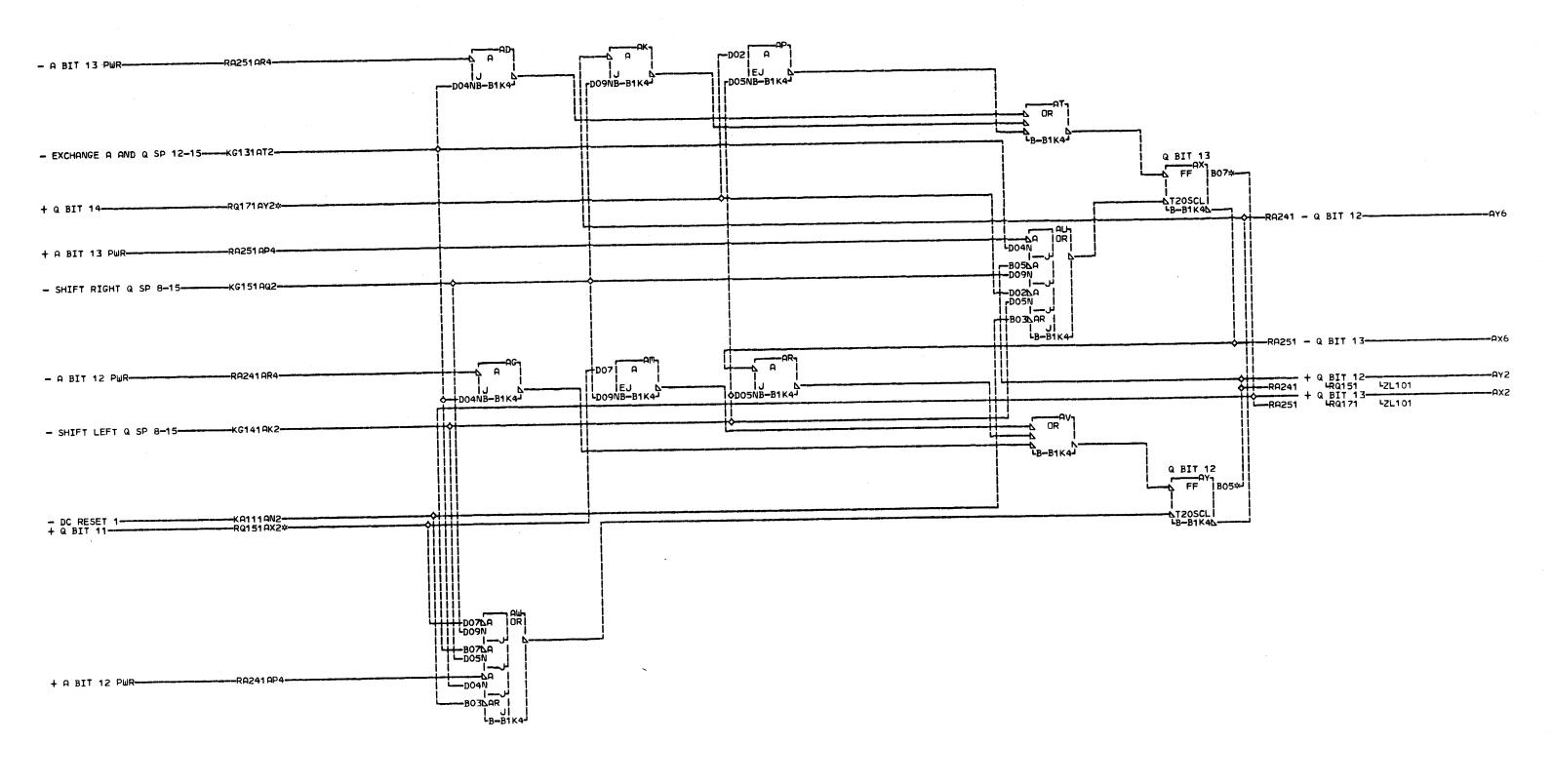
LOC. TYPE B-B1H4 6255

Q REGISTER BITS 8 AND 9		R
419631	FRAME 01	1 4
DATE LAST EC	IBM CORP. GPD	000
	P.N. 2231397	1

RQ131AX2
RESISTOR
Q B-B1H4D07
1 RQ151AY2
4 RESISTOR
B-B1H4D07
1 RQ151AY2
1 B-B1H4D02
AX2 B-B1N3D07
000 AY2 B-B1N3B07

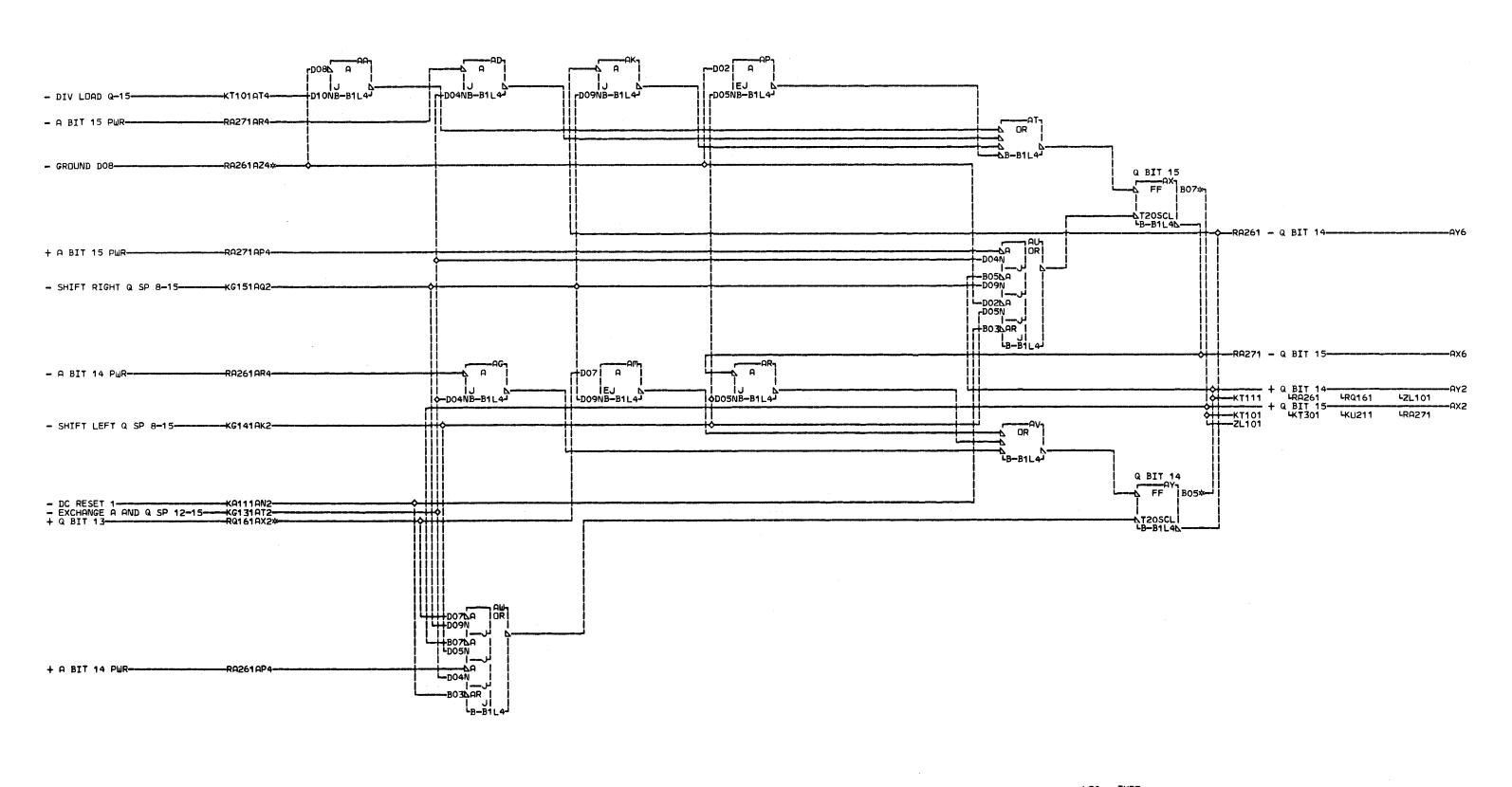


RQ141AX2 RESISTOR B-B1J4D07 RQ161AY2 RESISTOR B-B1J4D02 AX2 B-B1N3D09 AY2 B-B1N3B08

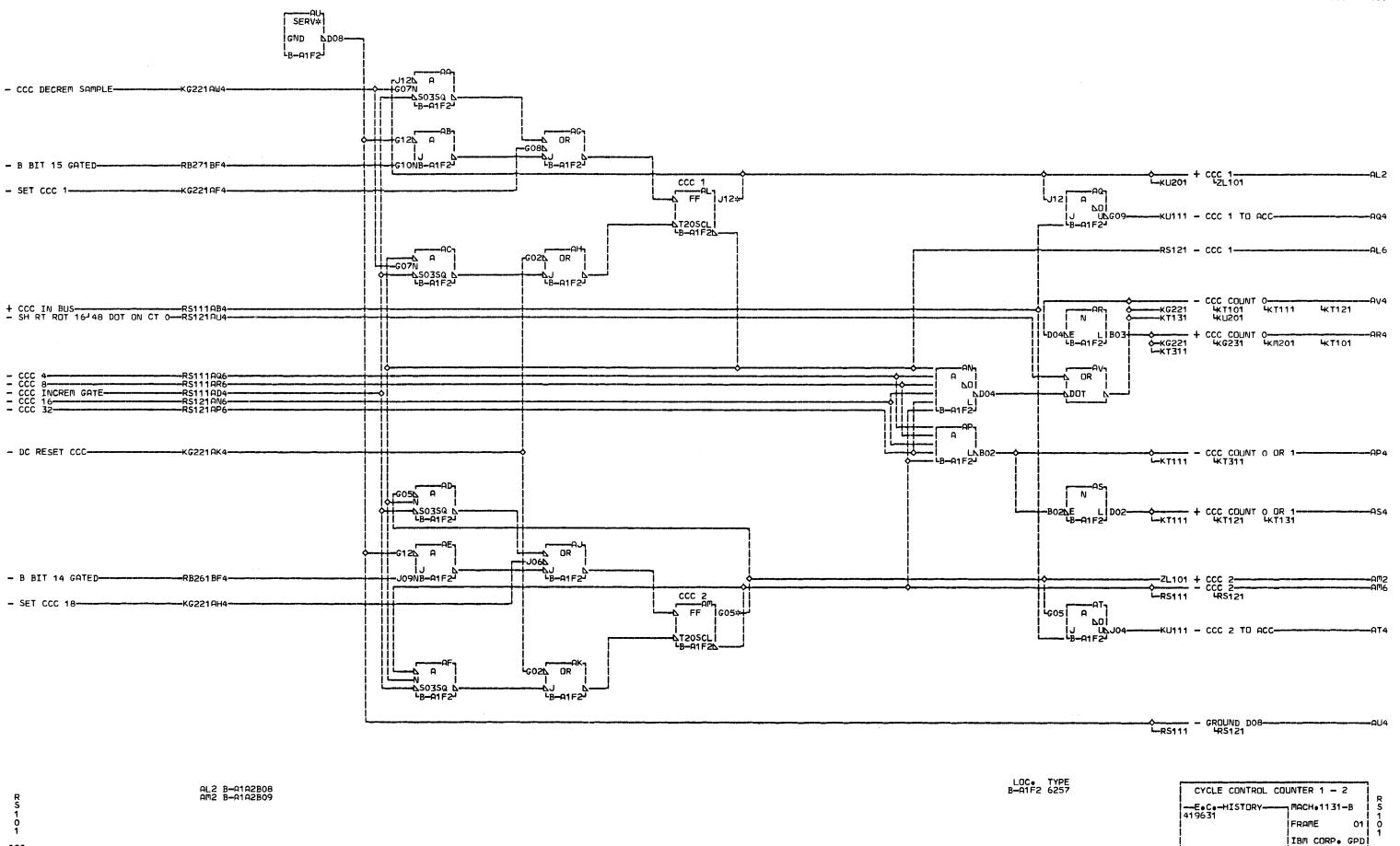


RQ151AX2
RESISTOR
B-B1K4D07
RQ171AY2
RESISTOR
RQ171AY2
RESISTOR
B-B1K4D02
RAZ B-B1N3D10
RX2 B-B1N3D10

LOC+ TYPE B-B1K4 6255	Q REGISTER BITS 12 AND 13E-C-HISTORY MACH-1131-B	
	419631 FRAME 01	6
n.	IBM CORP. GPD	, 20:
	I DATE LAST EC 111-11-66 419644 P.N. 2231399	,,,

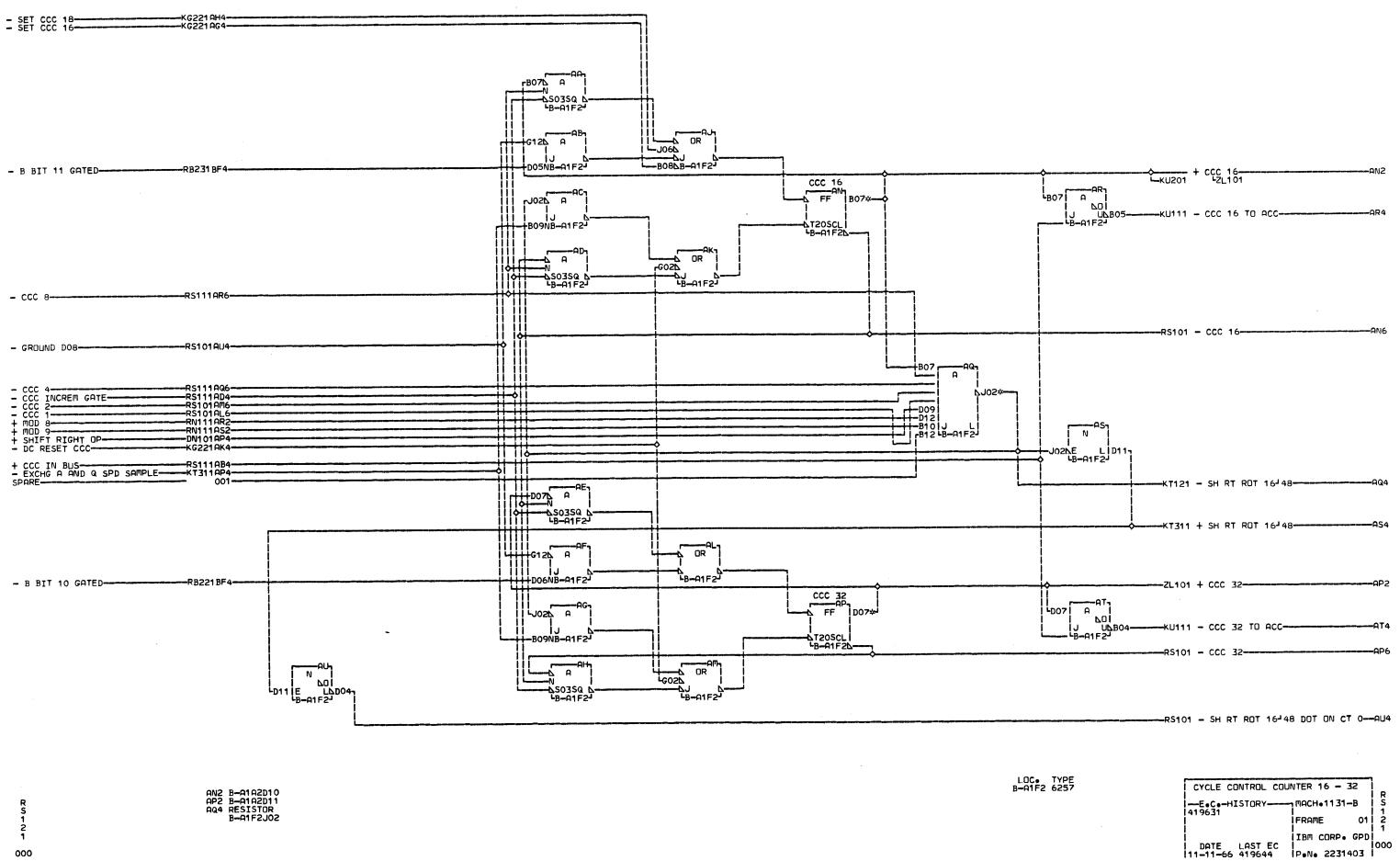


RA261AZ4 RESISTOR B-B1L4D02 RQ161AZ2 RESISTOR B-B1L4D07 AZ2 B-B1N3B10 01B-A1N5B09 01B-A1N5B09 01B-A1N5B09



DATE LAST EC | | 111-11-66 419644 | P.N. 2231401 |

KT301AZ4 RESISTOR B-A1F2J05 AQ2 B-A1A2B10 AR2 B-A1A2D09

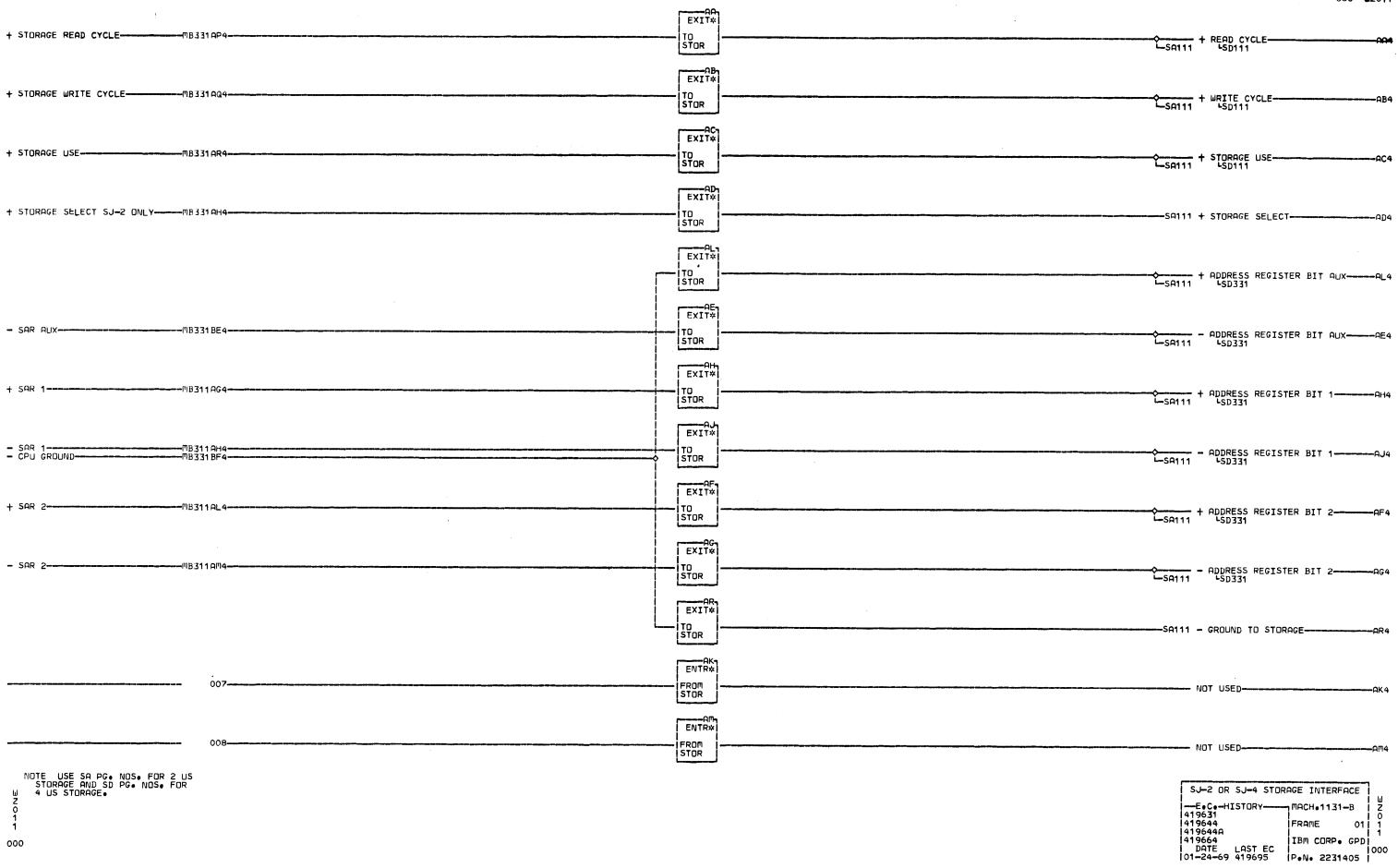


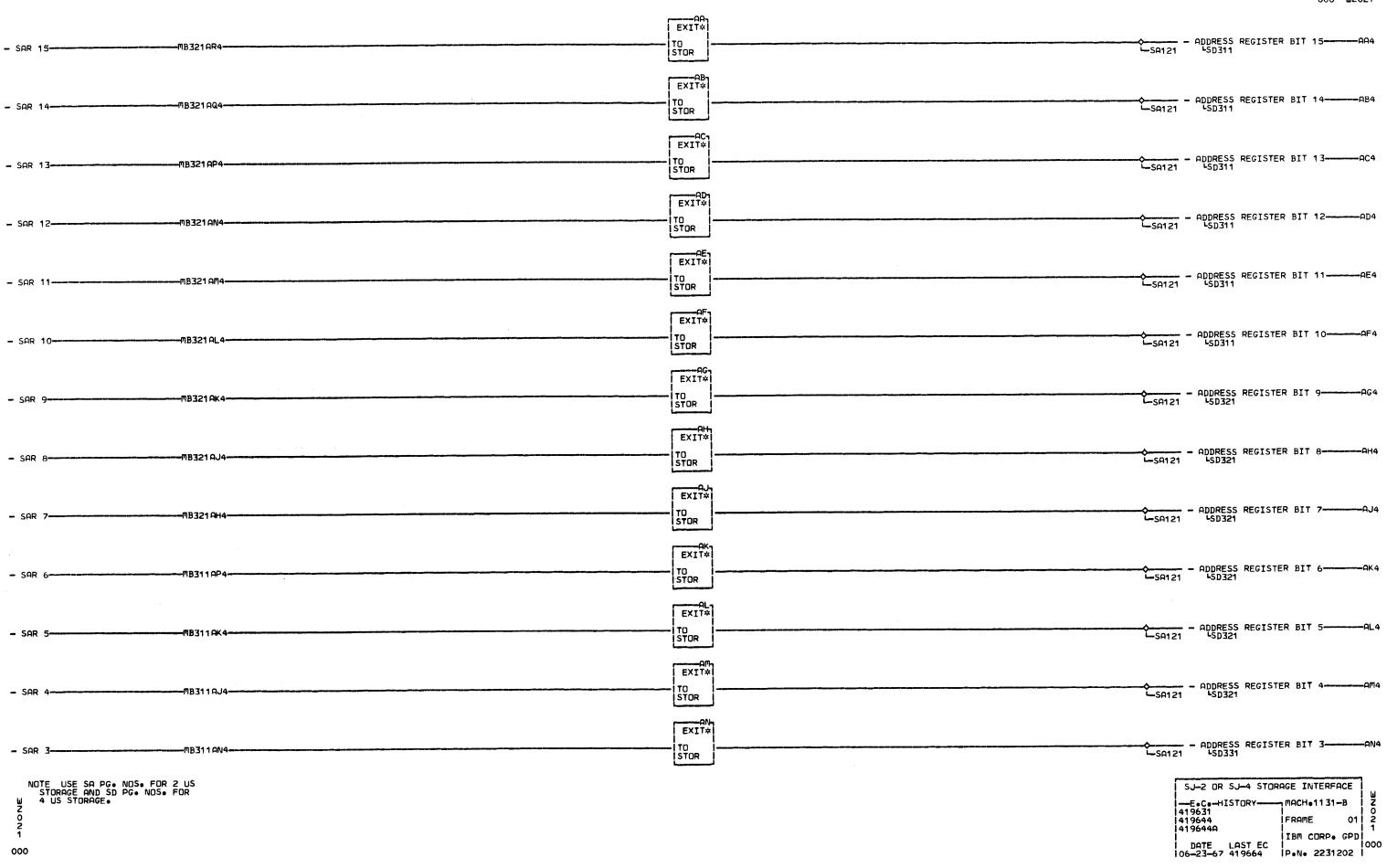
000 WA201

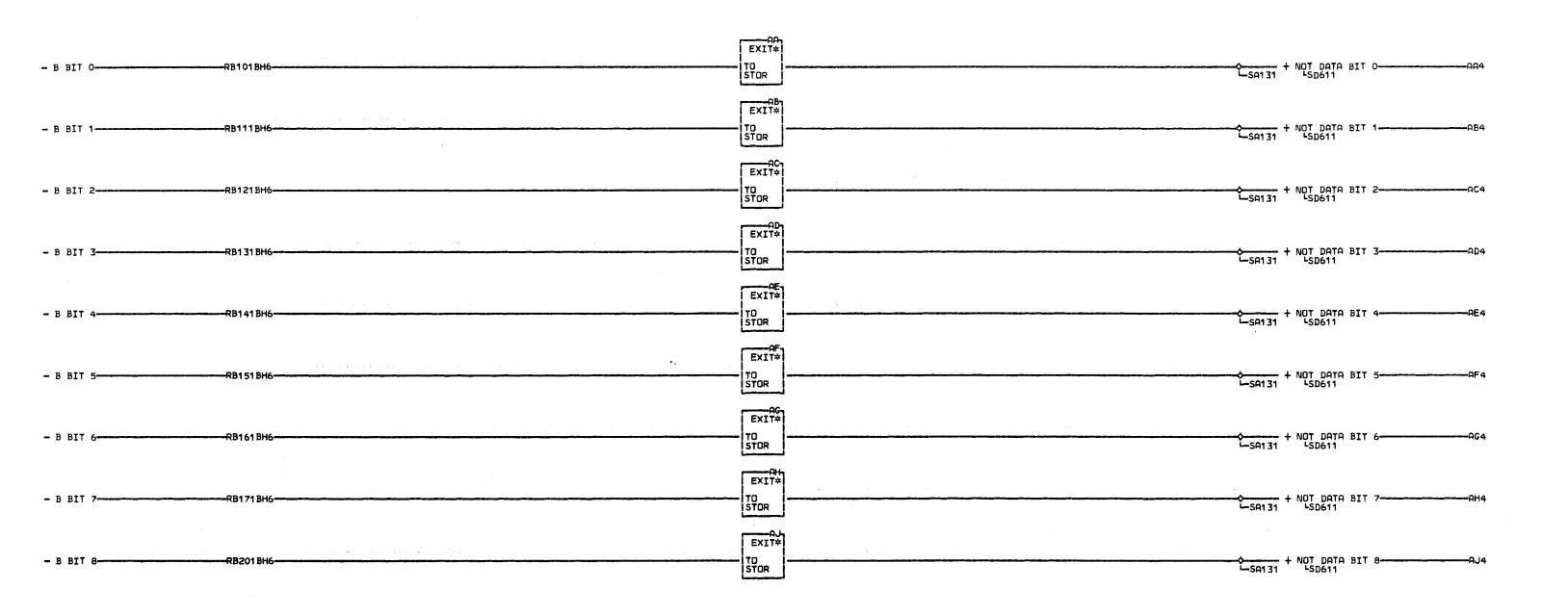
				000 w n201
- CPU CLOCK T-2	FA101AN4FA101AQ4	EXIT*		CPU CLOCK T-0
- CPU CLOCK T-6	FA101AR4————————————————————————————————————	TO CHAN		- OSCILLATOR PHASE AAA5 - CPU METER OUT
- XIO E-1	KW101AV4			
+ INTERRUPT LEVEL 2	FA111AV4	CHAN		+ INTERRUPT LVL 2
+ INTERRUPT LEVEL 5	FA121AW4 FA101AS4 FA101AT4 FA101AU4	- EXIT*		- + İNTERRUPT LVL 5
- CS CLOCK X-6	-FA101AV4	- 'cHan		CS CLOCK X-6
- CHAN DATA OUT BIT 0	FA111AV4	- FROM		- CHAN DATA OUT BIT 0
- CHAN DATA OUT BIT 5	-FA111AN4	- AE EXIT* - FROM		CHAN DATA OUT BIT 4
- CHAN DATA OUT BIT 10-	-FA121AK4	- ICHAN		- CHAN DATA OUT BIT 9AE6 - CHAN DATA OUT BIT 10AF1 - CHAN DATA OUT BIT 11
- CHAN DATA OUT BIT 12	-FA121AM4	- EXIT#		- CHAN DATA OUT BIT 12 - AF3 - CHAN DATA OUT BIT 13 - AF4 - CHAN DATA OUT BIT 14 - AF5 - CHAN DATA OUT BIT 15 - AF6
+ CHAN DATA IN BIT 0	- 001 - 002 - 003 - 003		CHANFA1	51 + CHAN DATA IN BIT 0
+ CHAN DATA IN BIT 3	- 004- - 005- - 006-		.LĊPUJ	51 + CHAN DATA IN BIT 4AG6 51 + CHAN DATA IN BIT 5AH1
+ CHAN DATA IN BIT 6	- 007- - 008- - 009- - 010- - 011-		ito i———————————————————————————————————	51 + CHAN DATA IN BIT 7
+ CHAN DATA IN BIT 11	- 012		CHAN FA1	61 + CHAN DATA IN BIT 12AJ2 61 + CHAN DATA IN BIT 13AJ3 61 + CHAN DATA IN BIT 14AJ4
+ CHAN DATA IN BIT 15-	016		. icPu	61 + CHAN DATA IN BIT 15AJ5 01 - METER INAJ6 01 - ADVANCE ID ENTRY
- ADVANCE ID ENTRY	- 018- - 019- - 020- - 021- - 022- - 023-		ENTR#	81 - CHÂN CÝCLE STEAL REQ
- INTERRUPT REQ LVL 4	- 024		ENTR* — FA1 FROM — FA1 CHAN — FA1	81 - INTERRUPT REQ LVL 4
+ CHAN ADDR BIT 2 + CHAN ADDR BIT 3	- 028 - 029 - 030			31 + CHAN ADDR BIT 3
+ CHAN ADDR BIT 4	031 - 032 - 033 - 034 - 035		ENTR*	31 + CHAN ADDR BIT 5
+ CHAN ADDR BIT 10	036		- FROMFA1	41 + CHAN ADDR BIT 11
+ CHAN ADDR BIT 14 + CHAN ADDR BIT 15 - NOTE WHEN THE 1133 IS	100			41 + CHAN ADDR BIT 15AN6
ATTACHED THE 1133 IS ATTACHED THE ABOVE SIGNAL W WILL BE SHOWN ON PAGE WAS A OF THE 1133 LOGICS.				STORAGE ACCESS CHANNEL INTERFACE UE • C • -HISTORY MACH • 11 31 -B A 2 FROMF 01 0

		ENTR*		JRITE CLOCK PHASE A
WRITE CLOCK PHASE A	XA061 AB2	IFROM		INTIE CLOCK PRINSE H
WARE TO BE SEED BURGER B	V007 4 0 D 6	FILE TO		
WRITE CLOCK PHASE B-	XA061 AB6	icpu		WRITE CLOCK PHASE B
		г АВ-	—XF141	- XF161
- READ DATA	XA061AA2XA061AA2	I ENTR*	XF141 = F	READ DATA—————————————————————————————————
		FROM FILE		
- READ CLOCK-	XA061AA6	TO CPU	YE141 _ U	READ CLOCK-AB6
			A() 1 1 1	READ CEOCK———————————————————————————————————
L GOOTES DEODY	V00640D2	ENTR*	xXF171 +	ACCESS READY————————————————————————————————————
+ ACCESS READY	XA061AD2	IFROM		-
- SECTOR PULSE-	XA061AE2	FILE TO		
- SECTOR FOESE	A CONTRACTOR	rCbn	「	EIGHT SECTOR PULSESAC6
		AD-		
- REFERENCE PULSE-	XA061AD6XA061AD6	FROM		INDEX PULSEAD2
		FILE	,	
- FILE READY-	XA061AC6	LCPU	 	FILE READYAD6
		AE-	►xF131	₩F191
- HOME	xa0619E6	ENTR*	xxF171 +	CARRIAGE HOME SW NJOAE2
		FROM FILE	 	90 SEC TIME DELAYAE4
- 90 SEC TIME DELAY-	XA061AE7XA061AE7	ito		
- WRITE SELECT ERROR-	XA061AC2	L CPU	XF201 -	WRITE SELECT ERROR——————————————————————————————————
- HEAD SELECT-	xF191AL2	EXIT*		U HEAD SELECT - HD 1AF4
		TO FILE		
		AG		
+ C E INTERLOCK	XF191AQ4	EXIT*	XA061 +	CE INTERLOCKAG2
		FROM CPU		
- READ SELECT-	XF141BF4		 xending	CPU READ SELECT-AG6
- WRITE DATA GATE-	XF161AR4		XR061 =	WRITE DATAAH2
- WRITE DATA GATE	· ····································	FROM ICPU		
- ACCESS DIRECTION	XF191AS4	ITO		
		4FILE	7XR061 CP	PU DIRECTION + FORAH6
		<u></u> -0,1	1	ACCESS GDAJ2
- ACCESS DRIVE-	XF171BB4	FROM ICPU		HCCESS GO
L USBS COUNTED BET AS	VEGALOR	CPU TO		
+ WORD COUNTER BIT 15	XF201AG2	FILE-	JXA061 10	20 MIL STEP - 10 MIL-AJ6
- WRITE SELECT POWER SAFE	XF201BH4	EXIT*	XA061 -	CPU CLOCK GATE-AK2
		I IFRUM		
		L LTO FILE-		CPU WRITE GATE-AK6
		7 16		

QA2 A-C1N7D05 AE4 A-C1N7D12
AA6 A-C1N7B05 AE6 A-C1N7B08
BE AB2 A-C1N7B03
AB6 A-C1N7D02
AC2 A-C1N7B07
AC6 A-C1N7B06
AD2 A-C1N7B12
AD6 A-C1N7D13
AE2 A-C1N7B02

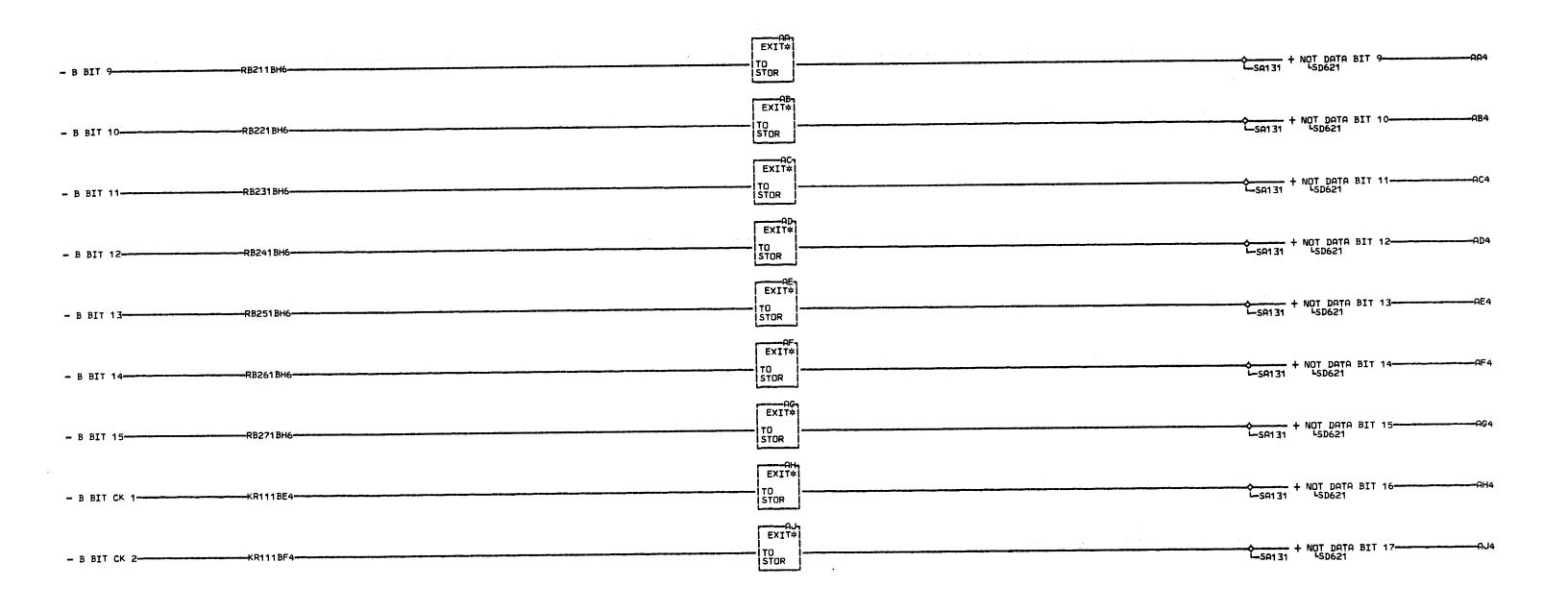






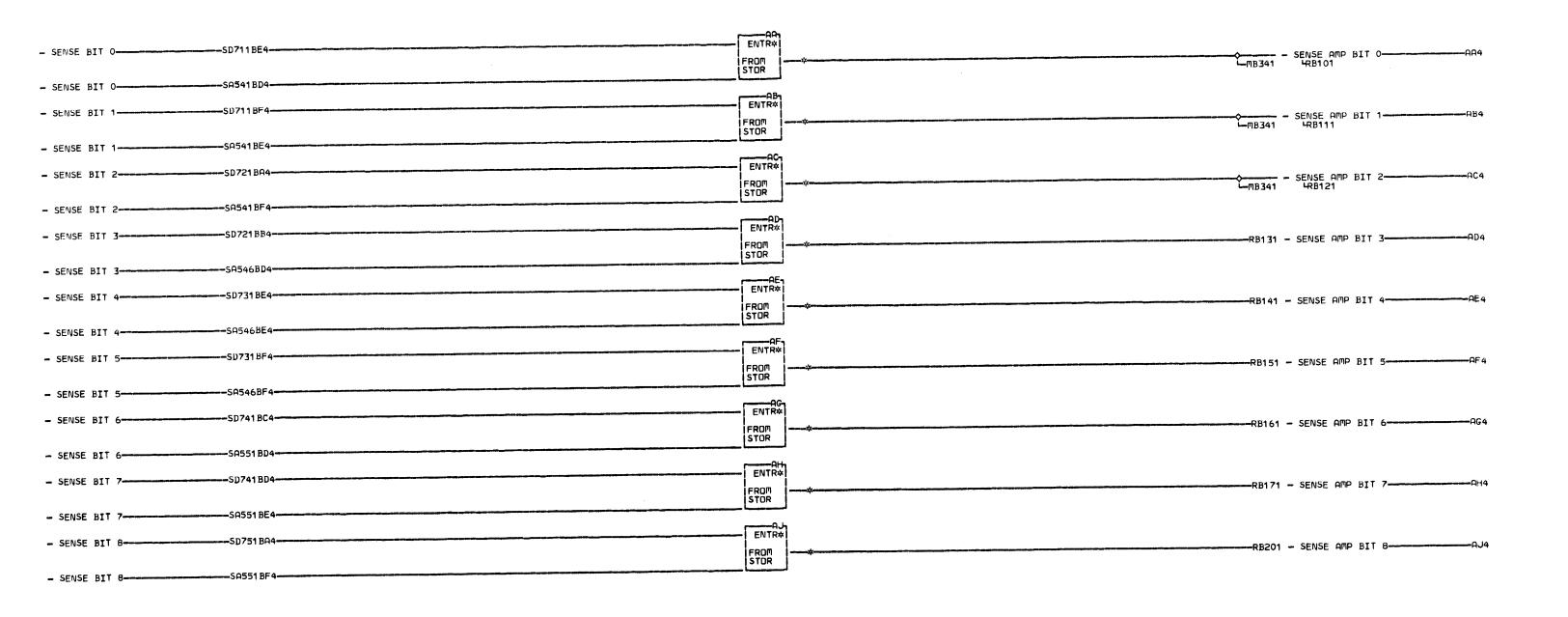
NOTE USE SA PG. NOS. FOR 2 US STORAGE AND SD PG. NOS. FOR W 4 US STORAGE. Z O O 3 1

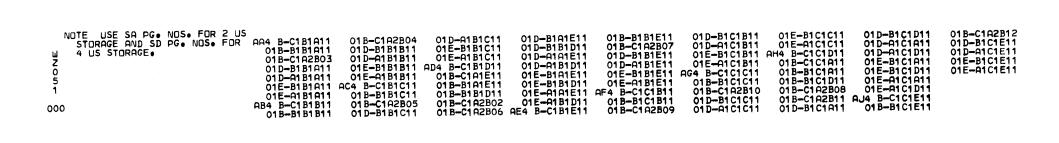
000



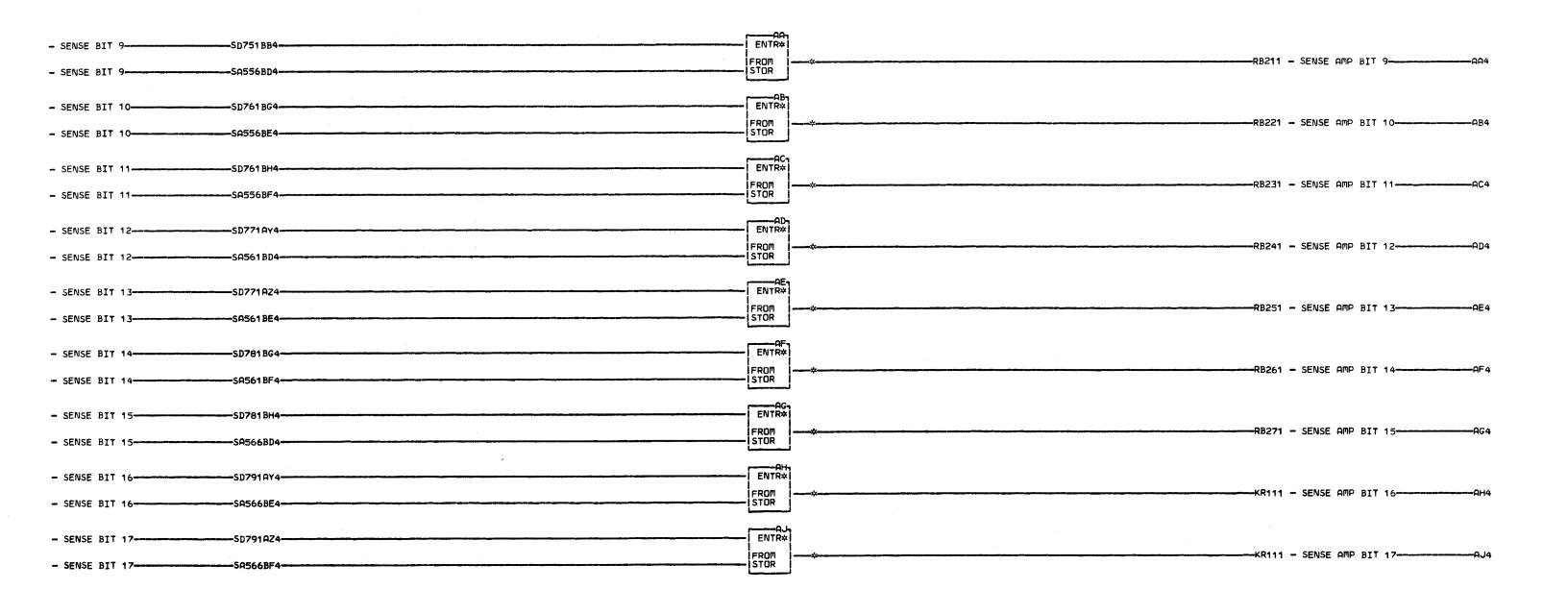
NOTE USE SA PG. NOS. FOR 2 US STORAGE AND SD PG. NOS. FOR 4 US STORAGE. 2 0 4 1

SJ-2 OR SJ-4 STORA	GE INTERFACE	
-E.CHISTORY-	MACH+1131-B	W Z O
	FRAME 01	4
	IBM CORP. GPD	
DATE LAST EC 06-23-67 419664	P.N. 2231407	1000



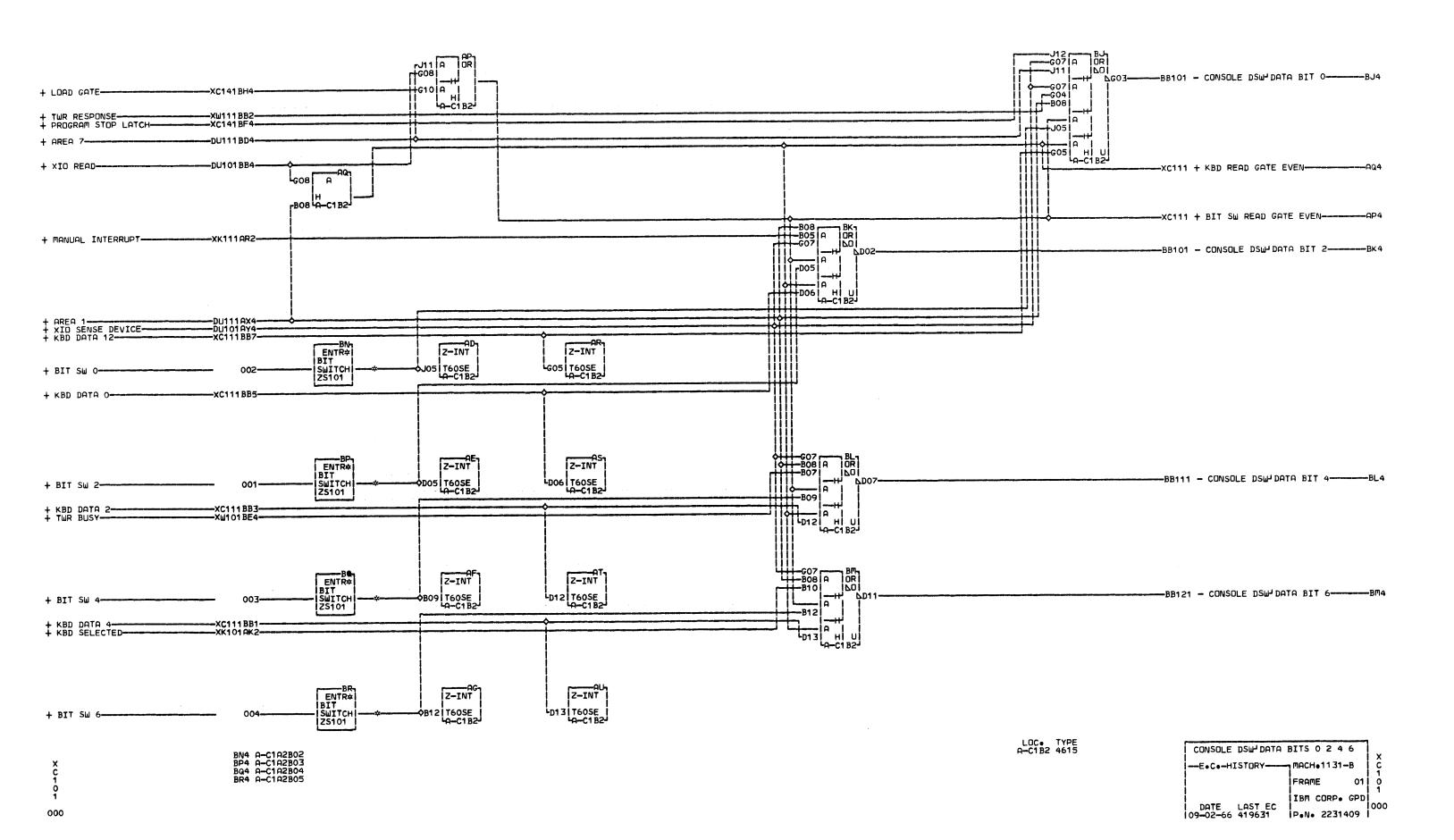


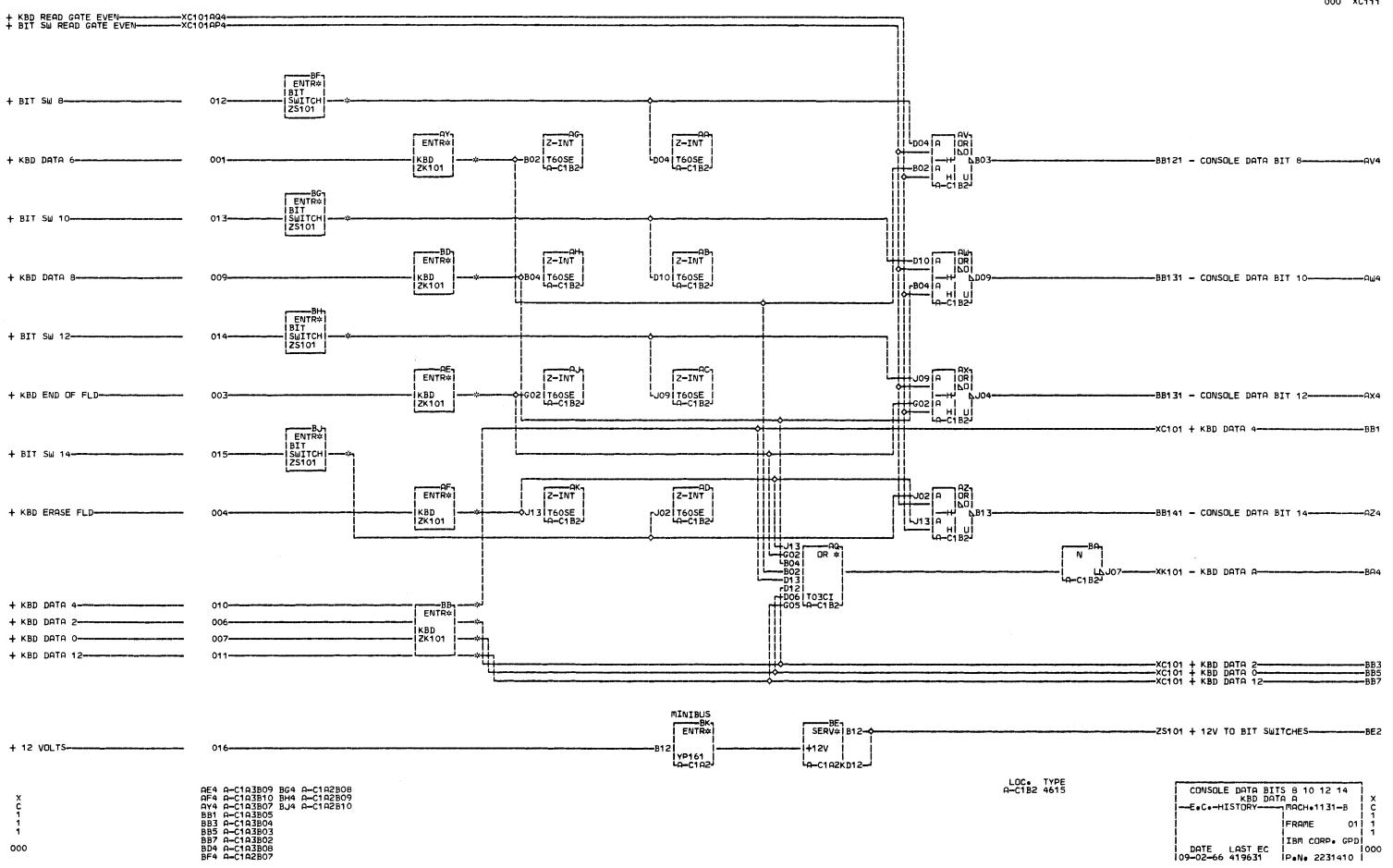
SJ-2 OR SJ-4 STORE	AGE INTERFACE	 W Z
E.CHISTORY		
419644 4196449	FRAME 01	0 5 1
1419664	IBM CORP. GPD	000
DATE LAST EC 101-24-69 419695	P.N. 2231408	1

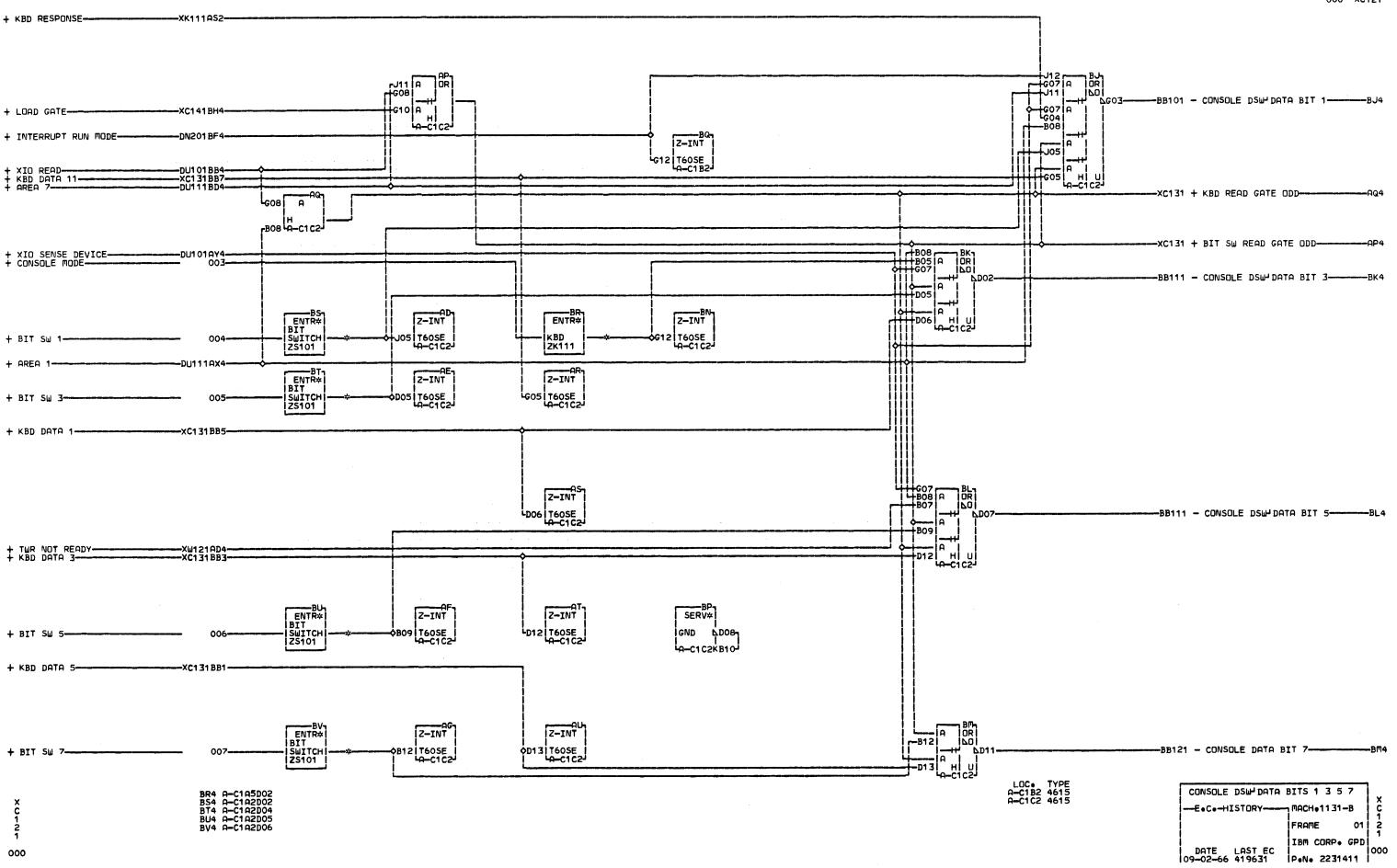


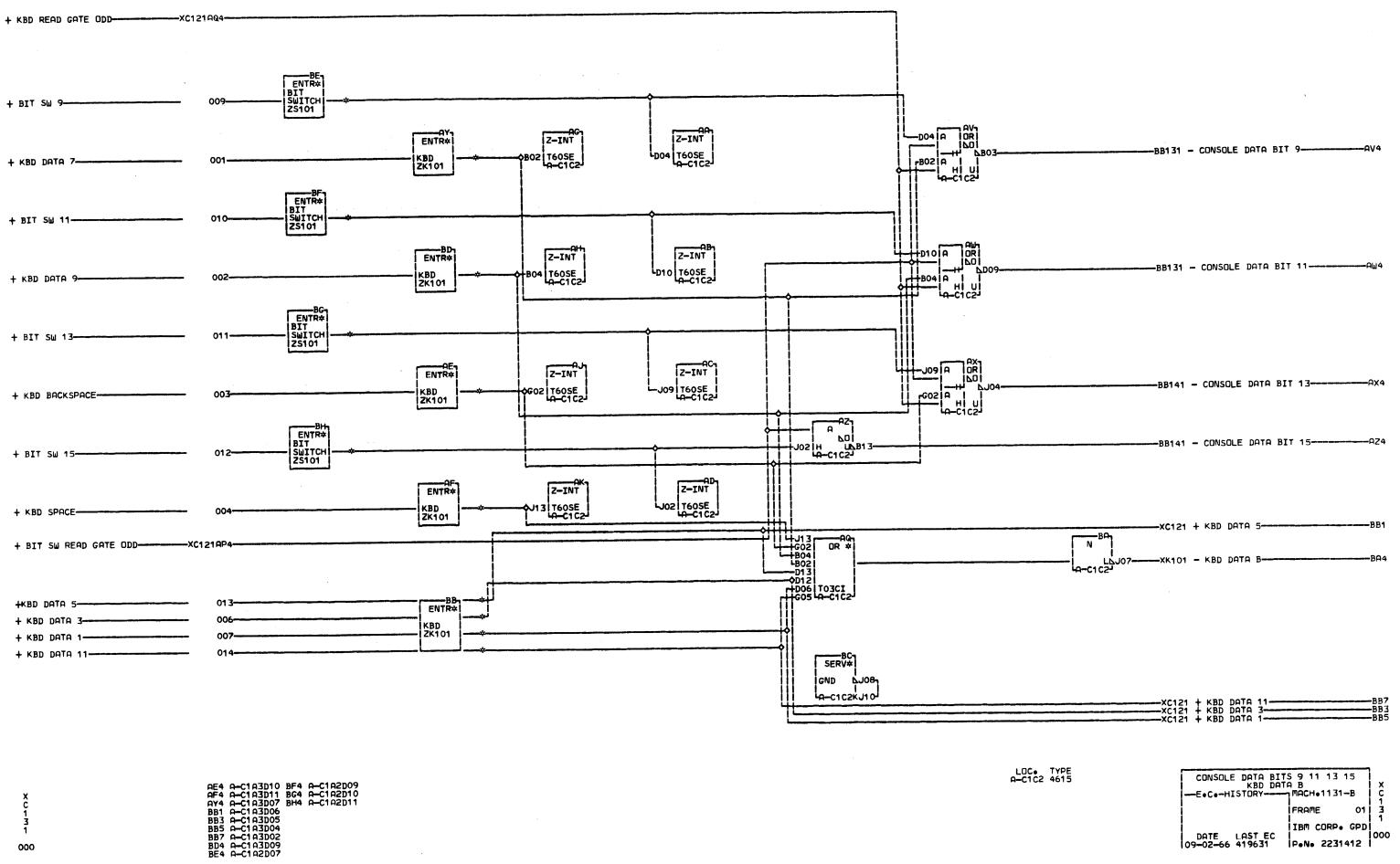


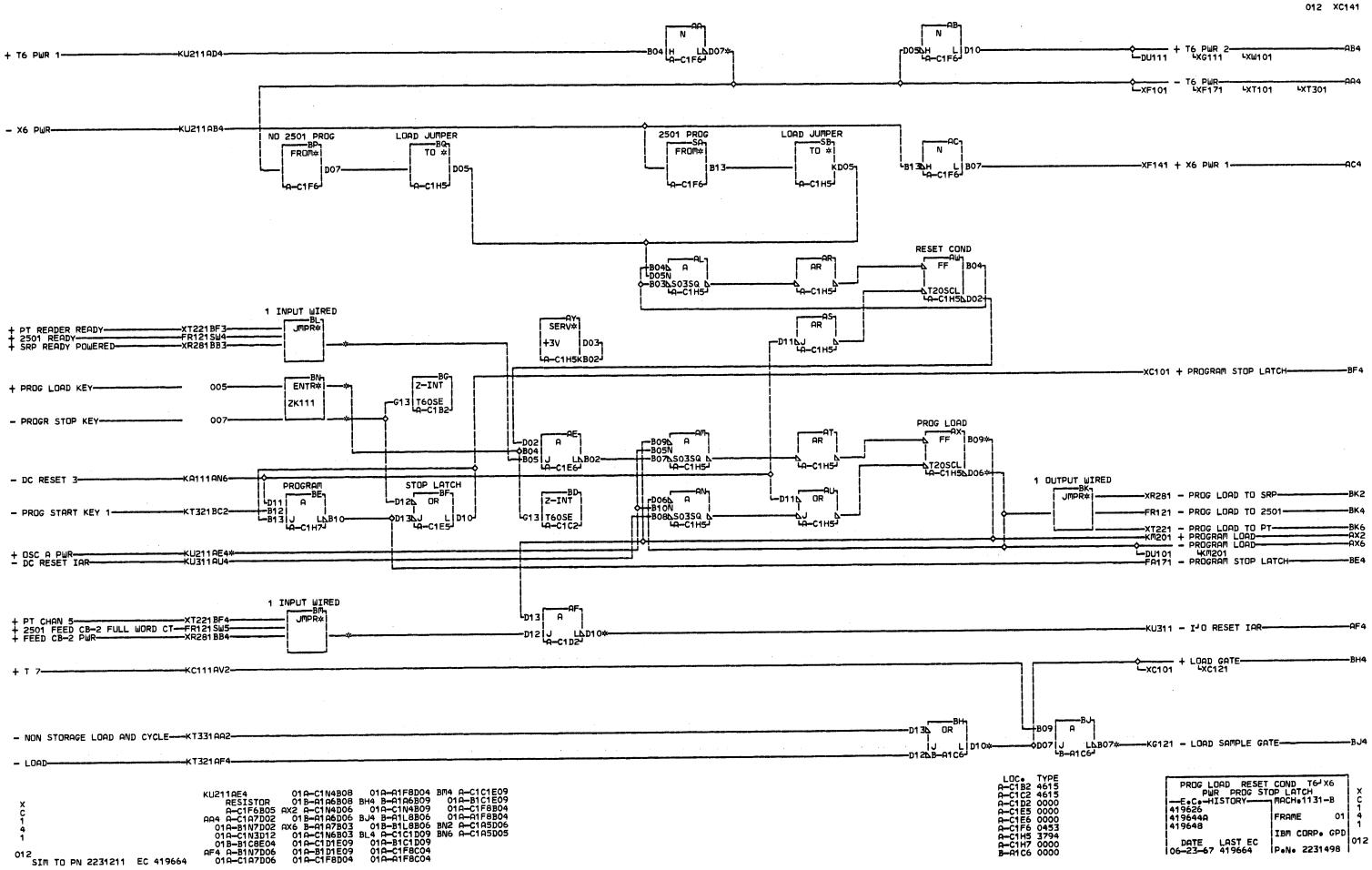
SJ-2 OR SJ-4 STOR	
E.CHISTORY	MACH-1131-B Z
1419644 1419644B	FRAME 01 6
1	IBM CORP. GPD
DATE LAST EC	P.N. 2231206



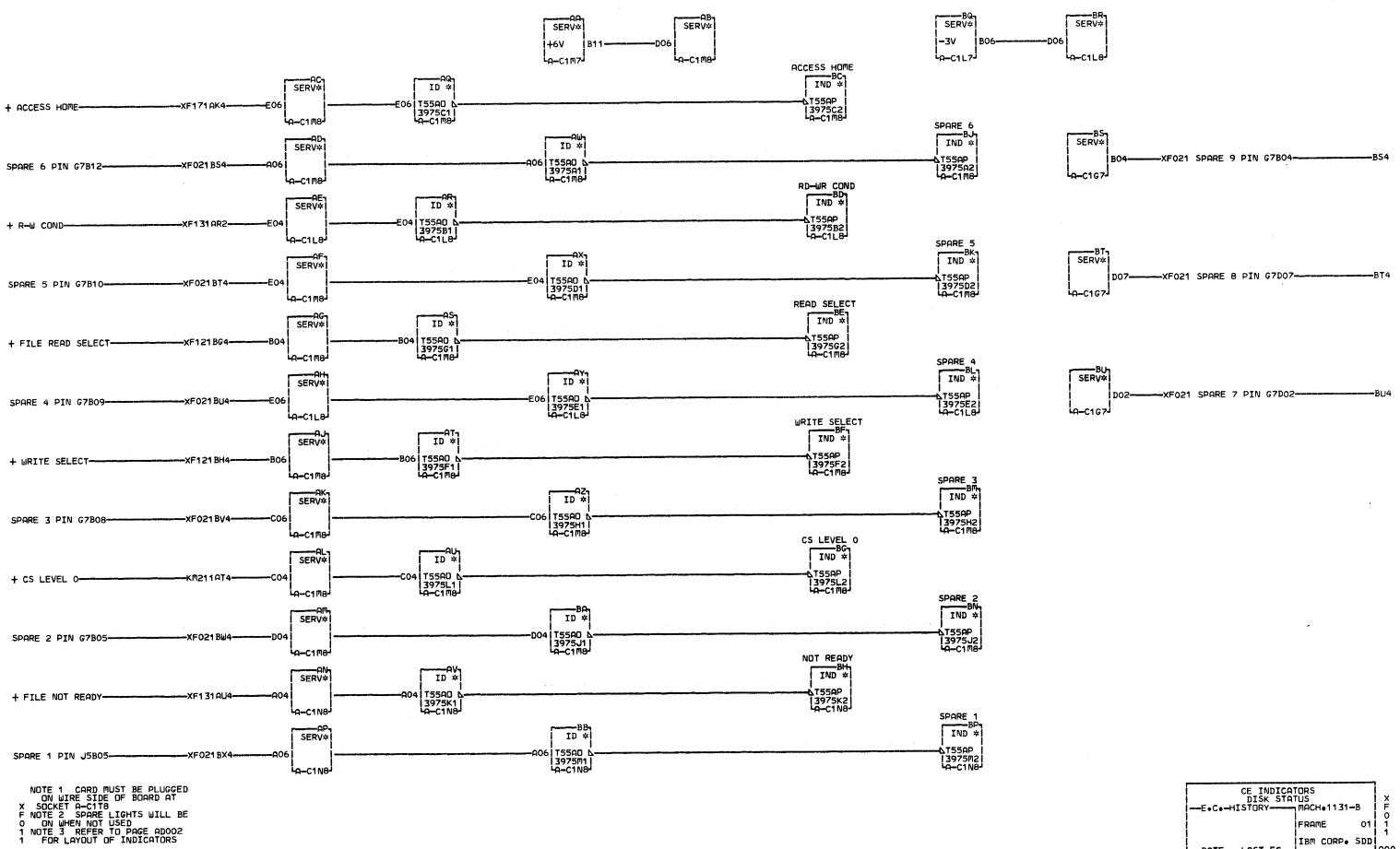


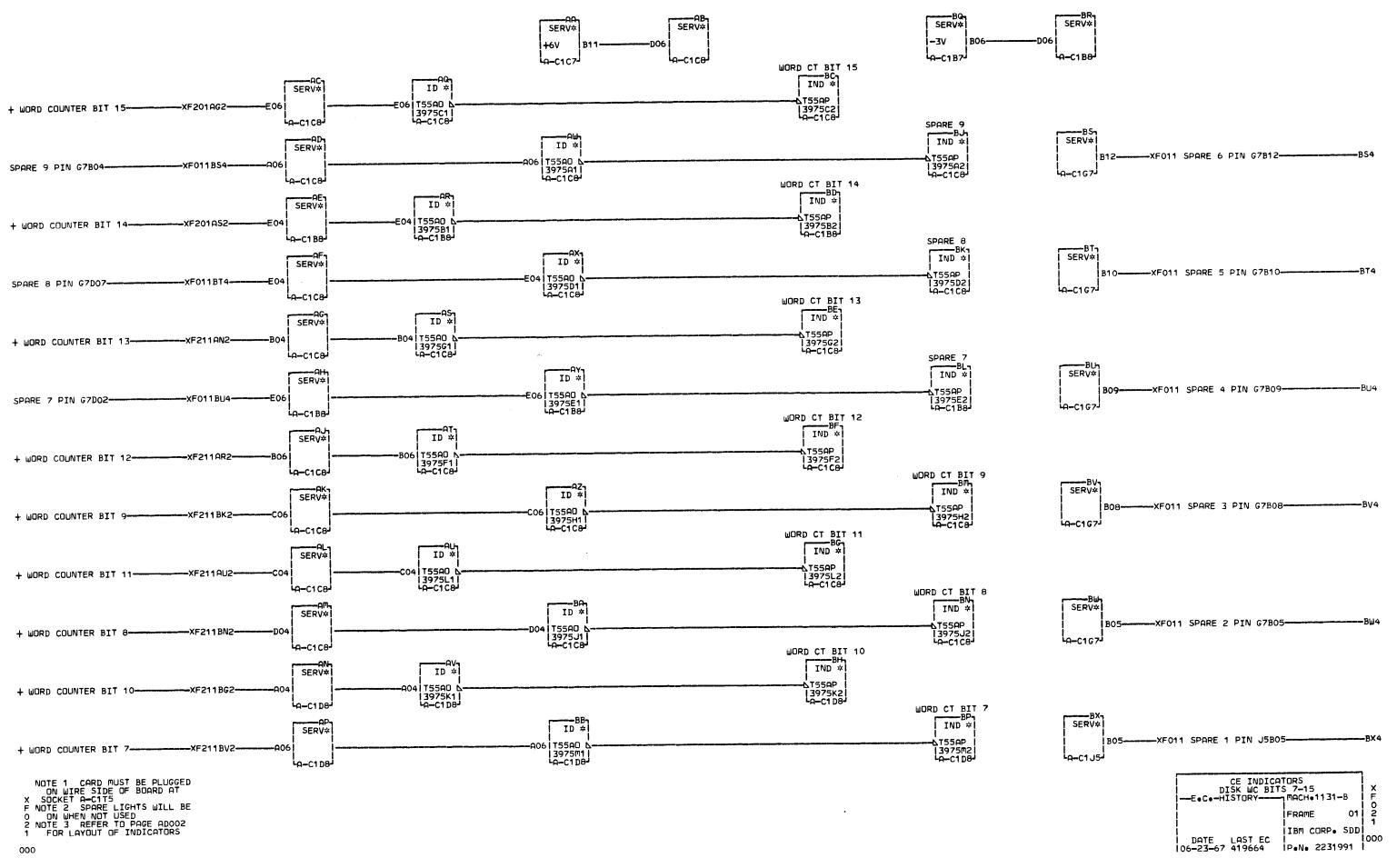


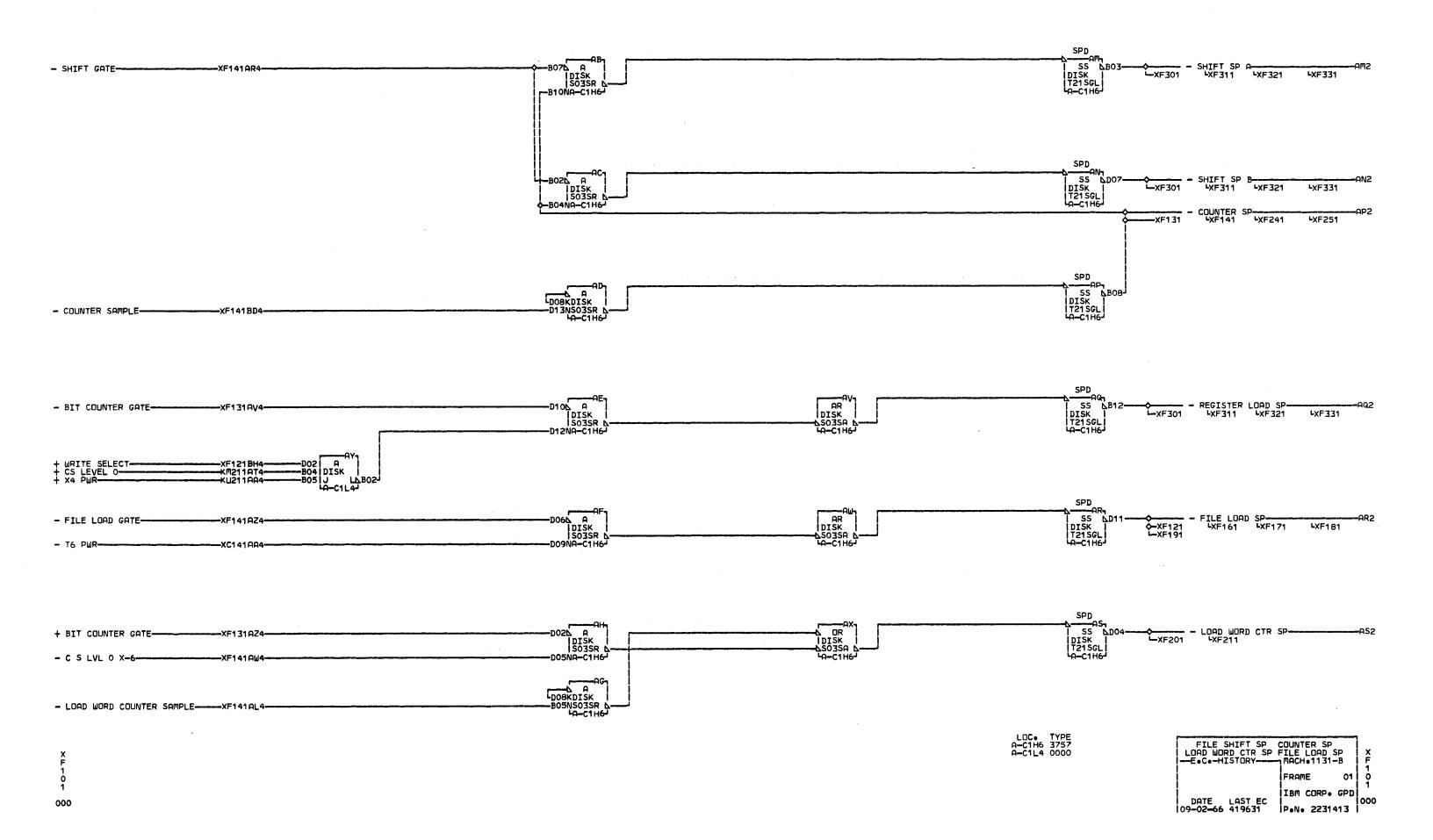


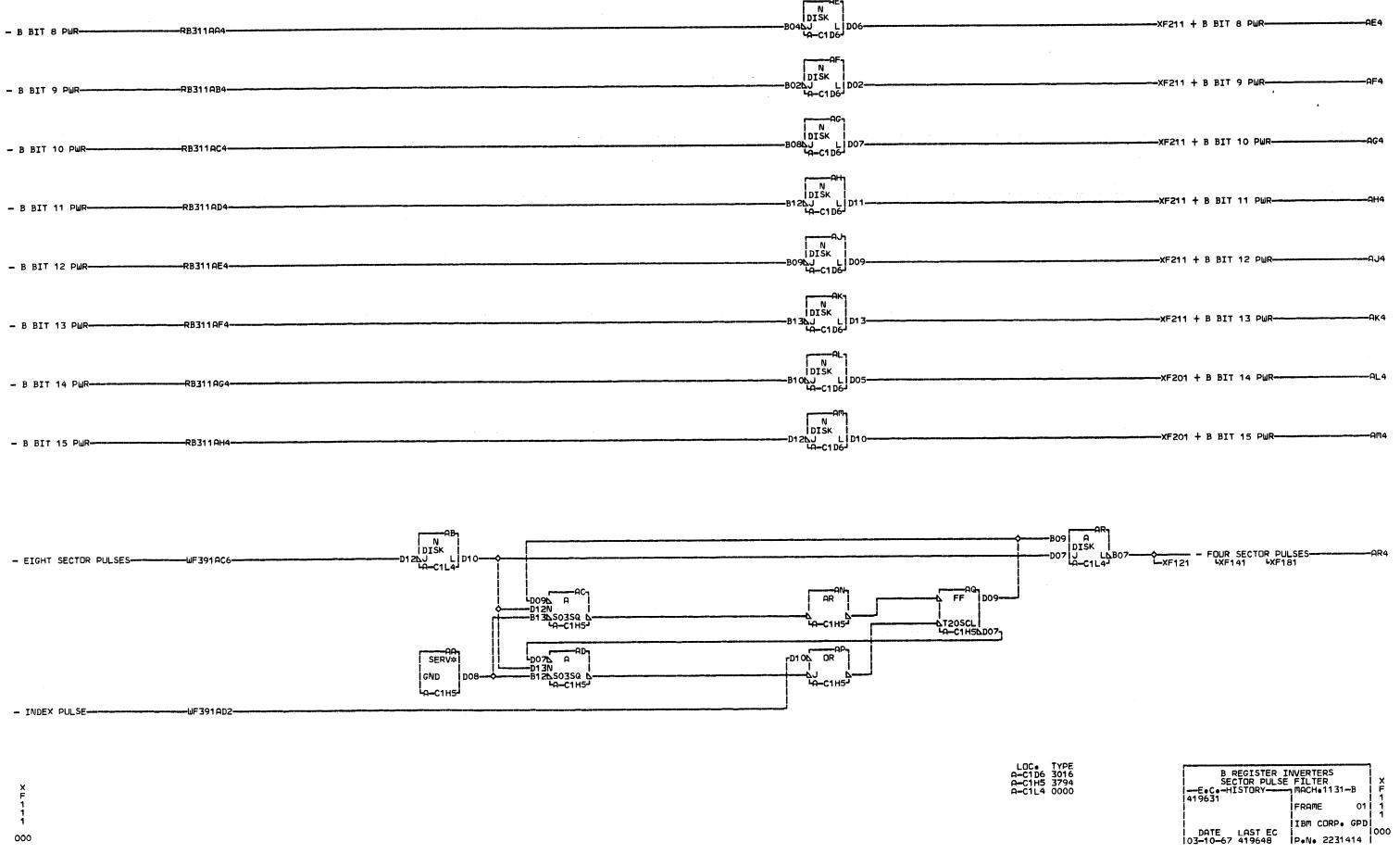


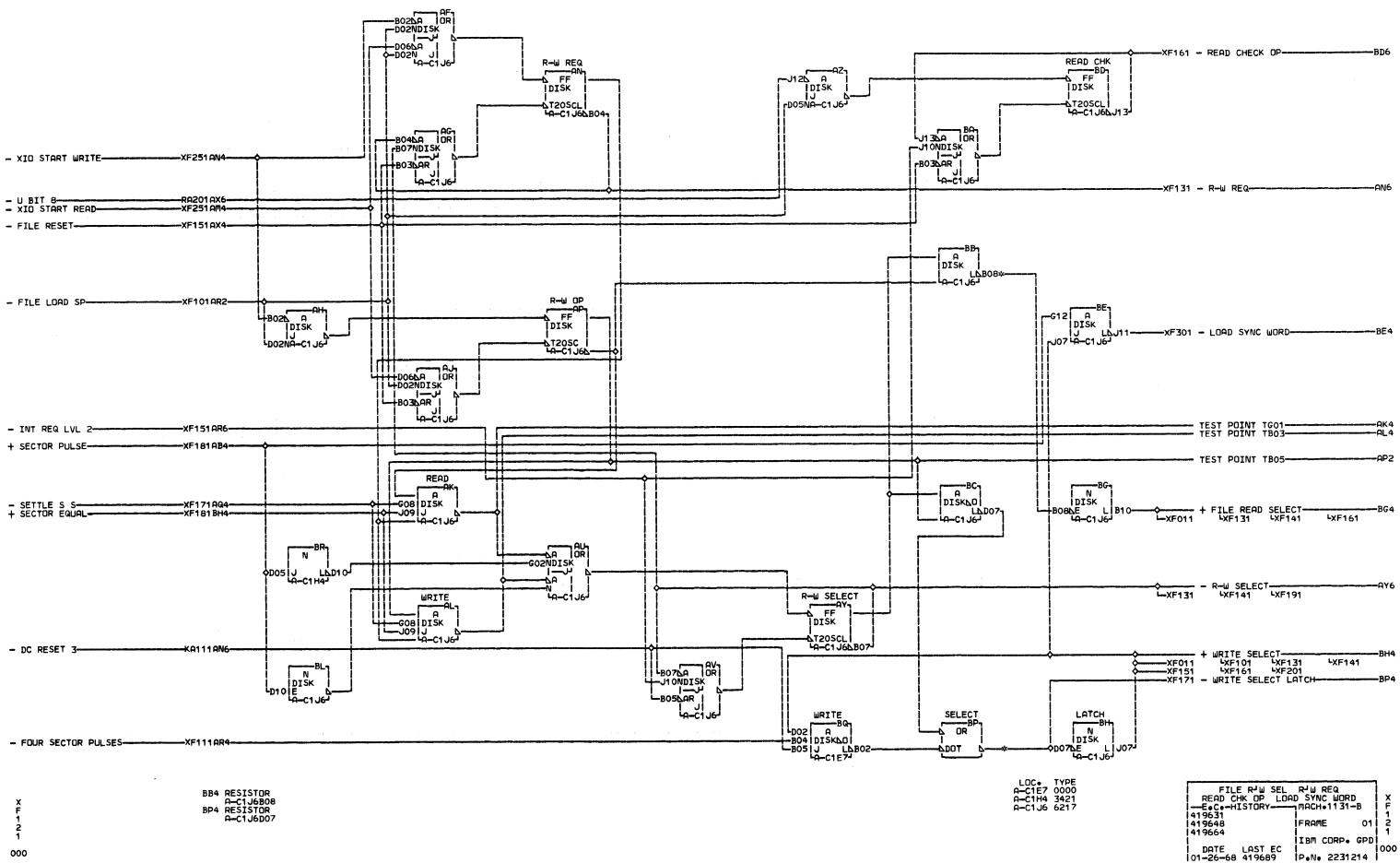
P.N. 2231990 i







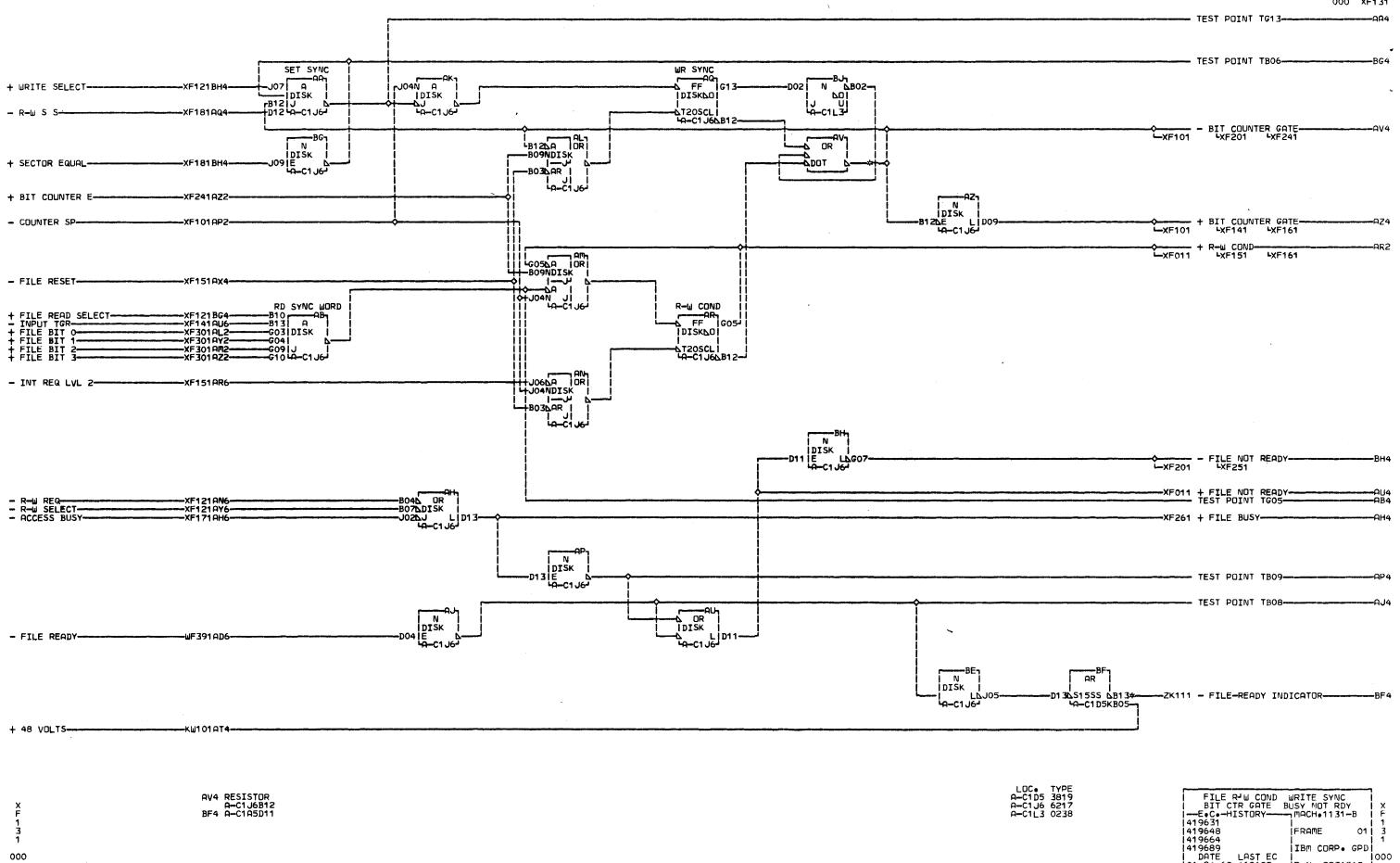


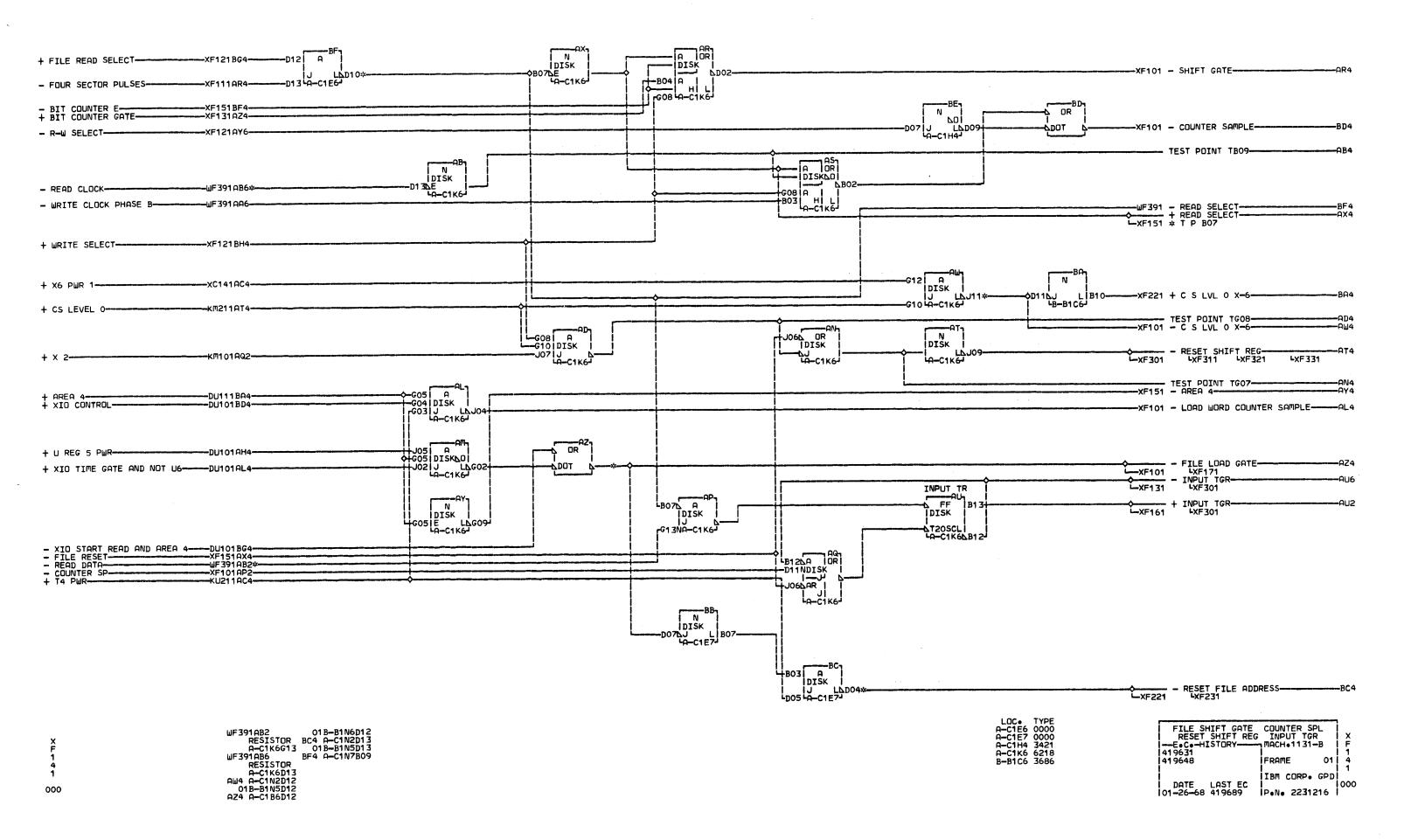


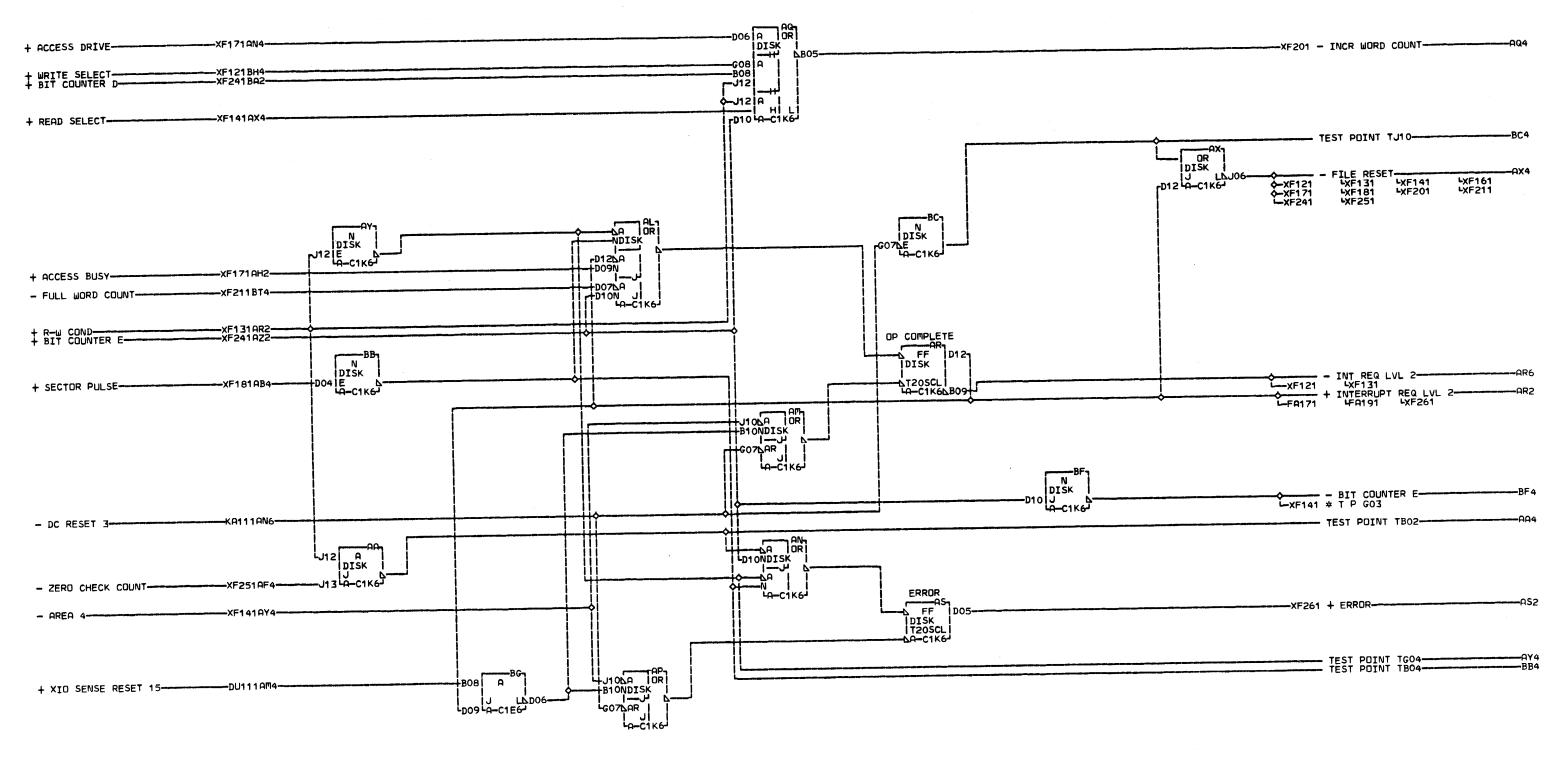


|P•N• 2231215 | 000

| DATE LAST EC | | 101-24-69 419695 |

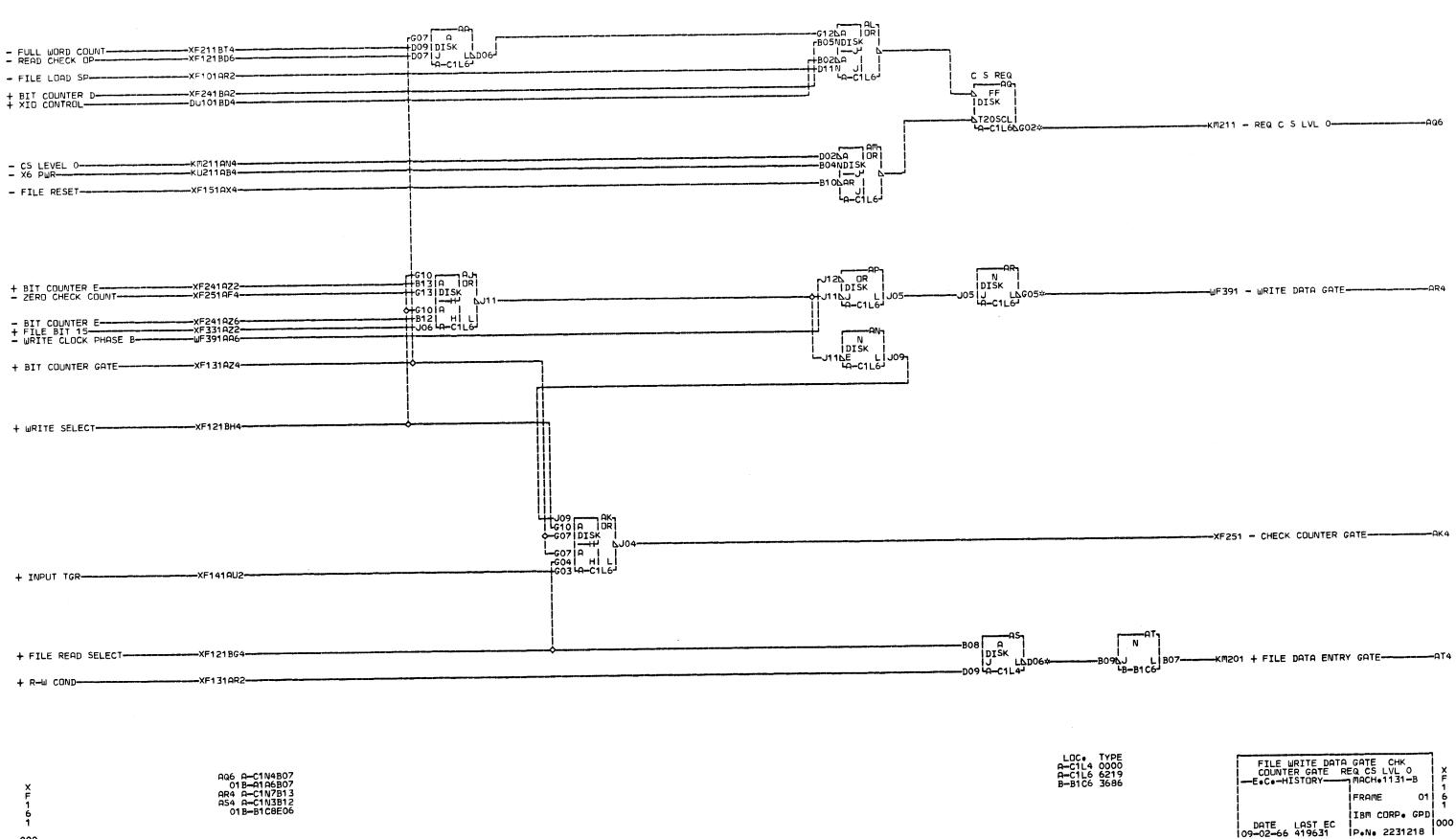


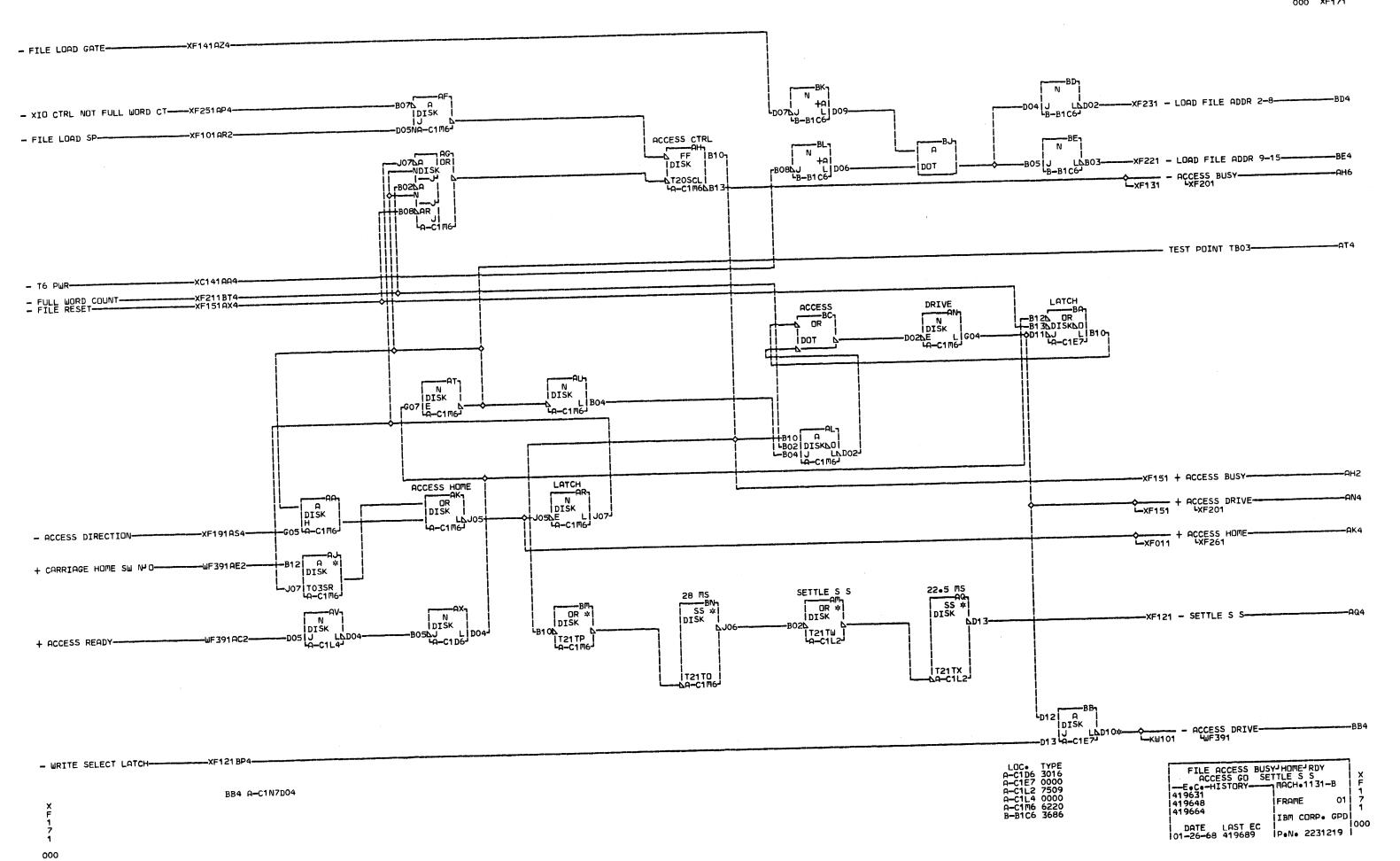


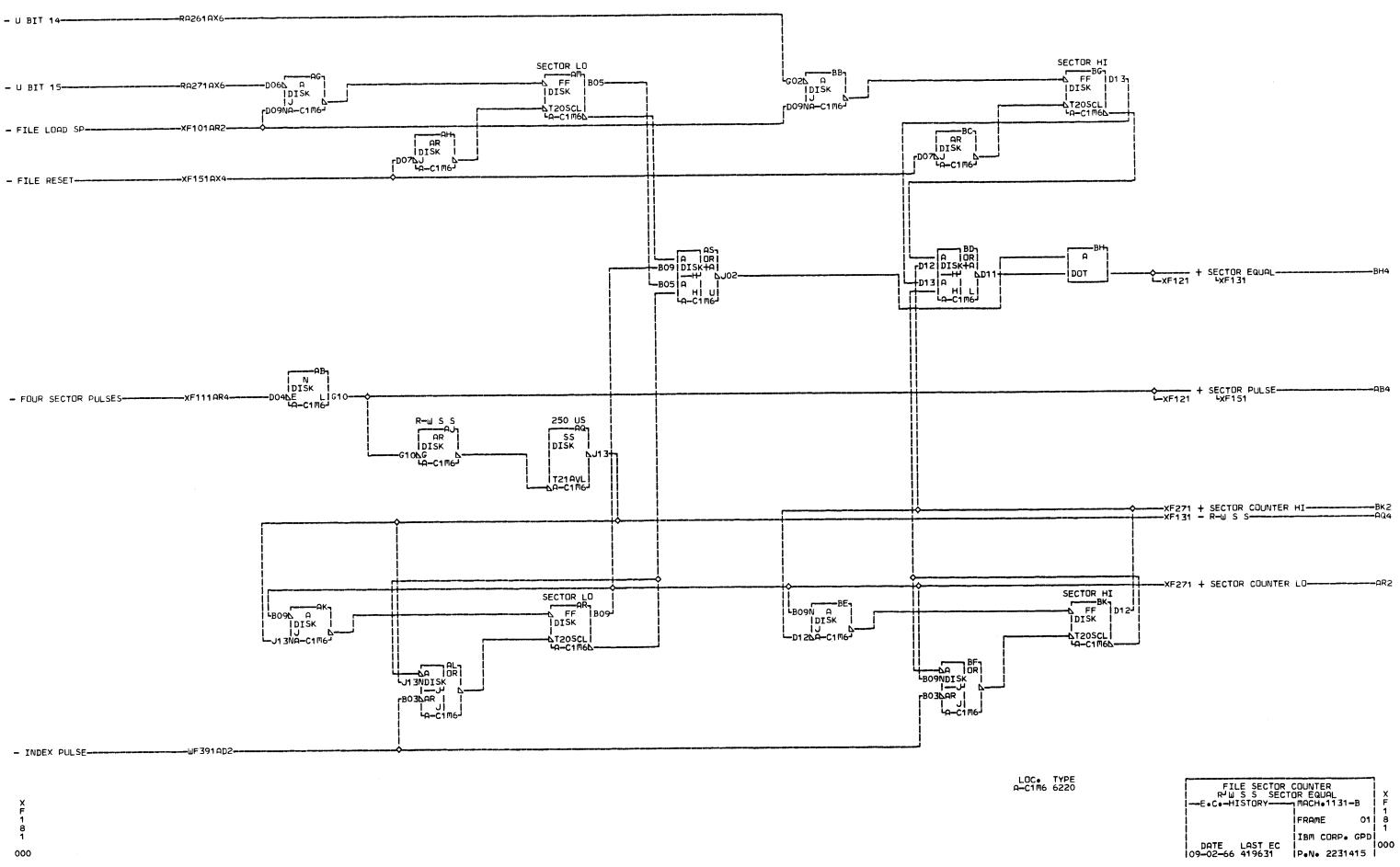


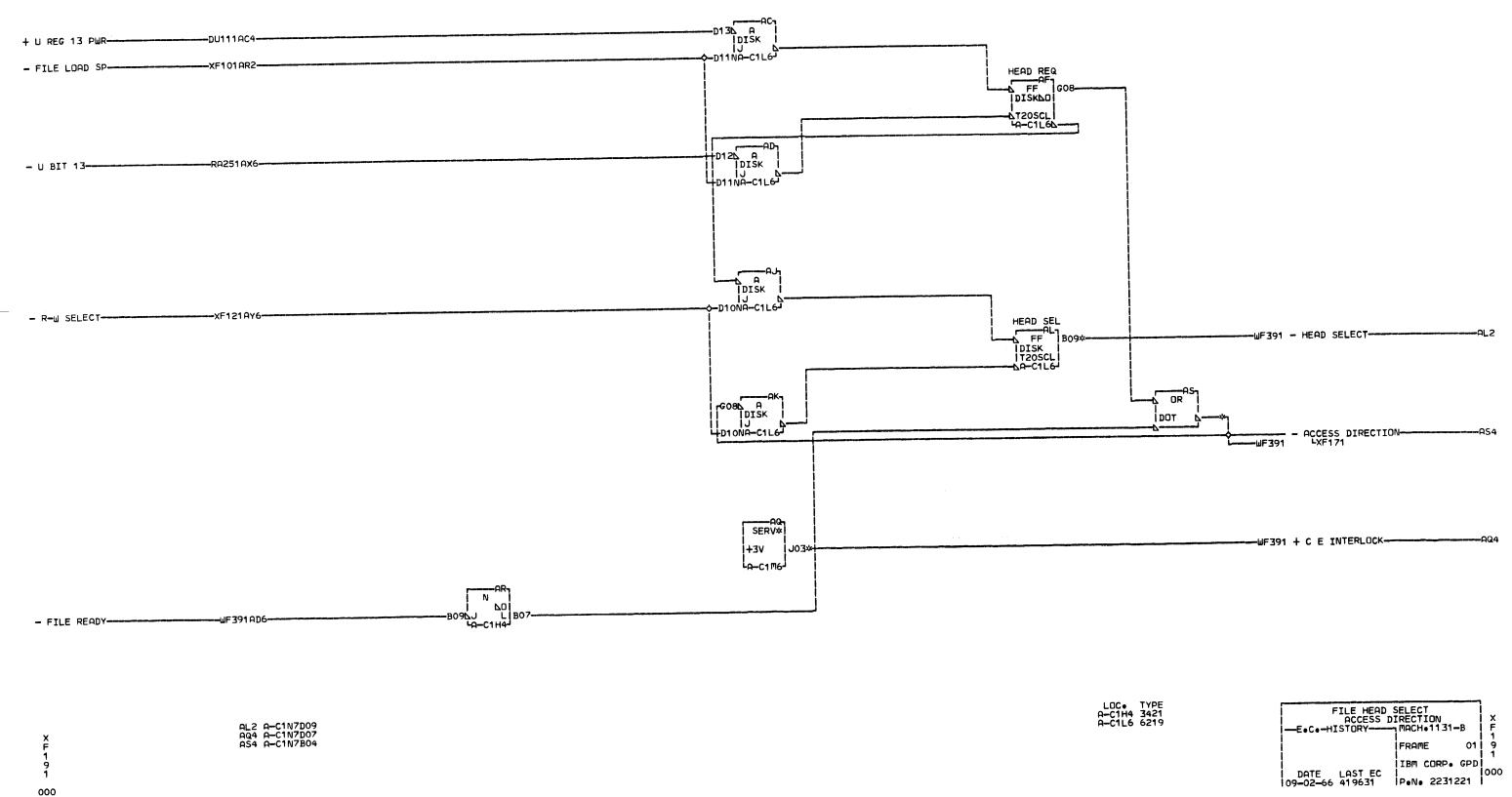
FILE ERROR INT REQ LVL 2
FILE RESET INCR WORD COUNT
FRAME
O1 5
1
IBM CORP. GPD
O00
O1-26-68 419689
P.N. 2231217

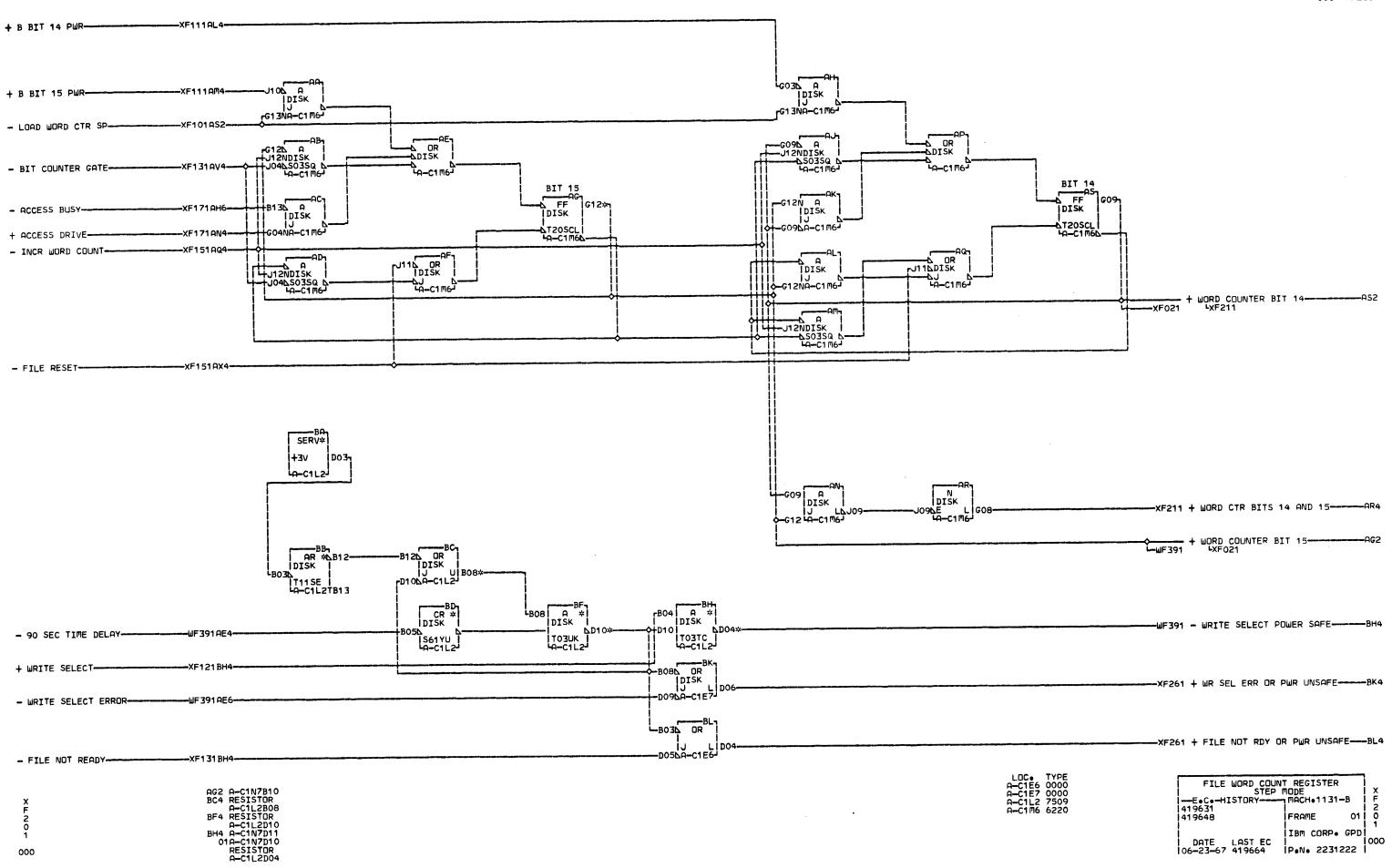
LOC. TYPE A-C1E6 0000 A-C1K6 6218

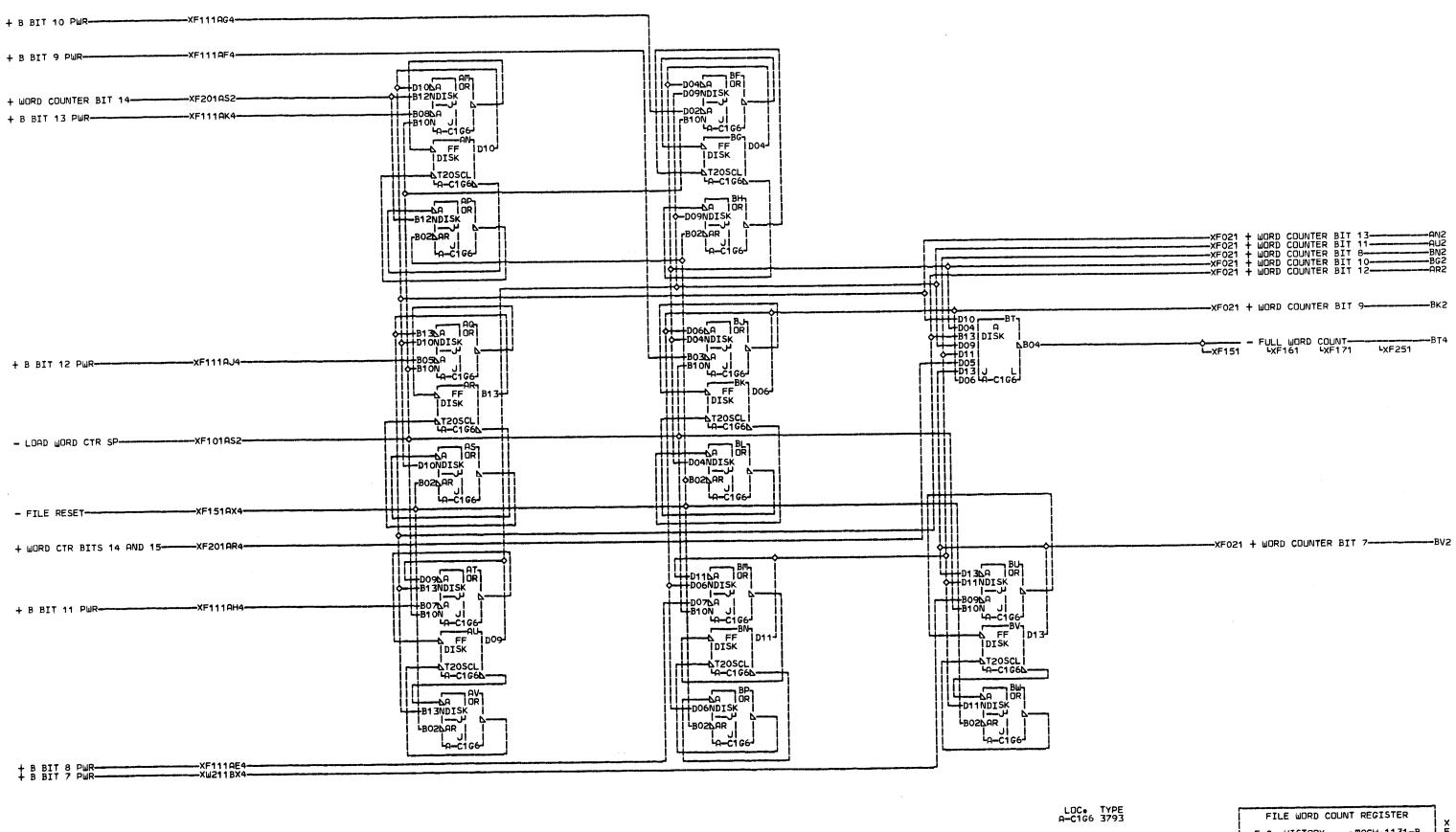












X F 2 1 1

- B BIT 12 PWR----

----RB311AE4-

000 XF221

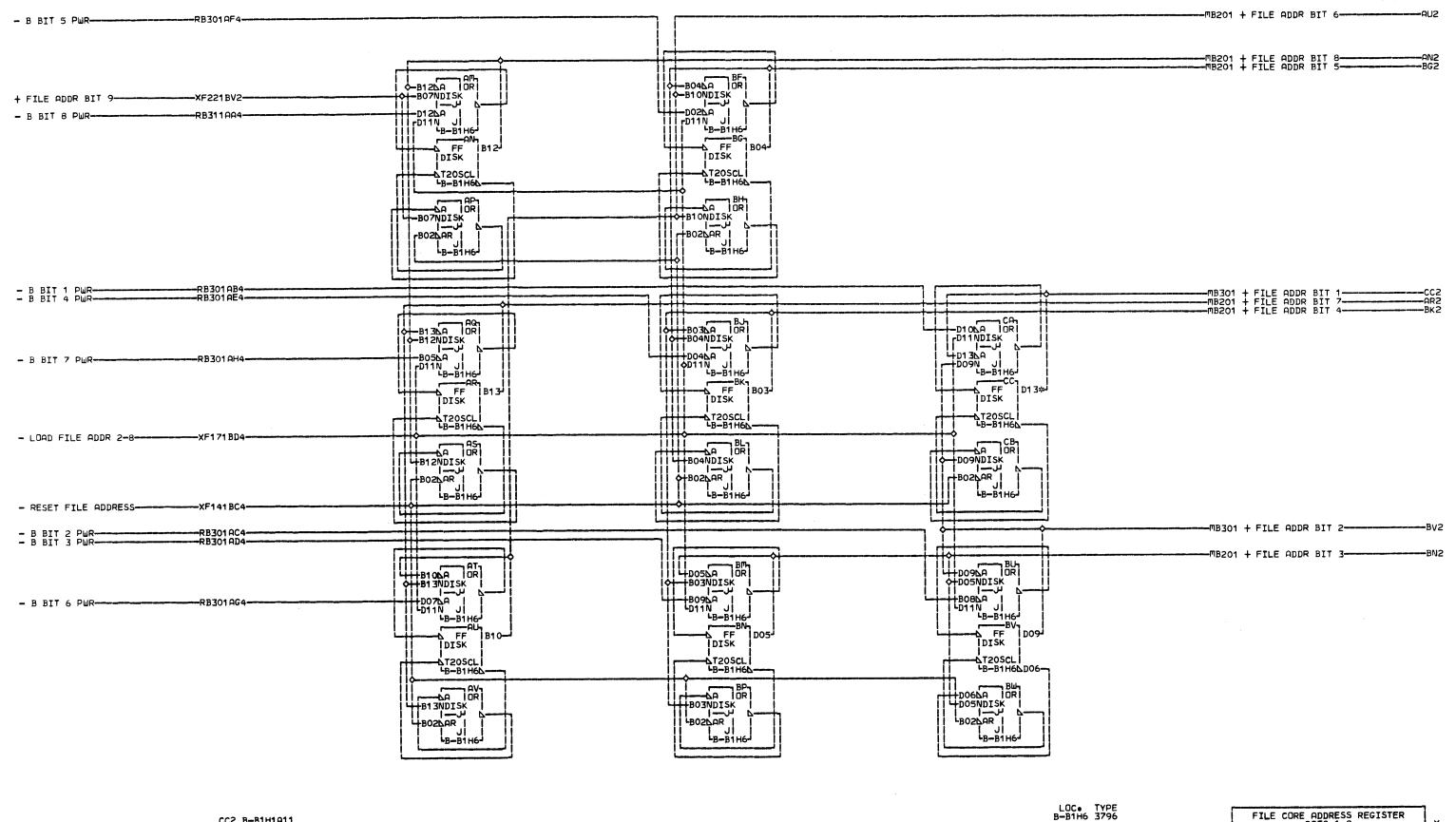
IBM CORP. GPD

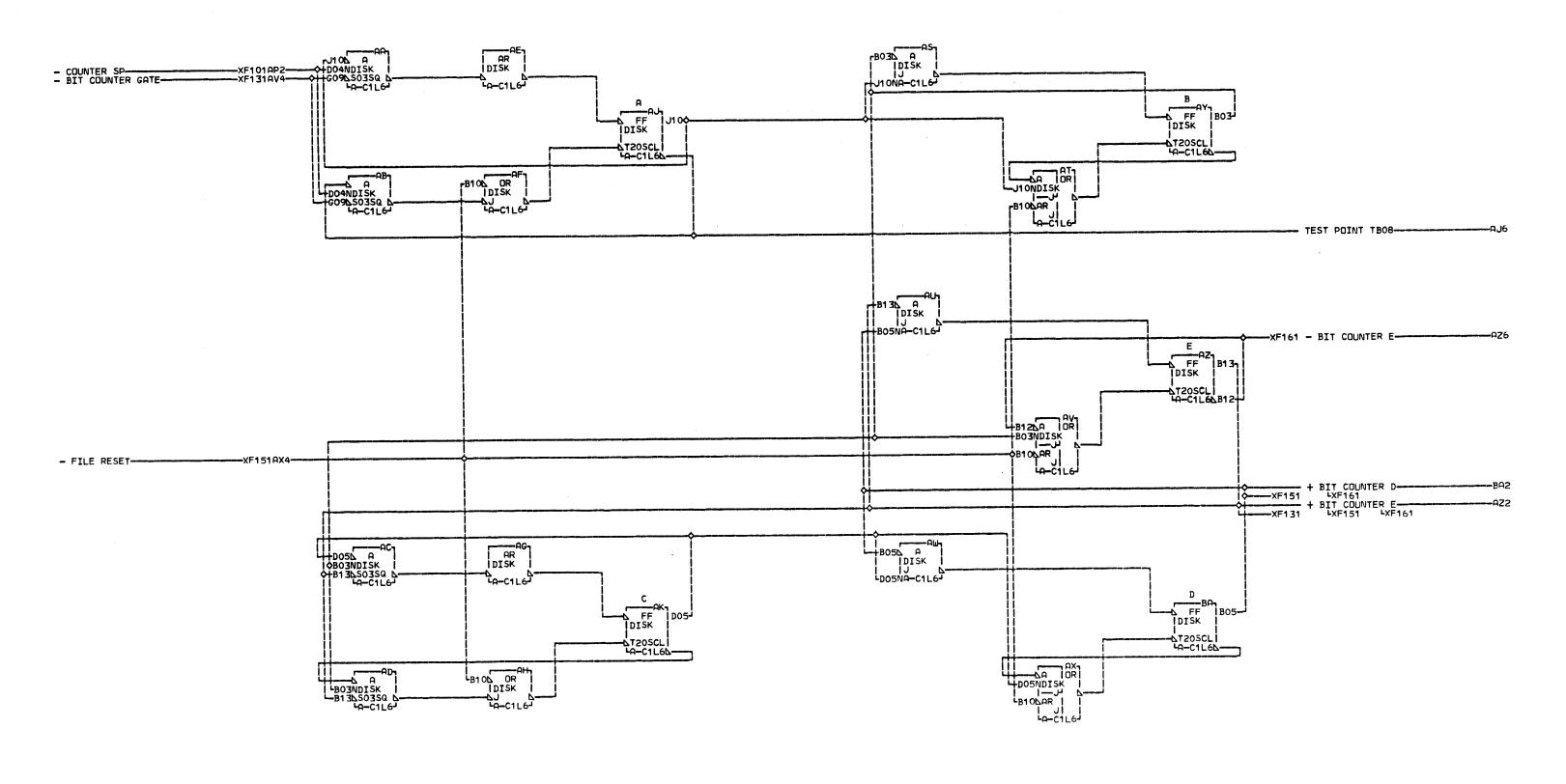
IP.N. 2231416

000

DATE LAST EC

-MB201 + FILE ADDR BIT 13-----



CC2 B-B1H1A11 01B-C1H1A11 

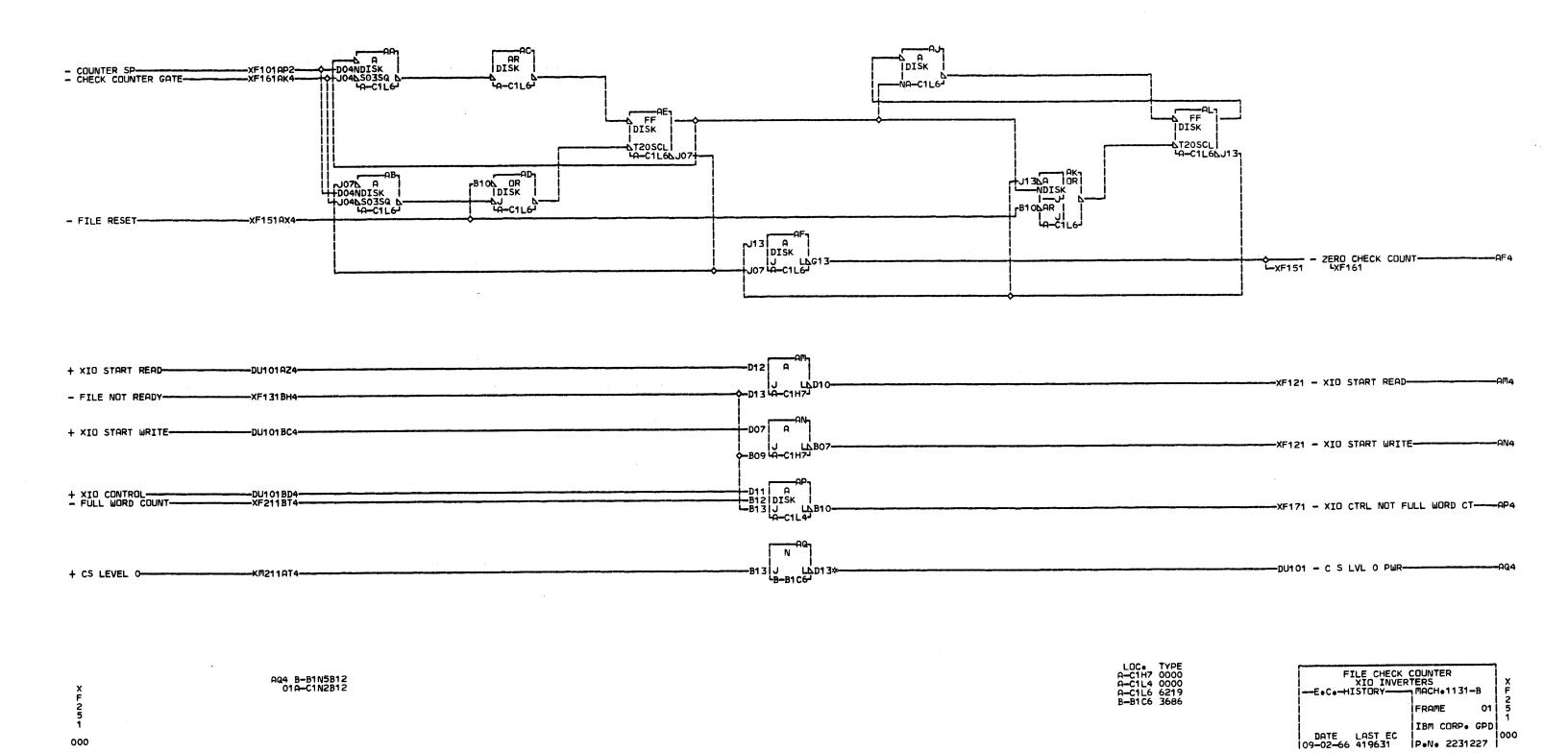
LOC. TYPE A-C1L6 6219 FILE BIT COUNTER --E.C.-HISTORY---

> IBM CORP. GPD PeNe 2231418 000 DATE LAST EC 109-02-66 419631

MACH • 1131-B

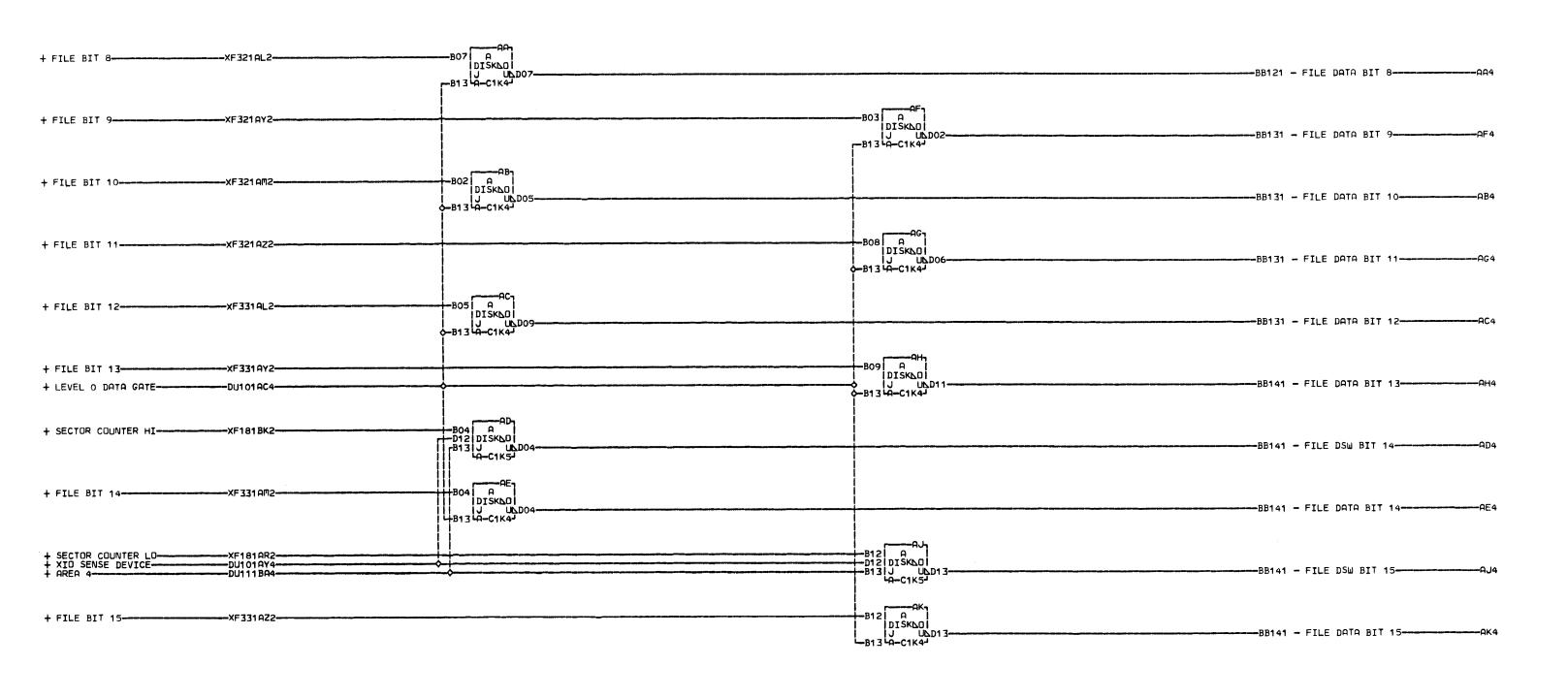
01 4

FRAME



DATE LAST EC 103-10-67 419648

IP-No 2231419



LOC. TYPE A-C1K4 3028 A-C1K5 3028

FILE INPUT BUSS ASSEMBLY
BITS 8-15

--E•C•-HISTORY | MACH•1131-B | F
FRAME | 01 | 7
1 | IBM CORP• GPD | 000

DATE LAST EC | 09-02-66 419631 | P•N• 2231420

X F 2 7 1

DATE LAST EC 109-02-66 419631

MACH-1131-B

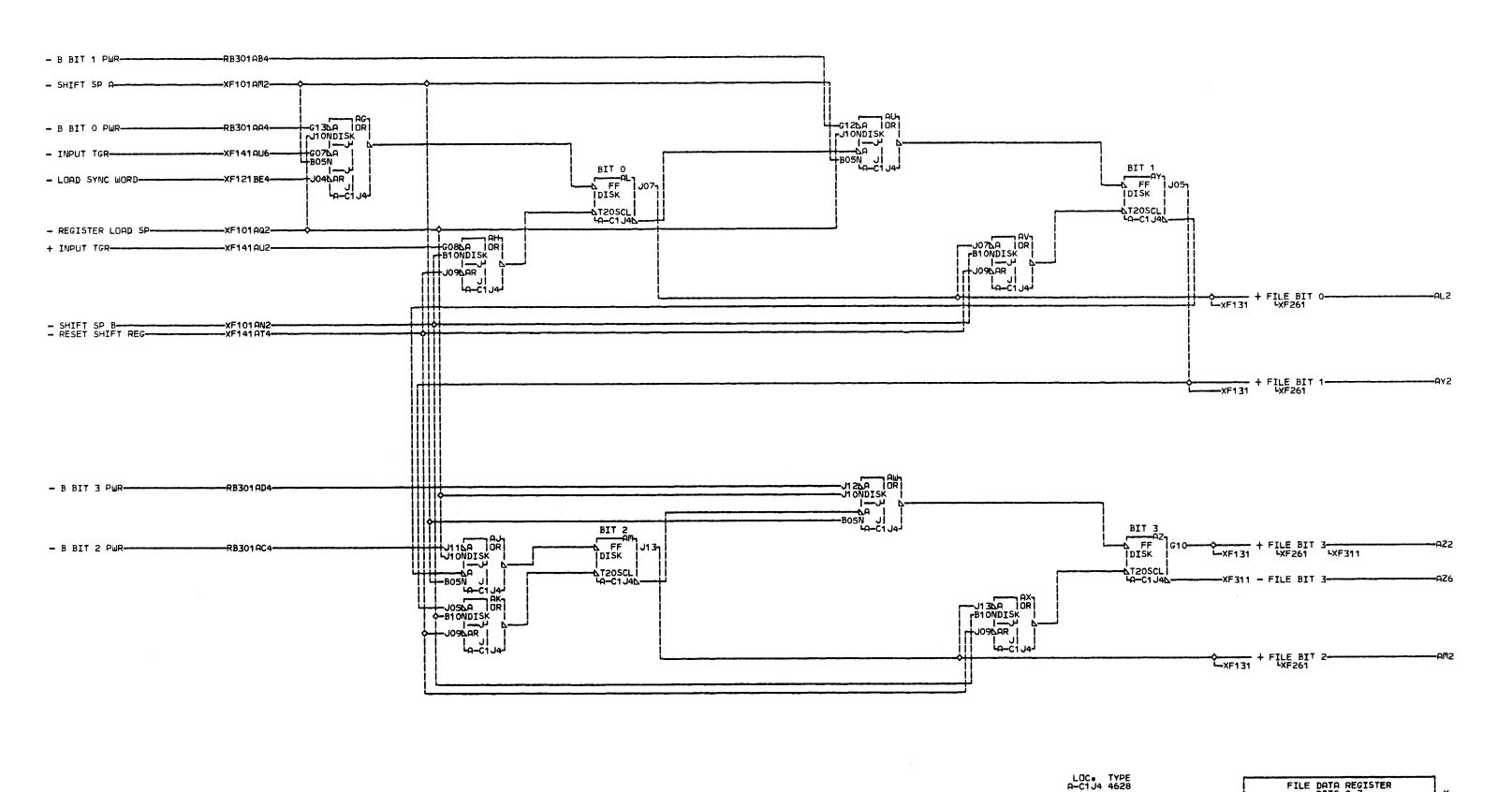
IBM CORP. GPD

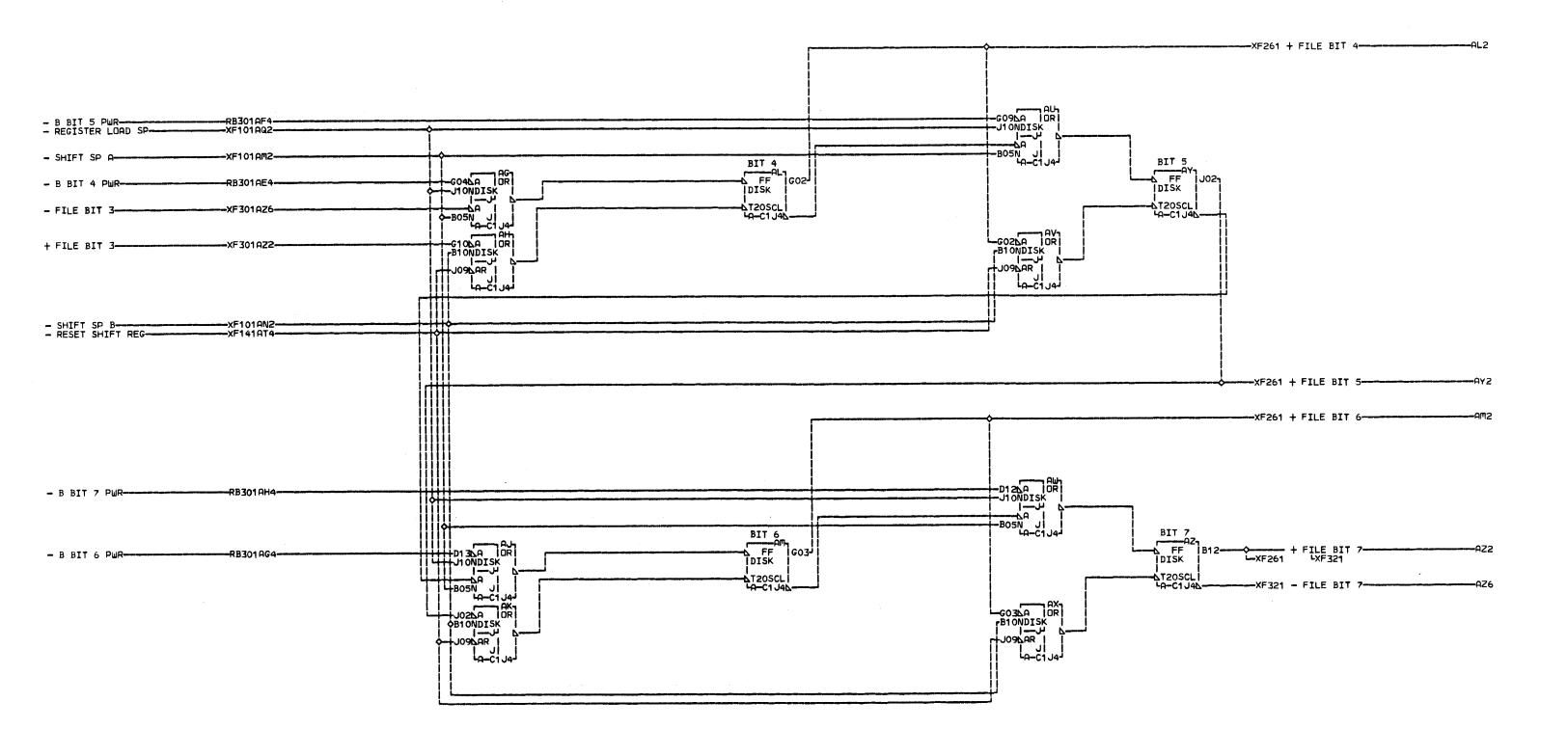
P.N. 2231421

FRAME

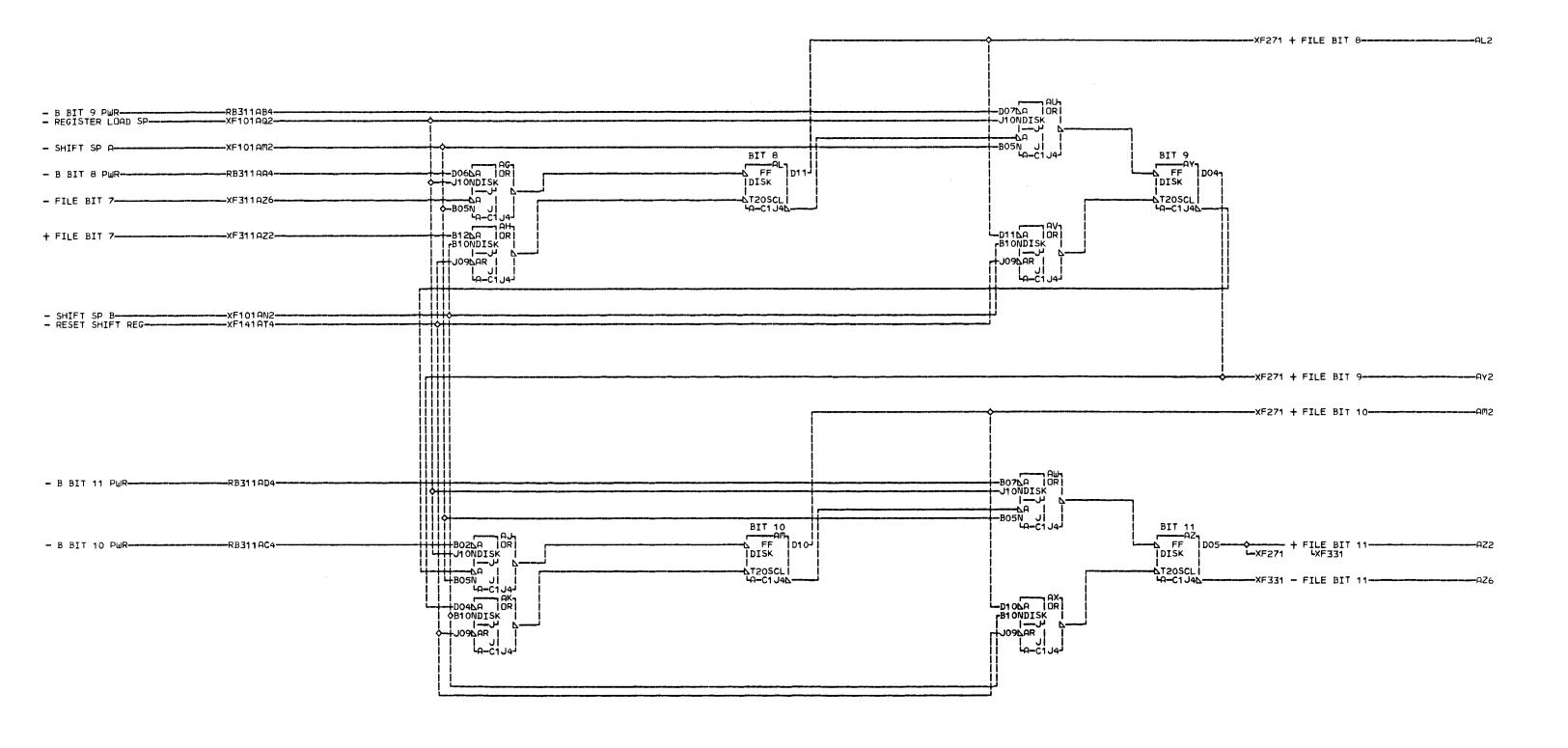
01 i õ

000



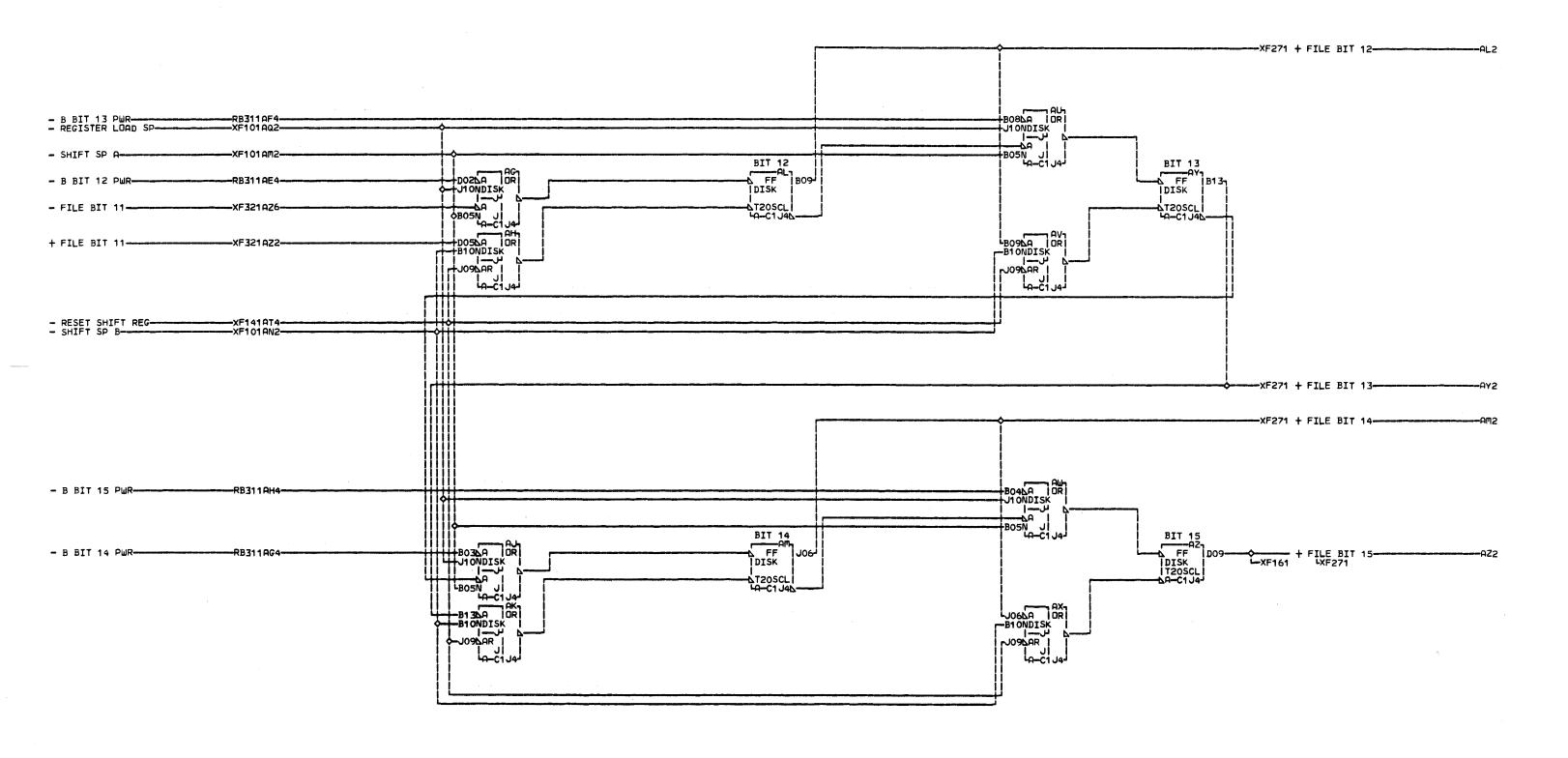


LOC. TYPE A-C1J4 4628



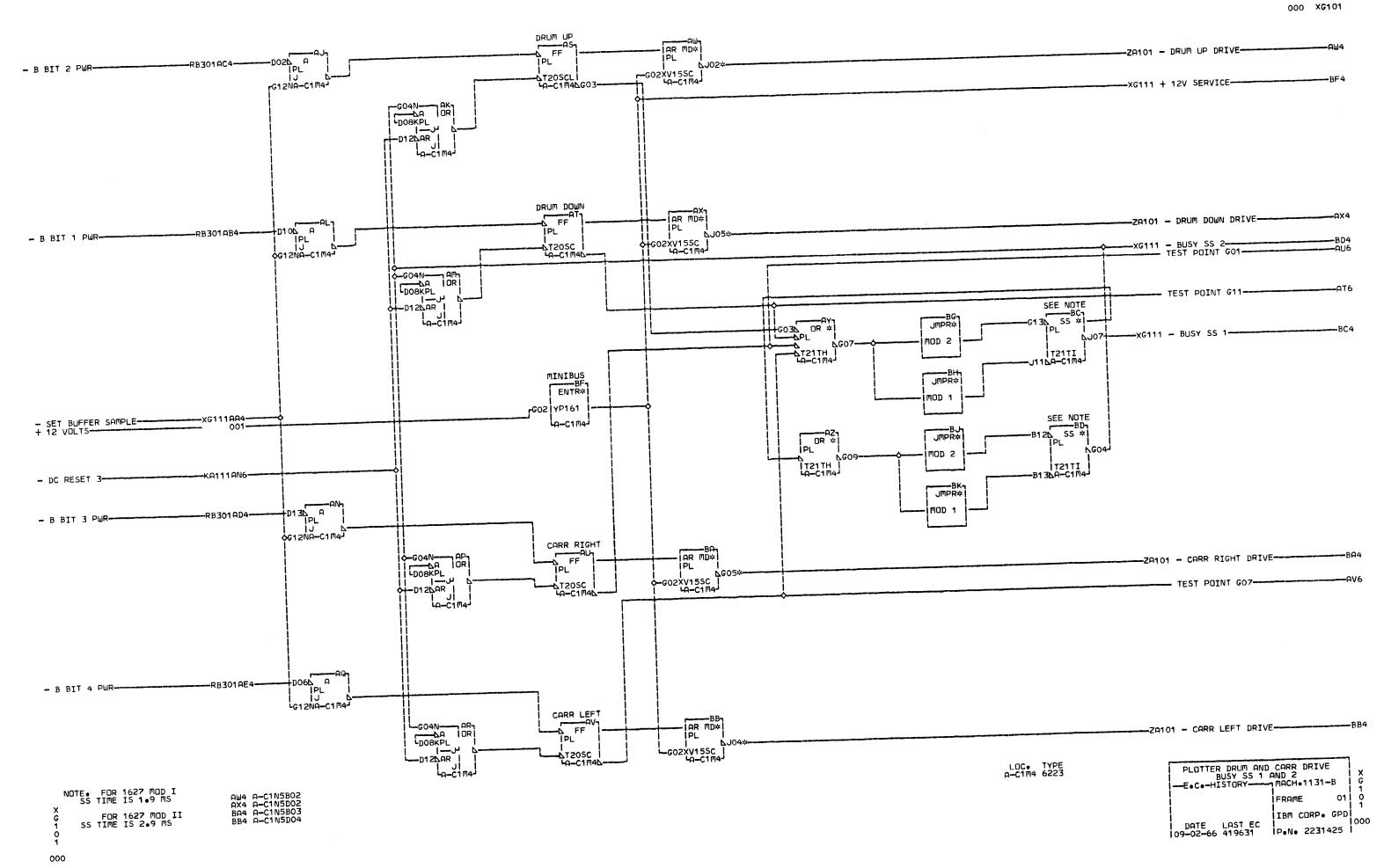
LOC. TYPE A-C1J4 4628

X F 3 2 1

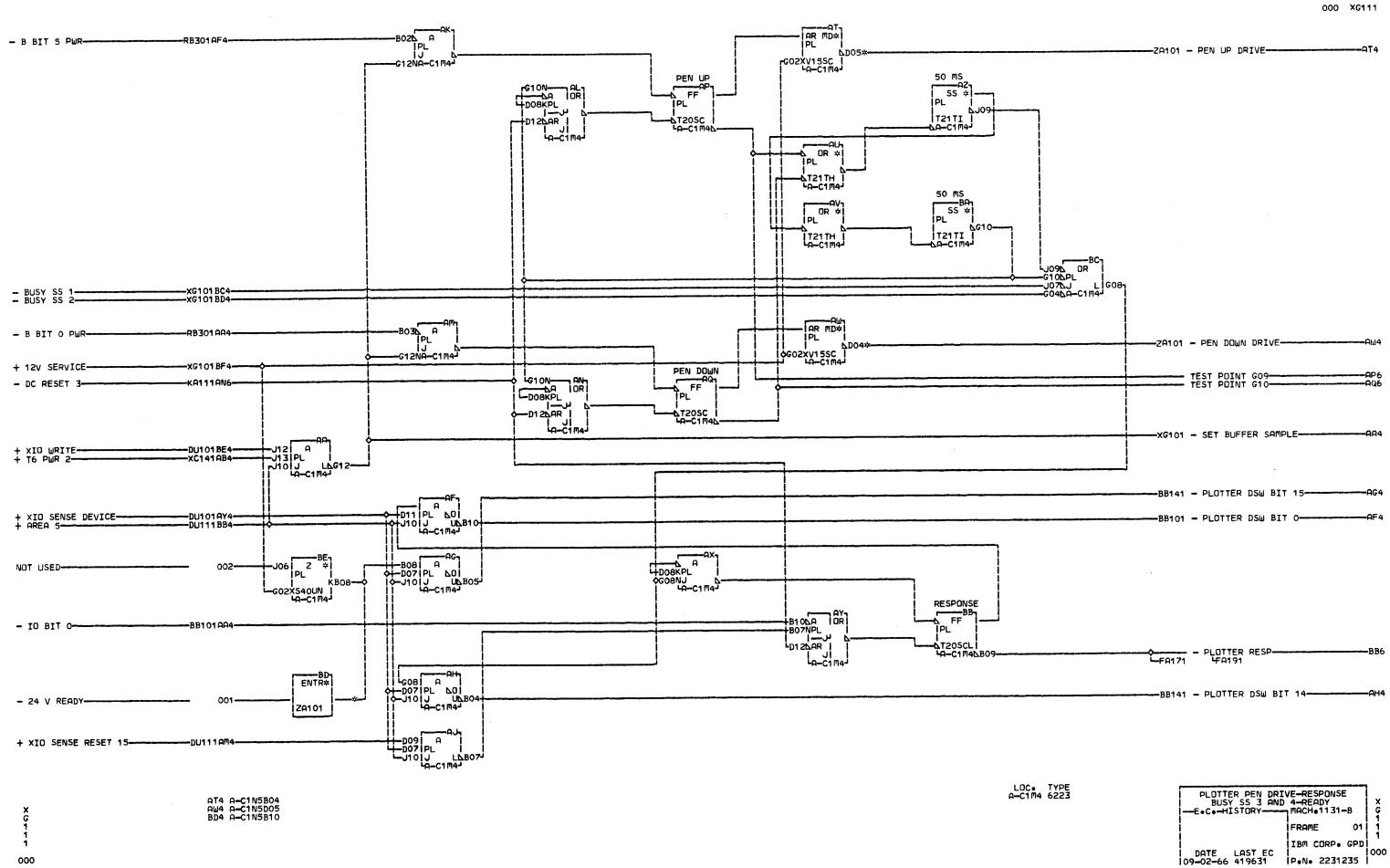


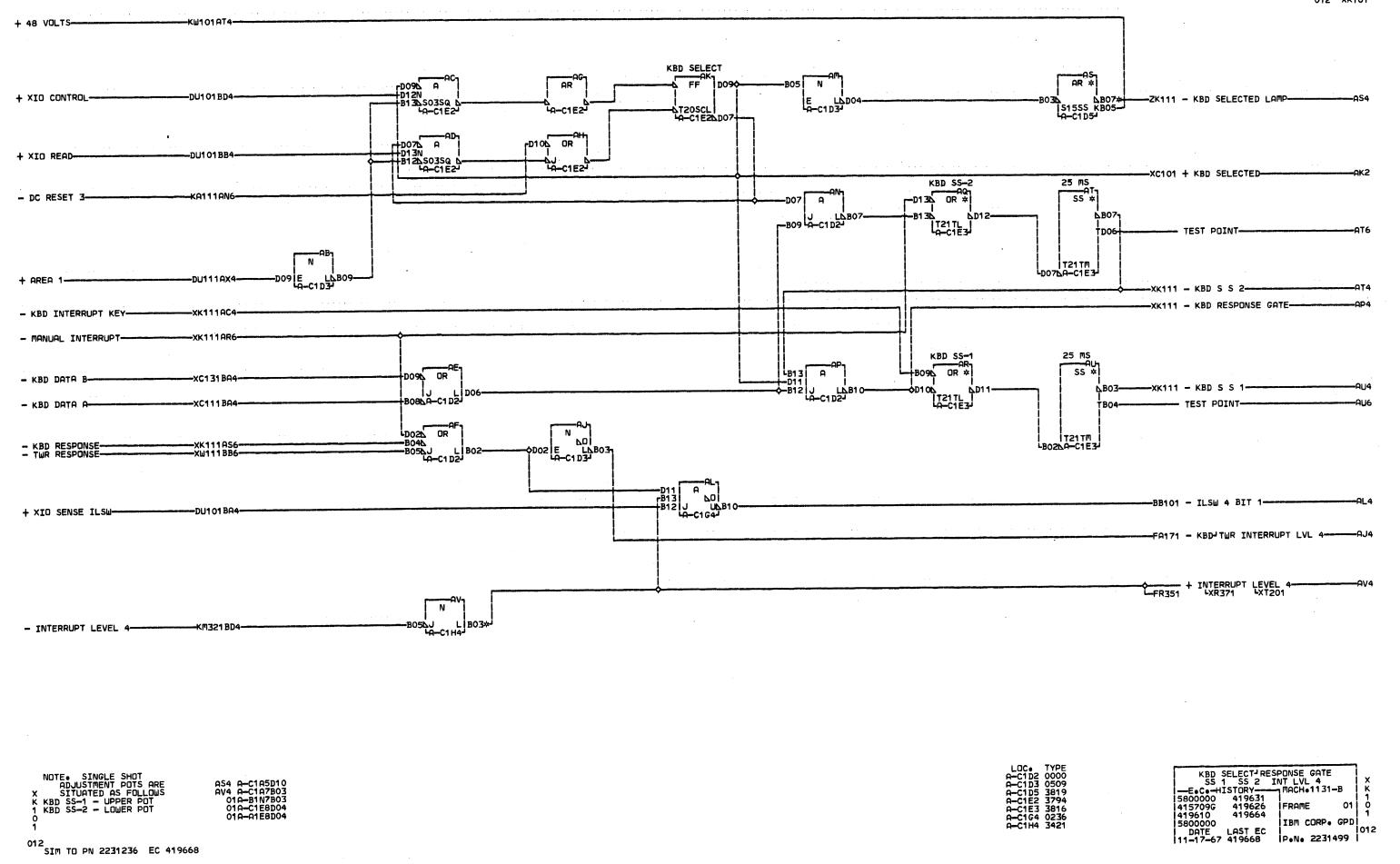
LOC. TYPE A-C1J4 4628

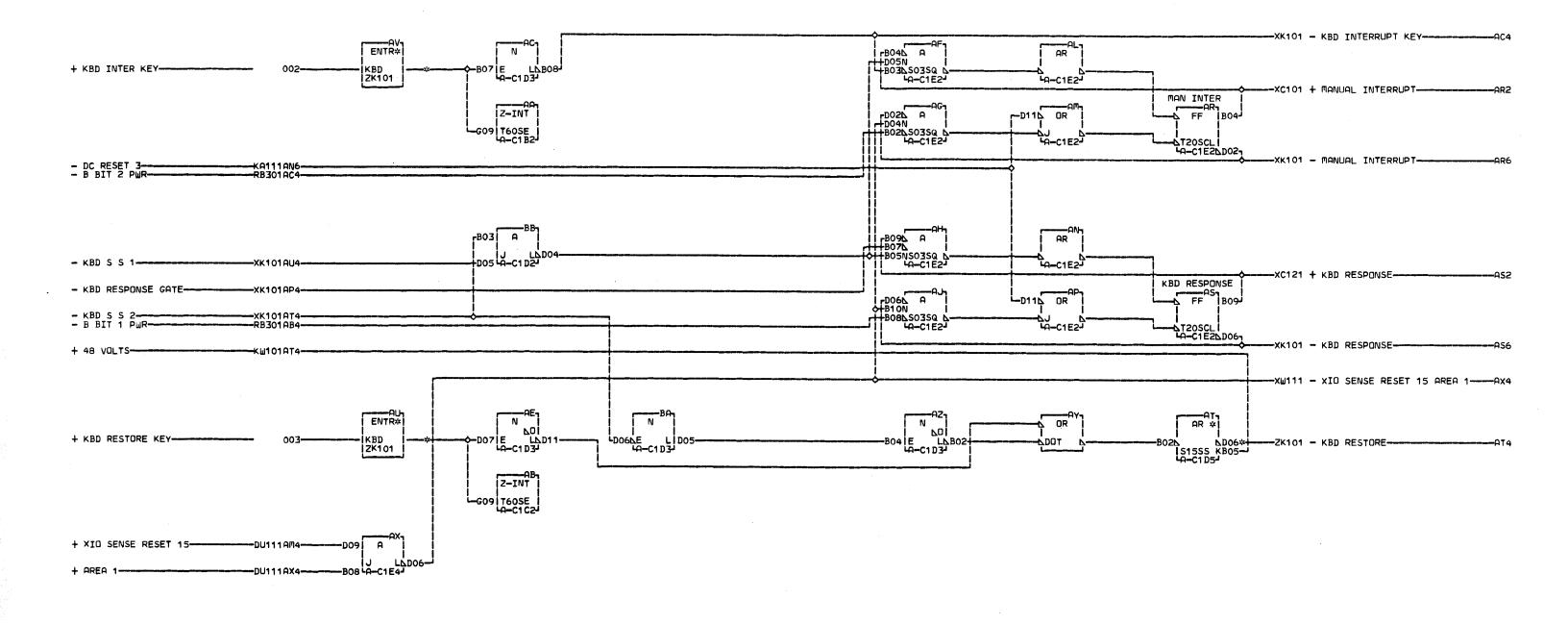
X F 3 1



IP.N. 2231235







LUC• TYPE A-C1B2 4615 A-C1C2 4615 A-C1D2 0000 A-C1D3 0509 A-C1D5 3819 A-C1E2 3794 A-C1E4 0000

AT4 A-C1A3D13 AU4 A-C1A3D12 AV4 A-C1A3B12